



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 16.4

ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

SPECint_base2006 = 15.7

CPU2006 license: 3

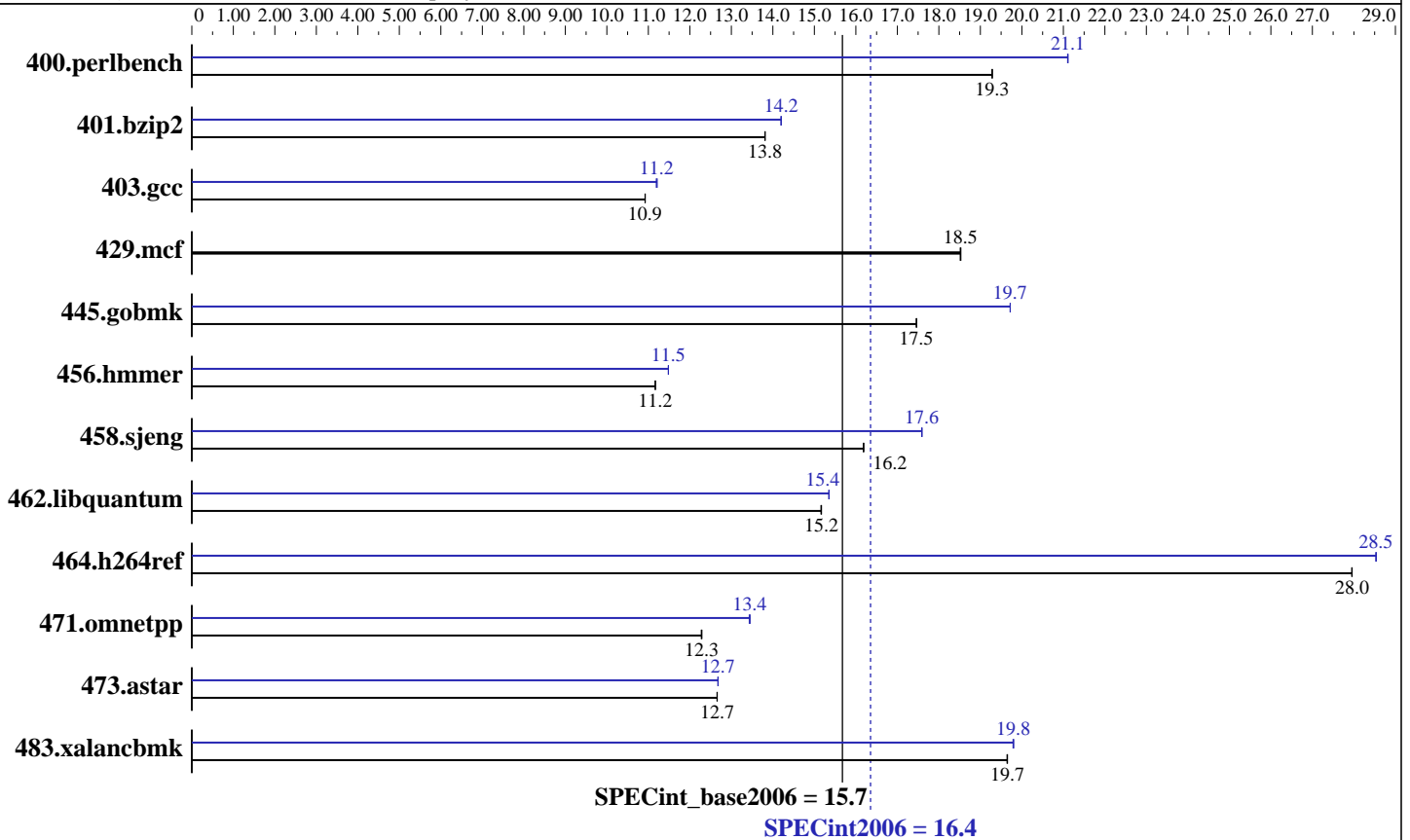
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300 CL5)
 Disk Subsystem: 2x72 GB 10k SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>507</u>	<u>19.3</u>	507	19.3	507	19.3	463	21.1	<u>463</u>	<u>21.1</u>	463	21.1
401.bzip2	699	13.8	699	13.8	<u>699</u>	<u>13.8</u>	<u>680</u>	<u>14.2</u>	680	14.2	680	14.2
403.gcc	<u>737</u>	<u>10.9</u>	737	10.9	737	10.9	<u>719</u>	<u>11.2</u>	718	11.2	719	11.2
429.mcf	<u>492</u>	<u>18.5</u>	492	18.5	493	18.5	<u>492</u>	<u>18.5</u>	492	18.5	493	18.5
445.gobmk	601	17.5	601	17.5	<u>601</u>	<u>17.5</u>	<u>532</u>	<u>19.7</u>	532	19.7	532	19.7
456.hammer	835	11.2	836	11.2	<u>835</u>	<u>11.2</u>	812	11.5	813	11.5	<u>813</u>	<u>11.5</u>
458.sjeng	747	16.2	748	16.2	<u>748</u>	<u>16.2</u>	688	17.6	688	17.6	<u>688</u>	<u>17.6</u>
462.libquantum	<u>1366</u>	<u>15.2</u>	1366	15.2	1367	15.2	<u>1350</u>	<u>15.4</u>	1350	15.4	1350	15.4
464.h264ref	792	27.9	792	28.0	<u>792</u>	<u>28.0</u>	<u>776</u>	<u>28.5</u>	775	28.5	776	28.5
471.omnetpp	509	12.3	<u>509</u>	<u>12.3</u>	509	12.3	465	13.4	465	13.4	<u>465</u>	<u>13.4</u>
473.astar	<u>555</u>	<u>12.7</u>	554	12.7	555	12.7	554	12.7	554	12.7	<u>554</u>	<u>12.7</u>
483.xalancbmk	351	19.7	351	19.6	<u>351</u>	<u>19.7</u>	<u>349</u>	<u>19.8</u>	349	19.8	348	19.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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