



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 82.0

ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 79.6

CPU2006 license: 3

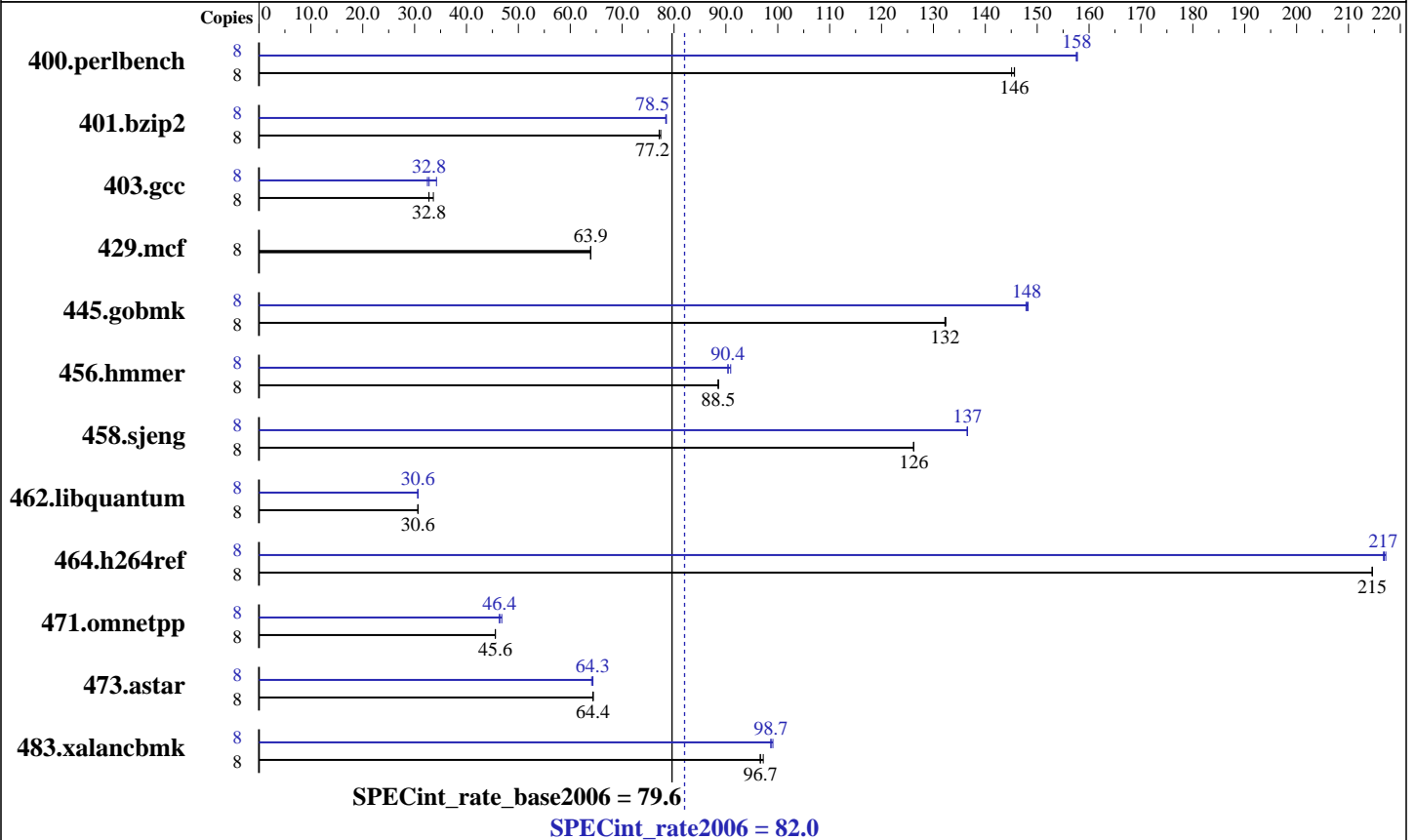
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	539	145	<u>537</u>	<u>146</u>	537	146	8	496	158	496	158	<u>496</u>	<u>158</u>
401.bzip2	8	<u>1000</u>	<u>77.2</u>	997	77.5	1001	77.1	8	985	78.4	<u>984</u>	<u>78.5</u>	983	78.5
403.gcc	8	1916	33.6	<u>1965</u>	<u>32.8</u>	1967	32.7	8	<u>1963</u>	<u>32.8</u>	1882	34.2	1983	32.5
429.mcf	8	1141	64.0	1141	63.9	<u>1141</u>	<u>63.9</u>	8	1141	64.0	1141	63.9	<u>1141</u>	<u>63.9</u>
445.gobmk	8	635	132	<u>635</u>	<u>132</u>	634	132	8	566	148	568	148	<u>567</u>	<u>148</u>
456.hammer	8	843	88.5	843	88.5	<u>843</u>	<u>88.5</u>	8	821	90.9	<u>825</u>	<u>90.4</u>	826	90.4
458.sjeng	8	767	126	<u>767</u>	<u>126</u>	767	126	8	<u>709</u>	<u>137</u>	709	136	709	137
462.libquantum	8	5411	30.6	5404	30.7	<u>5410</u>	<u>30.6</u>	8	<u>5412</u>	<u>30.6</u>	5412	30.6	5411	30.6
464.h264ref	8	<u>825</u>	<u>215</u>	825	215	825	215	8	817	217	<u>816</u>	<u>217</u>	815	217
471.omnetpp	8	<u>1097</u>	<u>45.6</u>	1098	45.5	1097	45.6	8	1068	46.8	1079	46.3	<u>1078</u>	<u>46.4</u>
473.astar	8	871	64.4	873	64.3	<u>872</u>	<u>64.4</u>	8	<u>873</u>	<u>64.3</u>	876	64.1	873	64.4
483.xalancbmk	8	568	97.2	<u>571</u>	<u>96.7</u>	571	96.6	8	557	99.1	559	98.7	<u>559</u>	<u>98.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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