



# SPEC® CFP2006 Result

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## Hewlett-Packard Company

SPECfp®2006 = 14.3

ProLiant ML350 G5  
(2.66 GHz, Intel Xeon processor X5355)

SPECfp\_base2006 = 14.1

CPU2006 license: 3

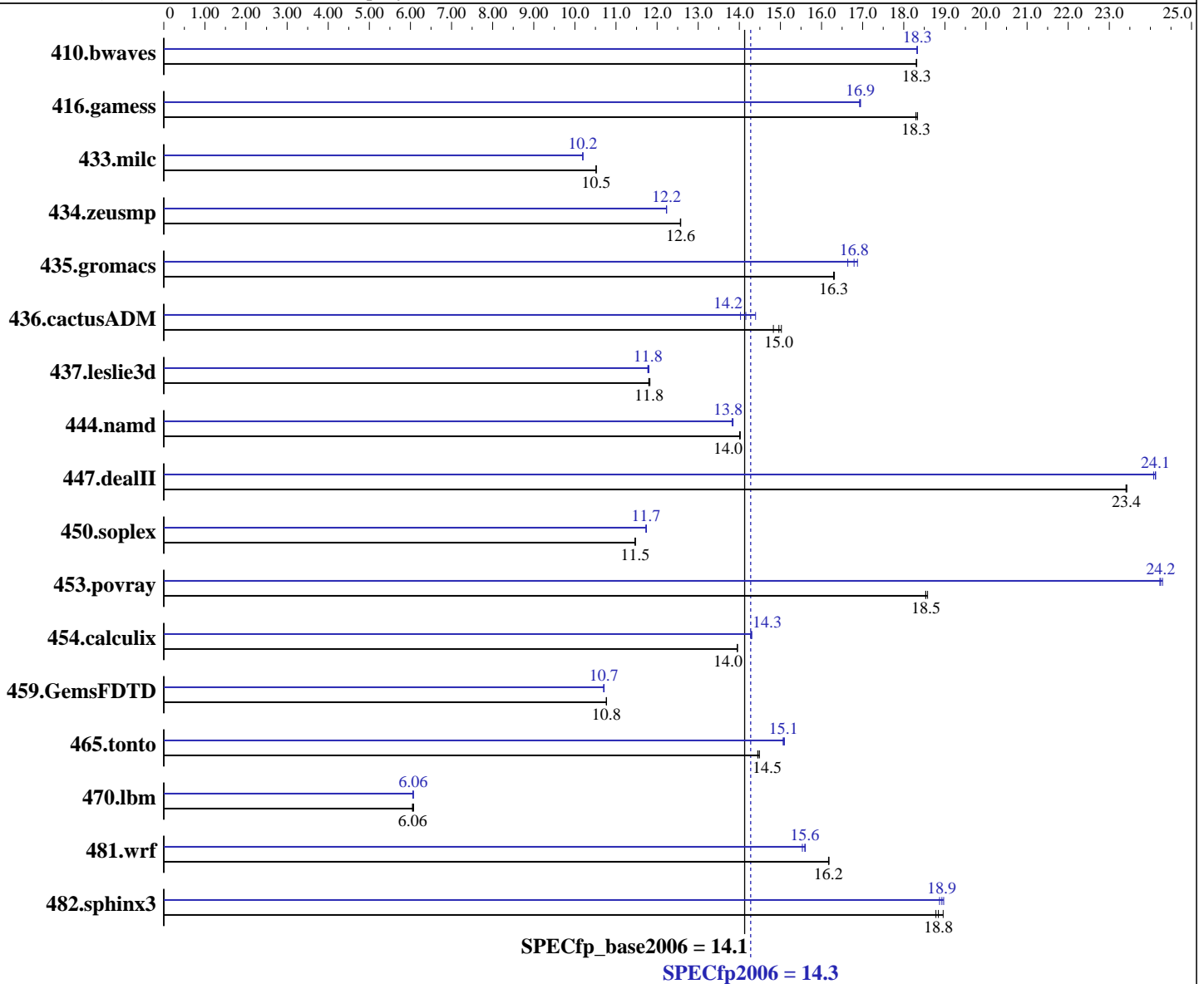
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 1 core, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-default  
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1  
 Package ID l\_cc\_c\_9.1.045 Build no 20061101  
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1  
 Package ID l\_fc\_c\_9.1.040 Build no 20061101  
 Auto Parallel: No

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (4x2 GB PC2-5300 CL5)  
Disk Subsystem: 4x36 GB 10 K SAS  
Other Hardware: None

File System: ext2  
System State: Multi-user run level 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base       |             |             |             |            |             | Peak       |             |             |             |             |             |
|---------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|
|               | Seconds    | Ratio       | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       |
| 410.bwaves    | <u>742</u> | <u>18.3</u> | 742         | 18.3        | 742        | 18.3        | 742        | 18.3        | <u>741</u>  | <u>18.3</u> | 741         | 18.3        |
| 416.gamess    | 1068       | 18.3        | <u>1070</u> | <u>18.3</u> | 1070       | 18.3        | 1157       | 16.9        | <u>1156</u> | <u>16.9</u> | 1155        | 16.9        |
| 433.milc      | <u>873</u> | <u>10.5</u> | 873         | 10.5        | 873        | 10.5        | <u>900</u> | <u>10.2</u> | 901         | 10.2        | 900         | 10.2        |
| 434.zeusmp    | <u>724</u> | <u>12.6</u> | 724         | 12.6        | 724        | 12.6        | 744        | 12.2        | <u>744</u>  | <u>12.2</u> | 744         | 12.2        |
| 435.gromacs   | 438        | 16.3        | <u>438</u>  | <u>16.3</u> | 438        | 16.3        | 423        | 16.9        | 429         | 16.6        | <u>425</u>  | <u>16.8</u> |
| 436.cactusADM | 795        | 15.0        | 806         | 14.8        | <u>799</u> | <u>15.0</u> | <u>844</u> | <u>14.2</u> | 830         | 14.4        | 852         | 14.0        |
| 437.leslie3d  | <u>796</u> | <u>11.8</u> | 795         | 11.8        | 797        | 11.8        | 796        | 11.8        | <u>798</u>  | <u>11.8</u> | 798         | 11.8        |
| 444.namd      | <u>572</u> | <u>14.0</u> | 572         | 14.0        | 572        | 14.0        | 580        | 13.8        | <u>580</u>  | <u>13.8</u> | 579         | 13.8        |
| 447.dealII    | 488        | 23.4        | 489         | 23.4        | <u>489</u> | <u>23.4</u> | 474        | 24.1        | 475         | 24.1        | <u>474</u>  | <u>24.1</u> |
| 450.soplex    | <u>727</u> | <u>11.5</u> | 727         | 11.5        | 728        | 11.5        | 710        | 11.7        | <u>711</u>  | <u>11.7</u> | 711         | 11.7        |
| 453.povray    | 287        | 18.5        | 286         | 18.6        | <u>287</u> | <u>18.5</u> | 220        | 24.2        | <u>219</u>  | <u>24.2</u> | 219         | 24.3        |
| 454.calculix  | 591        | 14.0        | <u>591</u>  | <u>14.0</u> | 591        | 13.9        | 578        | 14.3        | <u>577</u>  | <u>14.3</u> | 577         | 14.3        |
| 459.GemsFDTD  | 985        | 10.8        | 986         | 10.8        | <u>985</u> | <u>10.8</u> | 992        | 10.7        | 991         | 10.7        | <u>991</u>  | <u>10.7</u> |
| 465.tonto     | 679        | 14.5        | 681         | 14.4        | <u>681</u> | <u>14.5</u> | 652        | 15.1        | <u>653</u>  | <u>15.1</u> | 653         | 15.1        |
| 470.lbm       | 2261       | 6.08        | <u>2268</u> | <u>6.06</u> | 2272       | 6.05        | 2262       | 6.07        | <u>2266</u> | <u>6.06</u> | 2268        | 6.06        |
| 481.wrf       | 690        | 16.2        | <u>691</u>  | <u>16.2</u> | 691        | 16.2        | 716        | 15.6        | <u>716</u>  | <u>15.6</u> | 719         | 15.5        |
| 482.sphinx3   | 1028       | 19.0        | <u>1034</u> | <u>18.8</u> | 1038       | 18.8        | 1027       | 19.0        | 1033        | 18.9        | <u>1030</u> | <u>18.9</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.  
"ulimit -s unlimited" set  
Single processor kernel used

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

## Peak Compiler Invocation

C benchmarks:

icc

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

C++ benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

Fortran benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

Benchmarks using both Fortran and C:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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