



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint[®]_rate2006 = 45.7

ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.2

CPU2006 license: 3

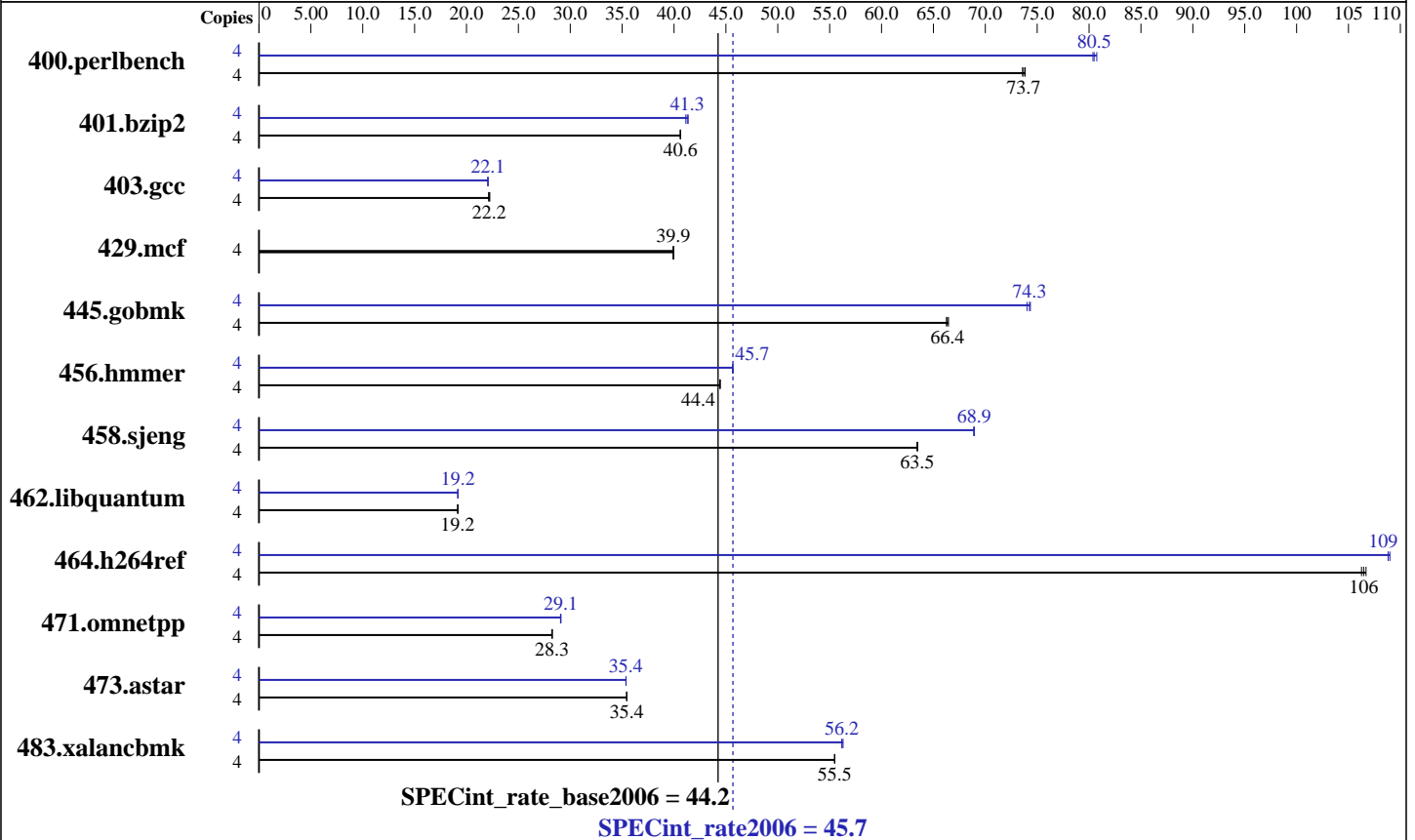
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 2x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 45.7

ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.2

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	531	73.6	529	73.8	<u>530</u>	<u>73.7</u>	4	486	80.4	<u>486</u>	<u>80.5</u>	484	80.7
401.bzip2	4	951	40.6	950	40.6	<u>950</u>	<u>40.6</u>	4	938	41.1	933	41.4	<u>935</u>	<u>41.3</u>
403.gcc	4	1455	22.1	1447	22.2	<u>1451</u>	<u>22.2</u>	4	<u>1459</u>	<u>22.1</u>	1459	22.1	1460	22.1
429.mcf	4	912	40.0	914	39.9	<u>914</u>	<u>39.9</u>	4	912	40.0	914	39.9	<u>914</u>	<u>39.9</u>
445.gobmk	4	631	66.4	633	66.2	<u>632</u>	<u>66.4</u>	4	<u>565</u>	<u>74.3</u>	564	74.3	567	74.0
456.hammer	4	840	44.4	840	44.4	<u>840</u>	<u>44.4</u>	4	817	45.7	818	45.6	<u>817</u>	<u>45.7</u>
458.sjeng	4	763	63.5	763	63.4	<u>763</u>	<u>63.5</u>	4	703	68.9	702	69.0	<u>702</u>	<u>68.9</u>
462.libquantum	4	4324	19.2	4327	19.2	<u>4325</u>	<u>19.2</u>	4	4325	19.2	<u>4324</u>	<u>19.2</u>	4321	19.2
464.h264ref	4	<u>831</u>	<u>106</u>	833	106	830	107	4	812	109	813	109	<u>813</u>	<u>109</u>
471.omnetpp	4	885	28.3	885	28.3	<u>885</u>	<u>28.3</u>	4	<u>860</u>	<u>29.1</u>	859	29.1	860	29.1
473.astar	4	<u>792</u>	<u>35.4</u>	793	35.4	792	35.5	4	794	35.4	794	35.4	<u>794</u>	<u>35.4</u>
483.xalancbmk	4	498	55.5	497	55.5	<u>498</u>	<u>55.5</u>	4	<u>491</u>	<u>56.2</u>	491	56.2	490	56.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 45.7

ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.2

CPU2006 license: 3

Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 45.7

ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.2

CPU2006 license: 3

Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:24:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 February 2007.