SPEC® CFP2006 Result
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IBM Corporation

$SPECfp^{®}2006 =$ Not Run

IBM System x3200 (Intel Xeon X3210)

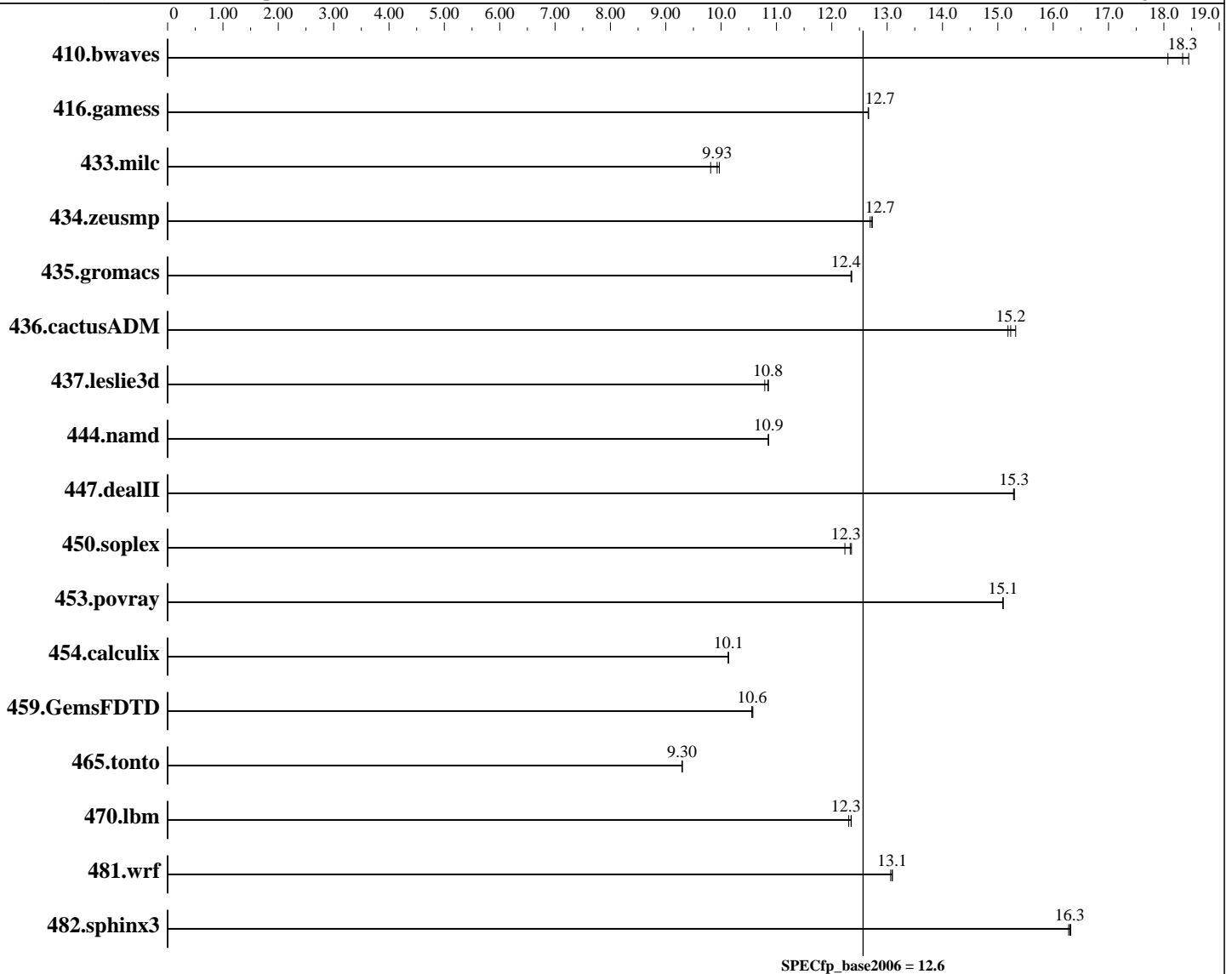$SPECfp\_base2006 =$ 12.6

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006

Benchmark	Result
410.bwaves	18.3
416.gamess	12.7
433.milc	9.93
434.zeusmp	12.7
435.gromacs	12.4
436.cactusADM	15.2
437.leslie3d	10.8
444.namd	10.9
447.dealII	15.3
450.soplex	12.3
453.povray	15.1
454.calculix	10.1
459.GemsFDTD	10.6
465.tonto	9.30
470.lbm	12.3
481.wrf	13.1
482.sphinx3	16.3

SPECfp_base2006 = 12.6

Hardware

CPU Name: Intel Xeon X3210
CPU Characteristics: 1066MHz system bus
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816
                   Intel Fortran Compiler for IA32 version 9.1 Build no 20060816
                   Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB 667MHz ECC)
Disk Subsystem:	1 x 80 GB SATA, 7200 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	752	18.1	737	18.4	**741**	**18.3**						
416.gamess	1546	12.7	**1546**	**12.7**	1547	12.7						
433.milc	936	9.81	921	9.97	**925**	**9.93**						
434.zeusmp	717	12.7	**715**	**12.7**	715	12.7						
435.gromacs	578	12.3	**578**	**12.4**	578	12.4						
436.cactusADM	787	15.2	780	15.3	**784**	**15.2**						
437.leslie3d	871	10.8	866	10.9	**867**	**10.8**						
444.namd	739	10.9	**739**	**10.9**	739	10.9						
447.dealII	749	15.3	**748**	**15.3**	748	15.3						
450.soplex	682	12.2	**676**	**12.3**	675	12.4						
453.povray	**352**	**15.1**	352	15.1	353	15.1						
454.calculix	815	10.1	814	10.1	**814**	**10.1**						
459.GemsFDTD	**1005**	**10.6**	1005	10.6	1003	10.6						
465.tonto	**1059**	**9.30**	1059	9.29	1058	9.30						
470.lbm	1112	12.4	1117	12.3	**1113**	**12.3**						
481.wrf	855	13.1	**853**	**13.1**	853	13.1						
482.sphinx3	1197	16.3	1194	16.3	**1196**	**16.3**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

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Base Portability Flags

```
    436.cactusADM: -Qlowercase /assume:underscore
       444.namd: -TP
       447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
                   -DBOOST_NO_INTRINSIC_WCHAR_T
      453.povray: -DSPEC_CPU_WINDOWS_ICL
     454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
         481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
    -fast /F950000000 shlw32m.lib              -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
    -fast -Qcxx_features /F950000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
    -fast /F950000000              -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
    -fast /F950000000              -link /FORCE:MULTIPLE
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

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For other inquiries, please contact webmaster@spec.org.

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