



SPEC® CINT2006 Result

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IBM Corporation
IBM System x3800

SPECint®2006 = Not Run
SPECint_base2006 = 10.4

CPU2006 license: 11

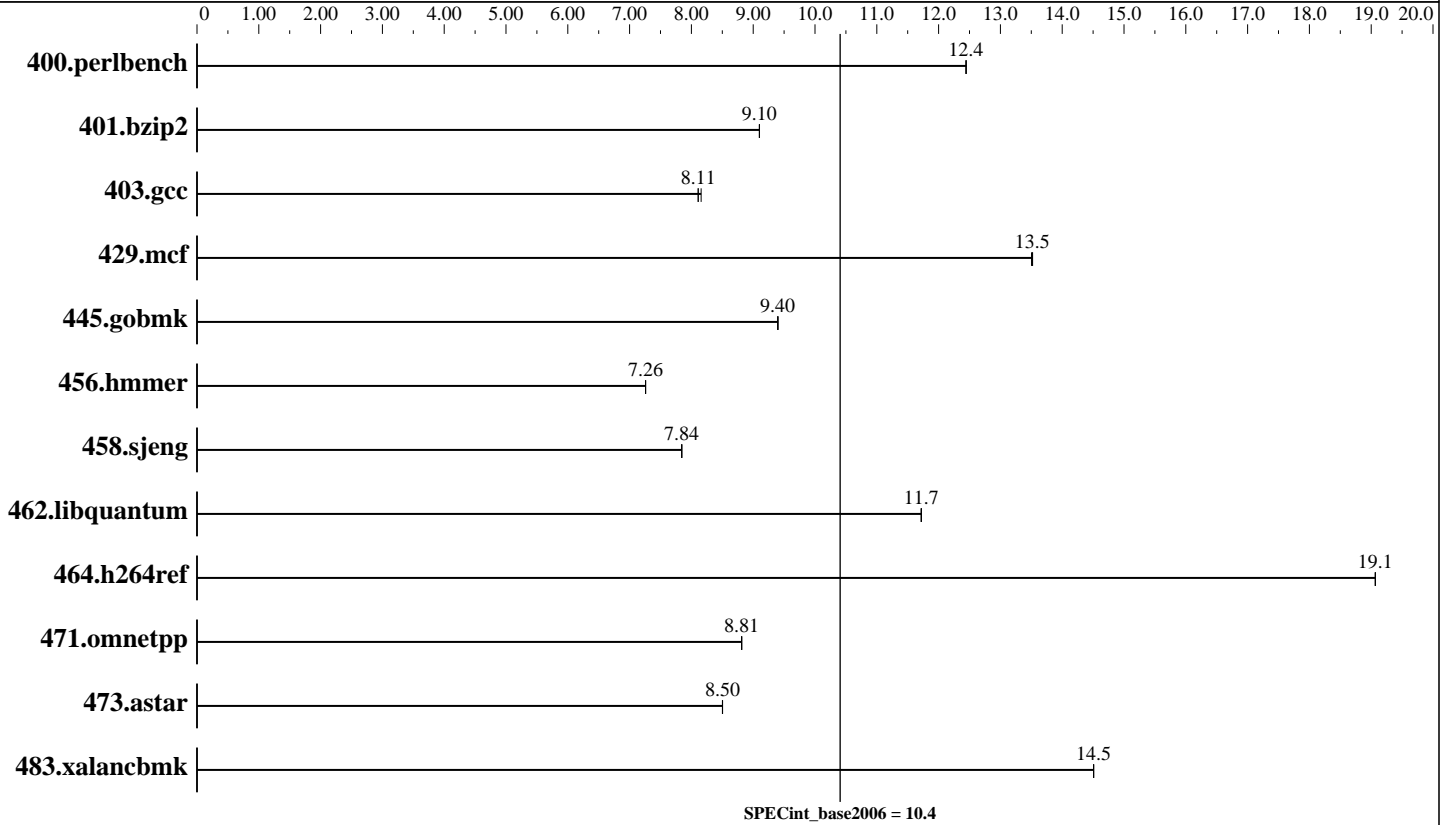
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Dec-2006

Hardware Availability: Oct-2006

Software Availability: Aug-2006



Hardware

CPU Name: Intel Xeon 7140N
 CPU Characteristics: 667 MHz bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)
 Disk Subsystem: 73 GB SAS, 10k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	785	12.4	<u>785</u>	<u>12.4</u>	785	12.4						
401.bzip2	1060	9.10	<u>1060</u>	<u>9.10</u>	1060	9.10						
403.gcc	993	8.11	<u>993</u>	<u>8.11</u>	987	8.16						
429.mcf	<u>675</u>	<u>13.5</u>	675	13.5	675	13.5						
445.gobmk	1116	9.40	1116	9.40	<u>1116</u>	<u>9.40</u>						
456.hammer	1285	7.26	<u>1285</u>	<u>7.26</u>	1285	7.26						
458.sjeng	1543	7.84	<u>1543</u>	<u>7.84</u>	1543	7.84						
462.libquantum	<u>1768</u>	<u>11.7</u>	1768	11.7	1768	11.7						
464.h264ref	<u>1161</u>	<u>19.1</u>	1161	19.1	1161	19.1						
471.omnetpp	709	8.81	<u>709</u>	<u>8.81</u>	709	8.81						
473.astar	826	8.50	825	8.50	<u>826</u>	<u>8.50</u>						
483.xalancbmk	476	14.5	<u>476</u>	<u>14.5</u>	476	14.5						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios Settings
Hardware Prefetch enabled
Memory Array set to High-Performance Memory Array

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:13:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 January 2007.