



# SPEC® CINT2006 Result

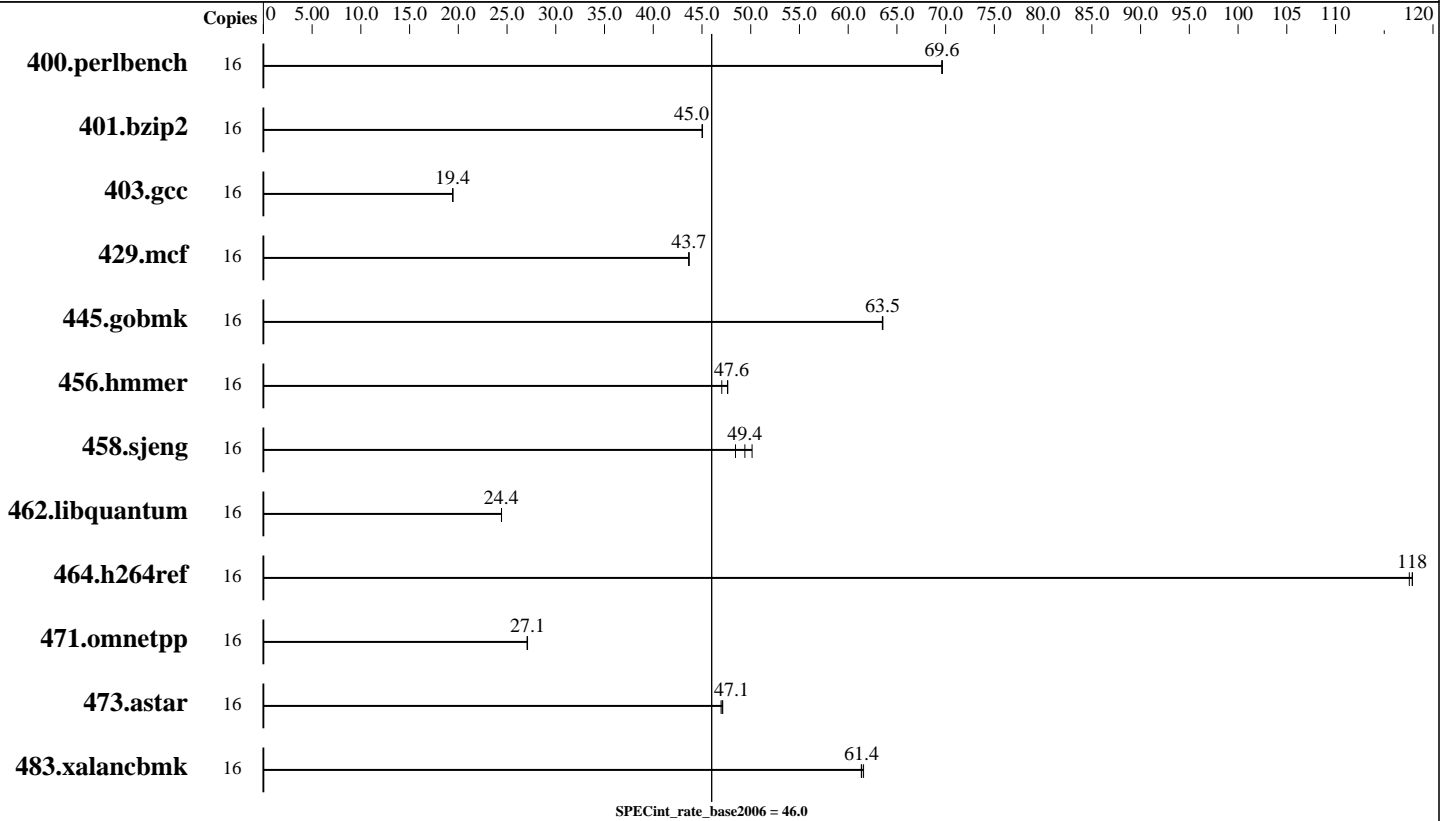
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IBM Corporation  
IBM System x3800

SPECint®\_rate2006 = Not Run  
SPECint\_rate\_base2006 = 46.0

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Jan-2007  
Hardware Availability: Oct-2006  
Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 7110N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	2244	69.7	<b><u>2245</u></b>	<b><u>69.6</u></b>	2246	69.6							
401.bzip2	16	3427	45.1	3430	45.0	<b><u>3428</u></b>	<b><u>45.0</u></b>							
403.gcc	16	<b><u>6629</u></b>	<b><u>19.4</u></b>	6630	19.4	6627	19.4							
429.mcf	16	3342	43.7	3343	43.7	<b><u>3342</u></b>	<b><u>43.7</u></b>							
445.gobmk	16	<b><u>2642</u></b>	<b><u>63.5</u></b>	2643	63.5	2641	63.5							
456.hammer	16	<b><u>3135</u></b>	<b><u>47.6</u></b>	3133	47.6	3174	47.0							
458.sjeng	16	<b><u>3919</u></b>	<b><u>49.4</u></b>	3997	48.4	3861	50.1							
462.libquantum	16	13567	24.4	13571	24.4	<b><u>13571</u></b>	<b><u>24.4</u></b>							
464.h264ref	16	3012	118	3004	118	<b><u>3004</u></b>	<b><u>118</u></b>							
471.omnetpp	16	3690	27.1	<b><u>3690</u></b>	<b><u>27.1</u></b>	3697	27.1							
473.astar	16	2383	47.1	<b><u>2386</u></b>	<b><u>47.1</u></b>	2392	47.0							
483.xalancbmk	16	1793	61.6	<b><u>1798</u></b>	<b><u>61.4</u></b>	1800	61.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings  
Hardware Prefetch enabled  
Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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