



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## IEIT Systems Co., Ltd.

SPECrate®2017\_int\_base = 473

### NF5180M7 (Intel Xeon Platinum 8571N)

SPECrate®2017\_int\_peak = 490

CPU2017 License: 3358

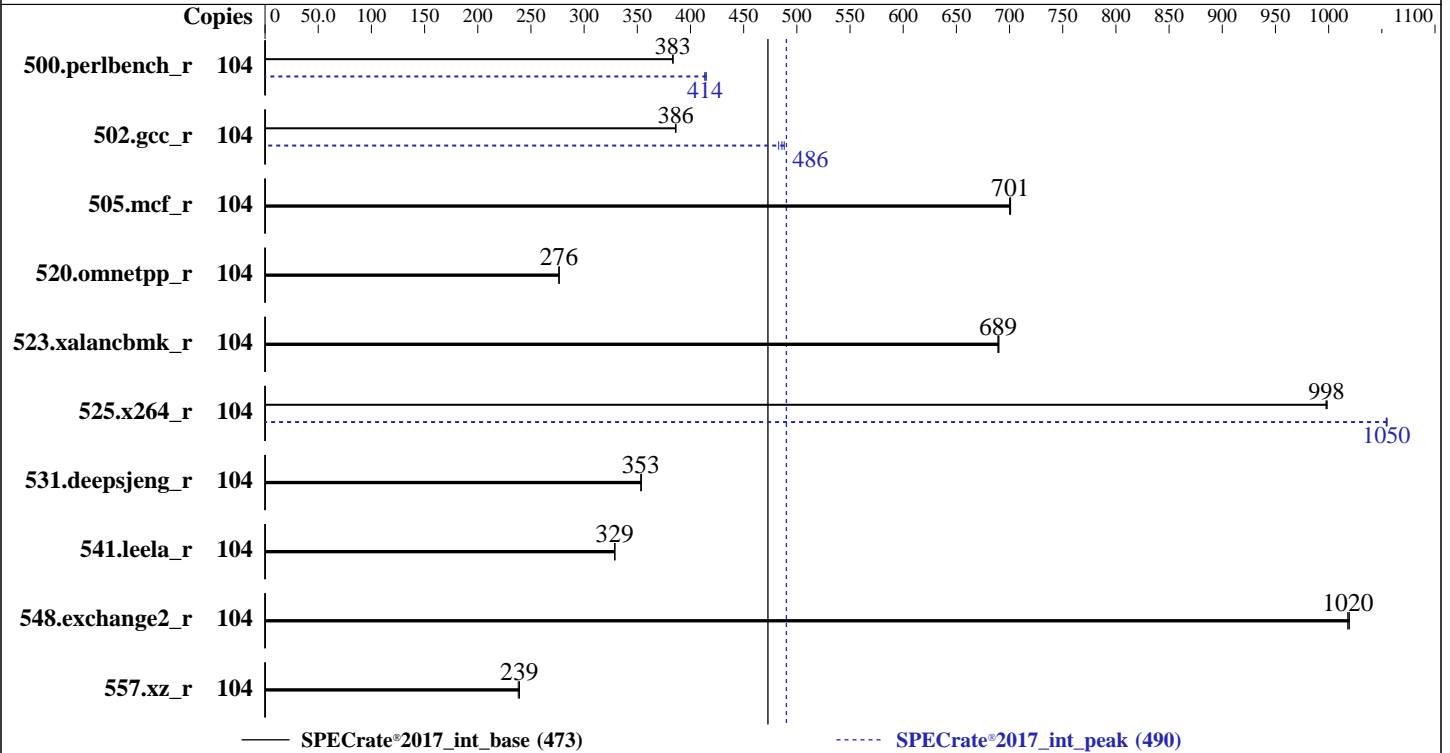
Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Jan-2024

Hardware Availability: Dec-2023

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Platinum 8571N  
 Max MHz: 4000  
 Nominal: 2400  
 Enabled: 52 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 300 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (8 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
 Storage: 1 x 1.92 TB NVME SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 05.13.00 released Nov-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	104	432	384	<b><u>432</u></b>	<b><u>383</u></b>	432	383	104	400	413	<b><u>400</u></b>	<b><u>414</u></b>	399	415
502.gcc_r	104	382	386	<b><u>381</u></b>	<b><u>386</u></b>	381	386	104	302	488	<b><u>303</u></b>	<b><u>486</u></b>	305	483
505.mcf_r	104	240	700	<b><u>240</u></b>	<b><u>701</u></b>	240	701	104	240	700	<b><u>240</u></b>	<b><u>701</u></b>	240	701
520.omnetpp_r	104	<b><u>494</u></b>	<b><u>276</u></b>	494	276	494	276	104	<b><u>494</u></b>	<b><u>276</u></b>	494	276	494	276
523.xalancbmk_r	104	<b><u>159</u></b>	<b><u>689</u></b>	159	690	159	689	104	<b><u>159</u></b>	<b><u>689</u></b>	159	690	159	689
525.x264_r	104	183	998	<b><u>182</u></b>	<b><u>998</u></b>	182	999	104	<b><u>173</u></b>	<b><u>1050</u></b>	173	1050	173	1050
531.deepsjeng_r	104	337	354	<b><u>337</u></b>	<b><u>353</u></b>	337	353	104	337	354	<b><u>337</u></b>	<b><u>353</u></b>	337	353
541.leela_r	104	<b><u>524</u></b>	<b><u>329</u></b>	524	329	524	329	104	<b><u>524</u></b>	<b><u>329</u></b>	524	329	524	329
548.exchange2_r	104	267	1020	<b><u>268</u></b>	<b><u>1020</u></b>	268	1020	104	267	1020	<b><u>268</u></b>	<b><u>1020</u></b>	268	1020
557.xz_r	104	<b><u>470</u></b>	<b><u>239</u></b>	470	239	472	238	104	<b><u>470</u></b>	<b><u>239</u></b>	470	239	472	238

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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## General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
ENERGY\_PERF\_BIAS\_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
Sub NUMA Cluster (SNC) set to SNC2

Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Feb 28 07:02:53 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

2. w  
07:02:53 up 3 min, 1 user, load average: 0.39, 0.42, 0.19  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 07:00 13.00s 2.01s 0.04s sh  
reportable-ic2023.2.3-lin-sapphirerapids-rate-smt-on-20231121.sh

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## Platform Notes (Continued)

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

```

core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 2060172
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 2060172
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

```

### 5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
sh reportable-ic2023.2.3-lin-sapphirerapids-rate-smt-on-20231121.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=104 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=52 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=104 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=52 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.003/temlogs/preenv.intrate.003.0.log --lognum 003.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

```

### 6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) PLATINUM 8571N
vendor_id      : GenuineIntel
cpu family      : 6
model          : 207
stepping       : 2
microcode      : 0x21000161
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 52
siblings       : 104
1 physical ids (chips)
104 processors (hardware threads)
physical id 0: core ids 0-51
physical id 0: apicids 0-103
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

### 7. lscpu

(Continued on next page)



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## Platform Notes (Continued)

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 104
On-line CPU(s) list:   0-103
Vendor ID:              GenuineIntel
Model name:             INTEL(R) XEON(R) PLATINUM 8571N
CPU family:             6
Model:                  207
Thread(s) per core:    2
Core(s) per socket:    52
Socket(s):              1
Stepping:               2
CPU max MHz:            4000.0000
CPU min MHz:            800.0000
BogoMIPS:               4800.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                        avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp
                        hwp_act_window hwp_epp hwp_pkg_req hfi avx512vbmi umip pku ospke waitpkg
                        avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                        avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                        enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                        amx_tile flush_lld arch_capabilities

L1d cache:             2.4 MiB (52 instances)
L1i cache:             1.6 MiB (52 instances)
L2 cache:              104 MiB (52 instances)
L3 cache:              300 MiB (1 instance)
NUMA node(s):          2
NUMA node0 CPU(s):    0-25,52-77
NUMA node1 CPU(s):    26-51,78-103
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBSRB-eIBRS SW
                        sequence
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.4M	12	Data	1	64	1	64
L1i	32K	1.6M	8	Instruction	1	64	1	64

(Continued on next page)



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### Platform Notes (Continued)

L2	2M	104M	16 Unified	2	2048	1	64
L3	300M	300M	20 Unified	3	245760	1	64

#### 8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-25,52-77
node 0 size: 257540 MB
node 0 free: 256621 MB
node 1 cpus: 26-51,78-103
node 1 size: 257532 MB
node 1 free: 256352 MB
node distances:
node  0  1
  0:  10  12
  1:  12  10

```

#### 9. /proc/meminfo

MemTotal: 527434648 kB

#### 10. who -r

run-level 3 Feb 28 07:00

#### 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```

Default Target Status
multi-user      running

```

#### 12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections postfix
purge-kernels rollback rsyslog smartd sshd systemd-pstore
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys kdump
kdump-early kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvme-autoconnect
rpcbind rpmconfigcheck rsyncd serial-getty@ smartd-generate_opts snmpd snmptrapd
systemd-boot-check-no-failures systemd-network-generator systemd-sysex
systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@ wicked wickedd-auto4
wicked-dhcp4 wickedd-dhcp6 wickedd-nanny
indirect wickedd

```

#### 13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=0a76fbd6-6541-4f13-928e-2a3caa636f4c
splash=silent
resume=/dev/disk/by-uuid/e6a5bcc3-5591-4cd4-a737-ee6cc3cdc083
mitigations=auto
quiet
security=apparmor
crashkernel=447M,high
crashkernel=72M,low

```

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## Platform Notes (Continued)

### 14. cpupower frequency-info

analyzing CPU 0:

current policy: frequency should be within 800 MHz and 4.00 GHz.

The governor "powersave" may decide which speed to use within this range.

boost state support:

Supported: yes

Active: yes

### 15. sysctl

```

kernel.numa_balancing      1
kernel.randomize_va_space  2
vm.compaction_proactiveness 20
vm.dirty_background_bytes  0
vm.dirty_background_ratio  10
vm.dirty_bytes              0
vm.dirty_expire_centisecs  3000
vm.dirty_ratio              20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness                60
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0

```

### 16. /sys/kernel/mm/transparent\_hugepage

```

defrag      always defer defer+madvice [madvice] never
enabled     [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```

alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000

```

### 18. OS release

```

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

### 19. Disk information

SPEC is set to: /home/CPU2017

```

Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p4 xfs 1.3T 163G 1.1T 14% /home

```

(Continued on next page)



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## Platform Notes (Continued)

```

20. /sys/devices/virtual/dmi/id
Vendor:      IEIT
Product:     NF5180-M7-A0-R0-00
Product Family: Not specified
Serial:      000000000

```

```

21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  3x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600
  5x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600

```

```

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      American Megatrends International, LLC.
BIOS Version:     05.13.00
BIOS Date:        11/30/2023

```

## Compiler Version Notes

```

=====
C      | 502.gcc_r(peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C      | 502.gcc_r(peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
=====

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
=====

```

```

=====
C++    | 520.omnetpp_r(base, peak) 523.xalanbmk_r(base, peak) 531.deepsjeng_r(base, peak)
=====

```

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## Compiler Version Notes (Continued)

| 541.leela\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base, peak)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsaphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/home/specdev/new\_compilers/ic2023.2.3/compiler/lib/intel64\_lin  
-lqkmallo

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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IEIT Systems Co., Ltd.

SPECrate®2017\_int\_base = 473

NF5180M7 (Intel Xeon Platinum 8571N)

SPECrate®2017\_int\_peak = 490

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Jan-2024

Hardware Availability: Dec-2023

Software Availability: Dec-2023

## Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsaphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsaphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```



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## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

```
502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes



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**CPU2017 License:** 3358

**Test Date:** Jan-2024

**Test Sponsor:** IEIT Systems Co., Ltd.

**Hardware Availability:** Dec-2023

**Tested by:** IEIT Systems Co., Ltd.

**Software Availability:** Dec-2023

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.4.xml>

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