



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_int\_base = 347

H3C UniServer R4700 G6 (Intel Xeon Silver 4416+)

SPECrate®2017\_int\_peak = 358

CPU2017 License: 9066

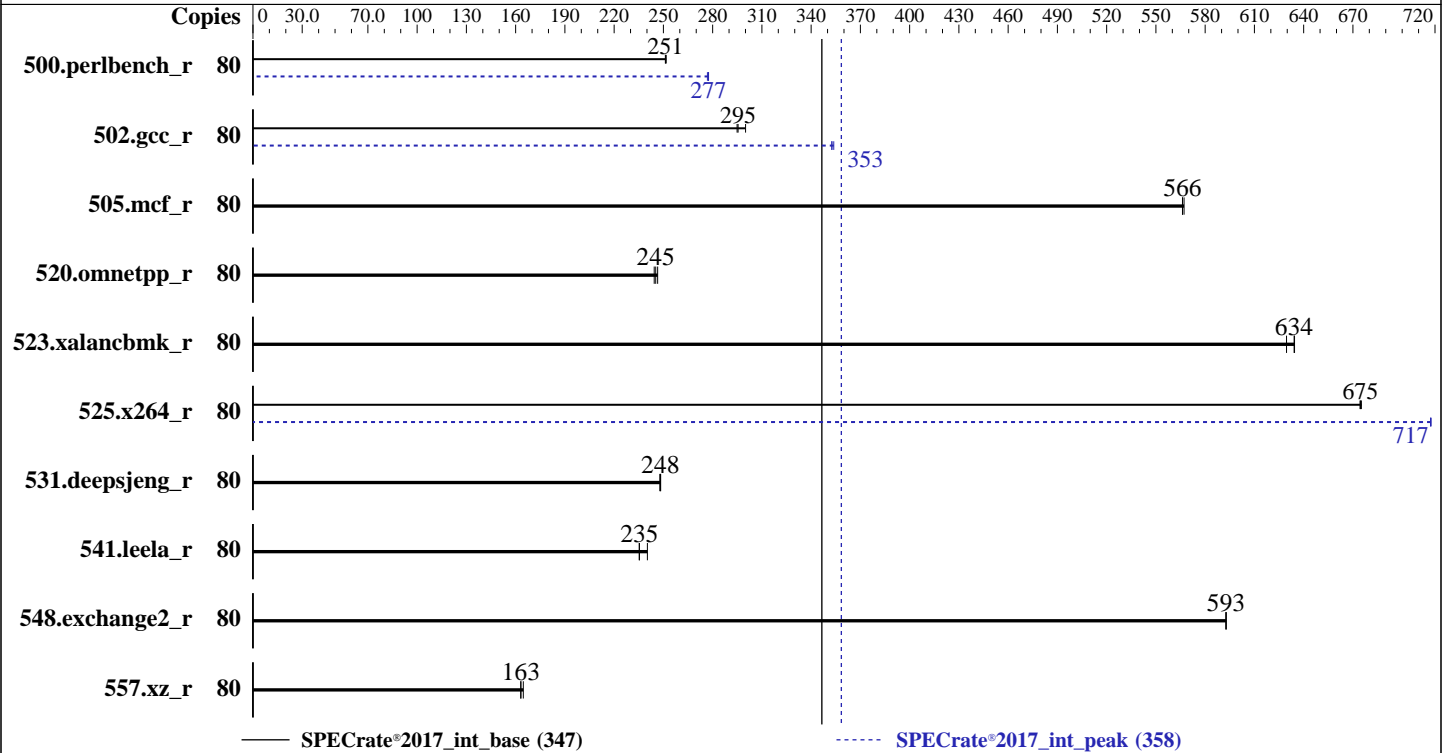
Test Date: Nov-2023

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2023

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Silver 4416+  
 Max MHz: 3900  
 Nominal: 2000  
 Enabled: 40 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 37.5 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R, running at 4000)  
 Storage: 1 x 480 GB SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.22.1.el9\_0.x86\_64  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 5.29 released Oct-2023 BIOS  
 File System: xfs  
 System State: Run level 5 (graphical)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	80	506	252	<b><u>507</u></b>	<b><u>251</u></b>	507	251	80	459	278	460	277	<b><u>459</u></b>	<b><u>277</u></b>
502.gcc_r	80	<b><u>383</u></b>	<b><u>295</u></b>	378	300	384	295	80	320	354	<b><u>321</u></b>	<b><u>353</u></b>	321	353
505.mcf_r	80	228	567	228	566	<b><u>228</u></b>	<b><u>566</u></b>	80	228	567	228	566	<b><u>228</u></b>	<b><u>566</u></b>
520.omnetpp_r	80	426	247	<b><u>428</u></b>	<b><u>245</u></b>	429	244	80	426	247	<b><u>428</u></b>	<b><u>245</u></b>	429	244
523.xalancbmk_r	80	133	634	134	630	<b><u>133</u></b>	<b><u>634</u></b>	80	133	634	134	630	<b><u>133</u></b>	<b><u>634</u></b>
525.x264_r	80	207	675	208	674	<b><u>208</u></b>	<b><u>675</u></b>	80	195	717	195	718	<b><u>195</u></b>	<b><u>717</u></b>
531.deepsjeng_r	80	369	248	<b><u>369</u></b>	<b><u>248</u></b>	370	248	80	369	248	<b><u>369</u></b>	<b><u>248</u></b>	370	248
541.leela_r	80	563	235	551	240	<b><u>563</u></b>	<b><u>235</u></b>	80	563	235	551	240	<b><u>563</u></b>	<b><u>235</u></b>
548.exchange2_r	80	354	593	354	593	<b><u>354</u></b>	<b><u>593</u></b>	80	354	593	354	593	<b><u>354</u></b>	<b><u>593</u></b>
557.xz_r	80	525	165	530	163	<b><u>529</u></b>	<b><u>163</u></b>	80	525	165	530	163	<b><u>529</u></b>	<b><u>163</u></b>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/lib/ia32:/home/speccpu/je5.0.1-32"  
MALLOCONF = "retain:true"



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## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
 memory using Red Hat Enterprise Linux 8.4  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
`sync; echo 3 > /proc/sys/vm/drop_caches`  
 runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`  
 NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
 is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
 is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
 is mitigated in the system as tested and documented.  
 jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:  
 Set SNC to Enable SNC2 (2-clusters)  
 Set Power Performance Tuning to BIOS Controls EFB  
 Set ENERGY\_PERF\_BIAS\_CFG mode to Performance

Sysinfo program /home/speccpu/bin/sysinfo  
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
 running on localhost.localdomain Sat Nov 4 03:40:43 2023

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. `uname -a`
2. `w`
3. Username
4. `ulimit -a`
5. `sysinfo process ancestry`
6. `/proc/cpuinfo`
7. `lscpu`
8. `numactl --hardware`
9. `/proc/meminfo`
10. `who -r`
11. Systemd service manager version: `systemd 250 (250-6.e19_0)`
12. Services, from `systemctl list-unit-files`
13. Linux kernel boot-time arguments, from `/proc/cmdline`
14. `cpupower frequency-info`
15. `tuned-adm active`
16. `sysctl`
17. `/sys/kernel/mm/transparent_hugepage`
18. `/sys/kernel/mm/transparent_hugepage/khugepaged`
19. OS release
20. Disk information
21. `/sys/devices/virtual/dmi/id`
22. `dmidecode`
23. BIOS

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## Platform Notes (Continued)

```

1. uname -a
Linux localhost.localdomain 5.14.0-70.22.1.el9_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux

```

```

2. w
03:40:43 up 5 min, 2 users, load average: 0.11, 0.11, 0.05
USER      TTY      LOGIN@   IDLE   JCPU   PCPU   WHAT
aaa       :1       03:38   ?xdm?  1:07   0.00s /usr/libexec/gdm-x-session --register-session --run-script
gnome-session
root     pts/0    03:38   1:15   0.12s  0.12s -bash

```

```

3. Username
From environment variable $USER: aaa

```

```

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2060364
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2060364
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
/usr/lib/systemd/systemd --user
/usr/libexec/gnome-terminal-server
bash
su
bash
sh reportable-ic2023.0-lin-core-avx512-rate-smt-on-20221201.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 -c
ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=40 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=80 --configfile
ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=40 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/tempslogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/speccpu

```

```

6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Silver 4416+

```

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## Platform Notes (Continued)

```

vendor_id      : GenuineIntel
cpu family     : 6
model         : 143
stepping      : 8
microcode     : 0x2b000461
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores     : 20
siblings      : 40
2 physical ids (chips)
80 processors (hardware threads)
physical id 0: core ids 0-19
physical id 1: core ids 0-19
physical id 0: apicids 0-39
physical id 1: apicids 128-167

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                80
On-line CPU(s) list:  0-79
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel(R) Corporation
Model name:            Intel(R) Xeon(R) Silver 4416+
BIOS Model name:      Intel(R) Xeon(R) Silver 4416+
CPU family:            6
Model:                 143
Thread(s) per core:   2
Core(s) per socket:   20
Socket(s):             2
Stepping:              8
CPU max MHz:          3900.0000
CPU min MHz:          800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                      sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                      invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                      tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmlil avx2
                      smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                      avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                      xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                      cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                      arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                      ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                      tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                      enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                      amx_tile flush_lld arch_capabilities

Virtualization:        VT-x
Lld cache:             1.9 MiB (40 instances)

```

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## Platform Notes (Continued)

```

L1i cache: 1.3 MiB (40 instances)
L2 cache: 80 MiB (40 instances)
L3 cache: 75 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-9,40-49
NUMA node1 CPU(s): 10-19,50-59
NUMA node2 CPU(s): 20-29,60-69
NUMA node3 CPU(s): 30-39,70-79
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.9M	12	Data	1	64	1	64
L1i	32K	1.3M	8	Instruction	1	64	1	64
L2	2M	80M	16	Unified	2	2048	1	64
L3	37.5M	75M	15	Unified	3	40960	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-9,40-49
node 0 size: 128140 MB
node 0 free: 127314 MB
node 1 cpus: 10-19,50-59
node 1 size: 129019 MB
node 1 free: 127545 MB
node 2 cpus: 20-29,60-69
node 2 size: 128983 MB
node 2 free: 128589 MB
node 3 cpus: 30-39,70-79
node 3 size: 129008 MB
node 3 free: 128609 MB
node distances:
node  0  1  2  3
0:  10 12 21 21
1:  12 10 21 21
2:  21 21 10 12
3:  21 21 12 10

```

9. /proc/meminfo

MemTotal: 527516268 kB

10. who -r

run-level 5 Nov 4 03:36

11. Systemd service manager version: systemd 250 (250-6.e19\_0)

```

Default Target Status
graphical      running

```

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## Platform Notes (Continued)

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker gdm
getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt lm_sensors
low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd nis-domainname
nvmefc-boot-connections ostree-remount pmcd pmie pmlgger power-profiles-daemon
qemu-guest-agent rhsmcertd rpcbind rsyslog rtkit-daemon selinux-autorelabel-mark smartd
sshd sssd switcheroo-control sysstat systemd-network-generator tuned udisks2 upower
vgauthd virtqemud vmtoolsd

enabled-runtime systemd-remount-fs
disabled arp-ethers autofs blk-availability brltty canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
dbus-daemon debug-shell dnsmasq dovecot fancontrol fcoe firewallld grafana-server gssproxy
httpd httpd@ ibacm iprdump iprinit iprupdate ipsec iscsid iscsiuiio kpatch kvm_stat ledmon
libvirt-guests libvirtl lldpad man-db-restart-cache-update named named-chroot nfs-blkmap
nfs-server nftables nmb numad nvme-autoconnect pmfind pmie_farm pmlgger_farm pmproxy
podman podman-auto-update podman-restart postfix powertop psacct ras-mc-ctl rasdaemon
rdisc rhcd rhsm rhsm-facts rpmdb-rebuild rrdcached saslauthd serial-getty@ smb snmpd
snmptrapd spamassassin speech-dispatcherd srp_daemon srp_daemon_port@ sshd-keygen@
systemd-boot-check-no-failures systemd-nspawn@ systemd-pstore systemd-sysex target
targetclid tog-pegasus trace-cmd virtinterfaced virtnetworkkd virtnodedevid virtnwfilterd
virtproxyd virtsecret virtstoraged vsftpd wpa_supplicant

indirect pcsd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
virtlockd virtlogd vsftpd@

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.22.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.90 GHz.
                   The governor "performance" may decide which speed to use
                   within this range.
boost state support:
  Supported: yes
  Active: yes

```

```

-----
15. tuned-adm active
  Current active profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness     20
vm.dirty_background_bytes      0

```

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## Platform Notes (Continued)

```

vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  10
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)

```

```

-----
20. Disk information
SPEC is set to: /home/speccpu
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs  372G  11G  361G   3% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      H3C
Product:     RS33M2C9S
Product Family: Rack

```

```

-----
22. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  15x Micron MTC20F2085S1RC48BA1 32 GB 2 rank 4800, configured at 4000

```

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Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2023

Hardware Availability: Oct-2023

Software Availability: Dec-2022

## Platform Notes (Continued)

1x Samsung M321R4GA3BB6-CQKMG 32 GB 2 rank 4800, configured at 4000

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 6.00.30  
BIOS Date: 10/20/2023  
BIOS Revision: 5.29

## Compiler Version Notes

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base, peak) 523.xalancbnk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECrate®2017\_int\_base = 347

H3C UniServer R4700 G6 (Intel Xeon Silver 4416+)

SPECrate®2017\_int\_peak = 358

**CPU2017 License:** 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Nov-2023

**Hardware Availability:** Oct-2023

**Software Availability:** Dec-2022

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```



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SPECrate®2017\_int\_peak = 358

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2023

Hardware Availability: Oct-2023

Software Availability: Dec-2022

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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## Peak Optimization Flags (Continued)

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.html](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.html)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevC.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevC.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.xml](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.xml)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-SPR-RevC.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevC.xml)

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