



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_int_base = 404

PowerEdge R6615 (AMD EPYC 9384X 32-Core Processor)

SPECrate®2017_int_peak = 410

CPU2017 License: 6573

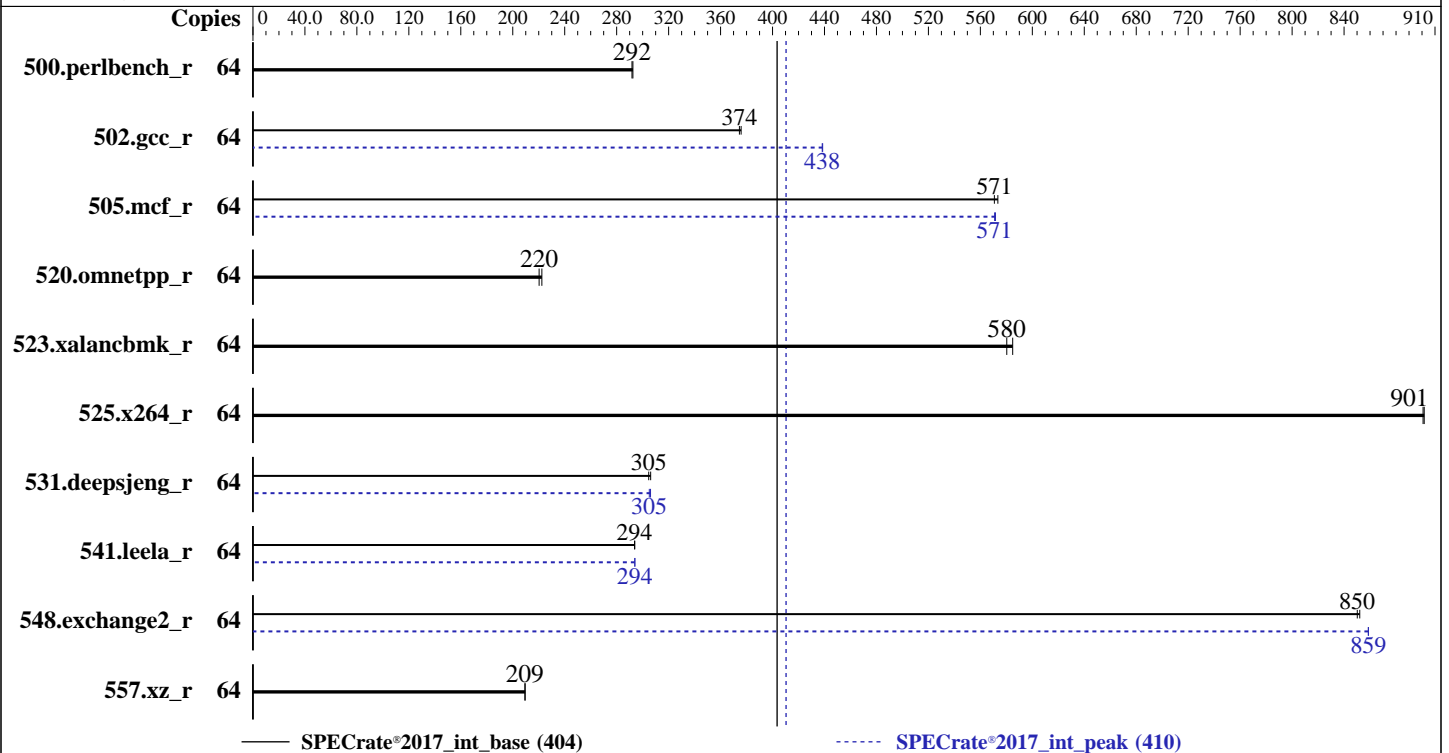
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jul-2023

Hardware Availability: Jul-2023

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9384X
 Max MHz: 3900
 Nominal: 3100
 Enabled: 32 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 768 MB I+D on chip per chip, 96 MB shared / 4 cores
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 60 GB on tmpfs
 Other: None

Software

OS: SUSE Linux Enterprise High Performance Computing 15 SP4 5.14.21-150400.22-default
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: No
 Firmware: Version 1.4.2 released May-2023
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	349	292	348	293			64	349	292	348	293		
502.gcc_r	64	242	374	241	376			64	207	438	207	439		
505.mcf_r	64	180	573	181	571			64	181	571	181	572		
520.omnetpp_r	64	378	222	381	220			64	378	222	381	220		
523.xalancbmk_r	64	116	585	116	580			64	116	585	116	580		
525.x264_r	64	124	902	124	901			64	124	902	124	901		
531.deepsjeng_r	64	240	306	241	305			64	240	306	240	305		
541.leela_r	64	361	294	361	294			64	360	294	361	294		
548.exchange2_r	64	197	850	197	852			64	195	859	195	859		
557.xz_r	64	330	210	330	209			64	330	210	330	209		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1/amd_rate_aocc400_znver4_A_lib/lib:/mnt/ramdisk/cpu2017-1
    .1.9-aocc400-znver4-A1/amd_rate_aocc400_znver4_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 60 GB ramdisk created with the cmd: "mount -t tmpfs -o size=60G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```
    DRAM Refresh Delay : Performance
    DIMM Self Healing on
Uncorrectable Memory Error : Disabled
Virtualization Technology : Disabled
    NUMA Nodes per Socket : 4
    L3 Cache as NUMA Domain : Enabled

    System Profile : Custom
    Memory Patrol Scrub : Disabled
    PCI ASPM L1 Link
    Power Management : Disabled
    Determinism Slider : Power Determinism
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu May 12 11:18:08 2022

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

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Platform Notes (Continued)

- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. sysctl
- 15. /sys/kernel/mm/transparent_hugepage
- 16. /sys/kernel/mm/transparent_hugepage/khugepaged
- 17. OS release
- 18. Disk information
- 19. /sys/devices/virtual/dmi/id
- 20. dmidecode
- 21. BIOS

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
11:18:08 up 2 min, 1 user, load average: 3.29, 2.14, 0.85
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 11:17 32.00s 1.62s 0.31s /bin/bash ./amd_rate_aocc400_znver4_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3094194
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 3094194
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-BIOS-adddcD=1
--define DL-BIOS-VirtD=1 --define DL-VERS=v4.6 --output_format html,pdf,txt
python3 ./run_amd_rate_aocc400_znver4_A1.py
/bin/bash ./amd_rate_main400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-BIOS-adddcD=1 --define DL-BIOS-VirtD=1 --define DL-VERS=v4.6
```

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Platform Notes (Continued)

```
--output_format html,pdf,txt intrate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-BIOS-addcd=1 --define DL-BIOS-VirtD=1 --define DL-VERS=v4.6
--output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size test:train:refrate intrate
--nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 9384X 32-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa101121
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores      : 32
siblings       : 64
1 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-3,8-11,16-19,24-27,32-35,40-43,48-51,56-59
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----
7. lscpu

From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                64
On-line CPU(s) list:   0-63
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9384X 32-Core Processor
CPU family:            25
Model:                 17
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):              1
Stepping:              1
Frequency boost:       enabled
CPU max MHz:           3911.3279
CPU min MHz:           1500.0000
BogoMIPS:              6201.57
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
perfctr_core perfctr_nb bpeext perfctr_llc mwaitx cpb cat_l3 cdp_l3
invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
```

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```
xsavc xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
```

```
AMD-V
Virtualization:
L1d cache: 1 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 32 MiB (32 instances)
L3 cache: 768 MiB (8 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-7,32-39
NUMA node1 CPU(s): 8-15,40-47
NUMA node2 CPU(s): 16-23,48-55
NUMA node3 CPU(s): 24-31,56-63
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

```
From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 32K 1M 8 Data 1 64 1 64
L1i 32K 1M 8 Instruction 1 64 1 64
L2 1M 32M 8 Unified 2 2048 1 64
L3 96M 768M 16 Unified 3 98304 1 64
```

```
-----
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0-7,32-39
node 0 size: 193076 MB
node 0 free: 190367 MB
node 1 cpus: 8-15,40-47
node 1 size: 193496 MB
node 1 free: 193025 MB
node 2 cpus: 16-23,48-55
node 2 size: 193530 MB
node 2 free: 191401 MB
node 3 cpus: 24-31,56-63
node 3 size: 193467 MB
node 3 free: 193111 MB
node distances:
node 0 1 2 3
0: 10 12 12 12
1: 12 10 12 12
2: 12 12 10 12
3: 12 12 12 10
```

```
-----
9. /proc/meminfo
MemTotal: 792137352 kB
```

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Platform Notes (Continued)

10. who -r
run-level 3 May 12 11:17

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
issue-generator kbdsettings postfix purge-kernels rollback sshd wicked wickedd-auto4
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autoyast-initscripts boot-sysctl ca-certificates chrony-wait chronyd console-getty
debug-shell grub2-once haveged-switch-root hwloc-dump-hwdata issue-add-ssh-keys kexec-load
lunmask munge nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd salt-minion serial-getty@
slurmctld slurmd slurmdbd systemd-boot-check-no-failures systemd-network-generator
systemd-sysext systemd-time-wait-sync systemd-timesyncd ypbind
indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=32833f21-2096-4803-86a7-7316a302782e
splash=silent
mitigations=auto
quiet
security=apparmor

14. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1

15. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152

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Platform Notes (Continued)

shmem_enabled always within_size advise [never] deny force

```
-----
16. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
-----
```

```
-----
17. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise High Performance Computing 15 SP4
-----
```

```
-----
18. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 60G 3.5G 57G 6% /mnt/ramdisk
-----
```

```
-----
19. /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R6615
Product Family: PowerEdge
Serial: GLM4018
-----
```

```
-----
20. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
12x 802C0000802C MTC40F2046S1RC48BA1 64 GB 2 rank 4800
-----
```

```
-----
21. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Dell Inc.
BIOS Version: 1.4.2
BIOS Date: 05/05/2023
BIOS Revision: 1.4
-----
```

Compiler Version Notes

C | 502.gcc_r(peak)

```
-----
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
-----
```

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Compiler Version Notes (Continued)

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
=====

```

```

=====
C      | 502.gcc_r(peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
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```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
=====

```

```

=====
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
=====

```

```

=====
Fortran | 548.exchange2_r(base, peak)
=====

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
=====

```

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

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Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

Base Portability Flags

```

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc

```

C++ benchmarks:

```

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext

```

Fortran benchmarks:

```

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop

```

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SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 404

PowerEdge R6615 (AMD EPYC 9384X 32-Core Processor)

SPECrate®2017_int_peak = 410

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jul-2023

Hardware Availability: Jul-2023

Software Availability: Nov-2022

Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
```

(Continued on next page)



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Peak Portability Flags (Continued)

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext

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Peak Optimization Flags (Continued)

```
541.leela_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-inline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.1.xml>



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