



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Inspur Electronic Information Industry Co., Ltd.  
NF5280M6 (Intel Xeon Gold 5318N)

SPECspeed®2017\_int\_base = 11.6

SPECspeed®2017\_int\_peak = 11.8

CPU2017 License: 3358

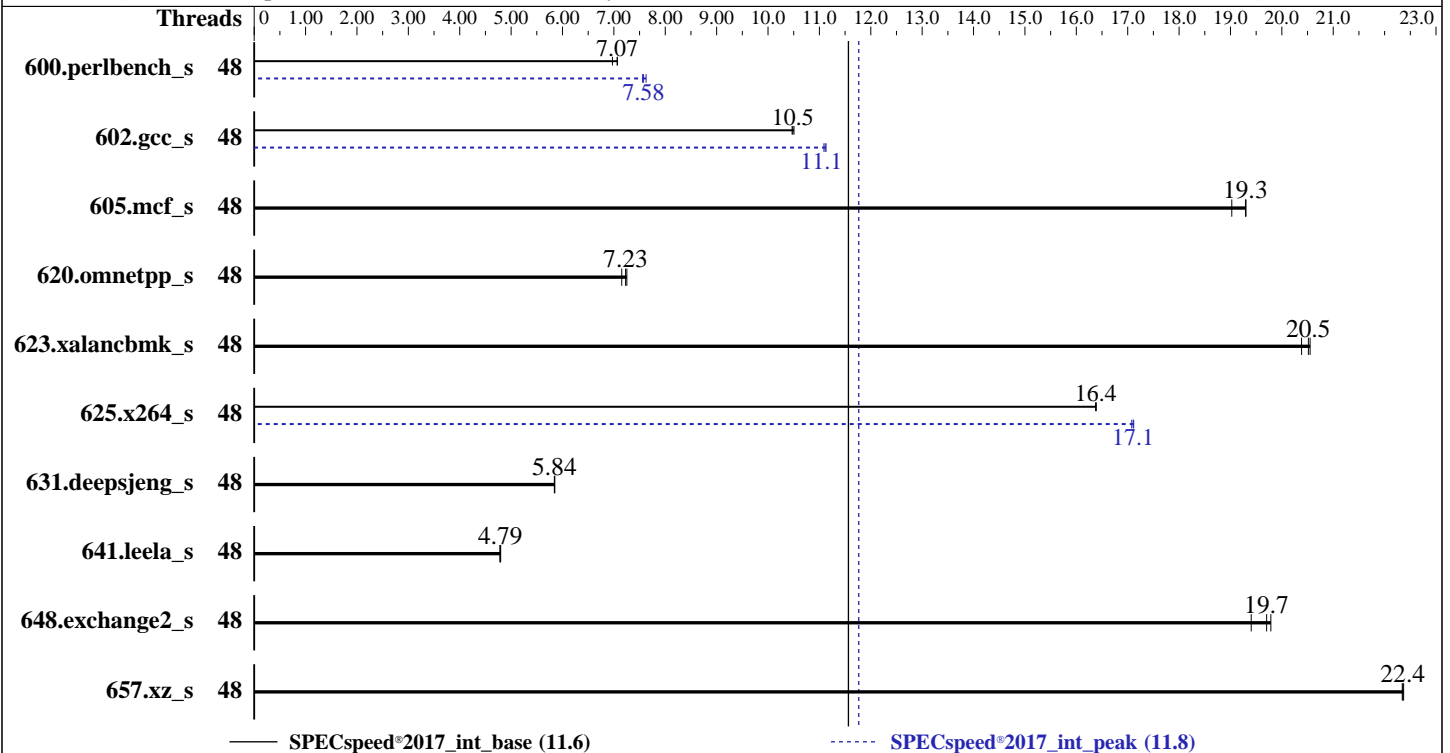
Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Aug-2023

Hardware Availability: May-2021

Software Availability: Dec-2022



## Hardware

CPU Name: Intel Xeon Gold 5318N  
 Max MHz: 3400  
 Nominal: 2100  
 Enabled: 24 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 36 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R, running at 2666)  
 Storage: 1 x 2 TB NVME SSD  
 Other: None

## Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.13.1.el9\_0.x86\_64  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: Yes  
 Firmware: Version 07.01.00 released Feb-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	255	6.97	251	7.07	<u>251</u>	<u>7.07</u>	48	233	7.63	235	7.56	<u>234</u>	<u>7.58</u>
602.gcc_s	48	379	10.5	<b>380</b>	<b>10.5</b>	380	10.5	48	<b>358</b>	<b>11.1</b>	359	11.1	358	11.1
605.mcf_s	48	245	19.3	248	19.0	<u>245</u>	<u>19.3</u>	48	245	19.3	248	19.0	<u>245</u>	<u>19.3</u>
620.omnetpp_s	48	<u>226</u>	<u>7.23</u>	228	7.15	225	7.25	48	<u>226</u>	<u>7.23</u>	228	7.15	225	7.25
623.xalancbmk_s	48	<b>69.1</b>	<b>20.5</b>	69.0	20.5	69.5	20.4	48	<b>69.1</b>	<b>20.5</b>	69.0	20.5	69.5	20.4
625.x264_s	48	108	16.4	108	16.4	<u>108</u>	<u>16.4</u>	48	103	17.1	103	17.1	<u>103</u>	<u>17.1</u>
631.deepsjeng_s	48	<u>245</u>	<u>5.84</u>	245	5.85	245	5.84	48	<u>245</u>	<u>5.84</u>	245	5.85	245	5.84
641.leela_s	48	356	4.80	357	4.78	<b>356</b>	<b>4.79</b>	48	356	4.80	357	4.78	<b>356</b>	<b>4.79</b>
648.exchange2_s	48	<u>149</u>	<u>19.7</u>	152	19.4	149	19.8	48	<u>149</u>	<u>19.7</u>	152	19.4	149	19.8
657.xz_s	48	276	22.4	<u>277</u>	<u>22.4</u>	277	22.3	48	276	22.4	<u>277</u>	<u>22.4</u>	277	22.3

SPECspeed®2017\_int\_base = **11.6**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

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## General Notes (Continued)

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
ENERGY\_PERF\_BIAS\_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
Sub NUMA Cluster (SNC) set to disable  
Active cores set to 12

Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Wed Aug 9 10:57:37 2023

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.e19\_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.e19\_0.x86\_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86\_64

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## Platform Notes (Continued)

x86\_64 x86\_64 GNU/Linux

```

-----
2. w
  10:57:37 up 0 min,  1 user,  load average: 0.13, 0.03, 0.01
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
root      ttyl    10:57   9.00s  1.07s  0.00s  sh
reportable-ic2023.0-lin-core-avx512-speed-smt-off-20221201.sh

```

```

-----
3. Username
  From environment variable $USER:  root

```

```

-----
4. ulimit -a
  real-time non-blocking time (microseconds, -R) unlimited
  core file size              (blocks, -c) 0
  data seg size                (kbytes, -d) unlimited
  scheduling priority          (-e) 0
  file size                    (blocks, -f) unlimited
  pending signals              (-i) 4126754
  max locked memory            (kbytes, -l) 64
  max memory size              (kbytes, -m) unlimited
  open files                   (-n) 1024
  pipe size                    (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority           (-r) 0
  stack size                   (kbytes, -s) unlimited
  cpu time                     (seconds, -t) unlimited
  max user processes           (-u) 4126754
  virtual memory               (kbytes, -v) unlimited
  file locks                   (-x) unlimited

```

```

-----
5. sysinfo process ancestry
  /usr/lib/systemd/systemd --switched-root --system --deserialize 28
  login -- root
  -bash
  sh reportable-ic2023.0-lin-core-avx512-speed-smt-off-20221201.sh
  runcpu --nobuild --action validate --define default-platform-flags -c
    ic2023.0-lin-core-avx512-speed-20221201.cfg --define cores=48 --tune base,peak -o all --define
    intspeedaffinity --define drop_caches intspeed
  runcpu --nobuild --action validate --define default-platform-flags --configfile
    ic2023.0-lin-core-avx512-speed-20221201.cfg --define cores=48 --tune base,peak --output_format all
    --define intspeedaffinity --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed
    intspeed --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log
    --lognum 001.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
  $SPEC = /home/CPU2017

```

```

-----
6. /proc/cpuinfo
  model name      : Intel(R) Xeon(R) Gold 5318N CPU @ 2.10GHz
  vendor_id      : GenuineIntel
  cpu family     : 6
  model          : 106
  stepping       : 6
  microcode      : 0xd000331
  bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
  cpu cores     : 12

```

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## Platform Notes (Continued)

```
siblings          : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 12-23
physical id 1: core ids 12-23
physical id 0: apicids 24-47
physical id 1: apicids 88-111
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 48
On-line CPU(s) list:   0-47
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) Gold 5318N CPU @ 2.10GHz
BIOS Model name:       Intel(R) Xeon(R) Gold 5318N CPU @ 2.10GHz
CPU family:             6
Model:                  106
Thread(s) per core:    2
Core(s) per socket:    12
Socket(s):              2
Stepping:               6
CPU max MHz:           3400.0000
CPU min MHz:           800.0000
BogoMIPS:               4200.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl smx est
                        tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe
                        popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_ppin ssbd mba
                        ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
                        invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
                        xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect
                        wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2
                        gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                        rdpid fsrm md_clear pconfig flush_lld arch_capabilities

L1d cache:             1.1 MiB (24 instances)
L1i cache:             768 KiB (24 instances)
L2 cache:              30 MiB (24 instances)
L3 cache:              72 MiB (2 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0-11,24-35
NUMA node1 CPU(s):    12-23,36-47
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:   Not affected
Vulnerability Mds:    Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
```

(Continued on next page)



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## Platform Notes (Continued)

Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1.3M	30M	20	Unified	2	1024	1	64
L3	36M	72M	12	Unified	3	49152	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-11,24-35
node 0 size: 258042 MB
node 0 free: 257411 MB
node 1 cpus: 12-23,36-47
node 1 size: 257995 MB
node 1 free: 257290 MB
node 2 cpus:
node 2 size: 257642 MB
node 2 free: 257516 MB
node 3 cpus:
node 3 size: 258047 MB
node 3 free: 257929 MB
node distances:
node  0  1  2  3
  0:  10  20  11  20
  1:  20  10  20  11
  2:  11  20  10  20
  3:  20  11  20  10

```

9. /proc/meminfo

MemTotal: 1056489624 kB

10. who -r

run-level 3 Aug 9 10:57

11. Systemd service manager version: systemd 250 (250-6.el9\_0)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE          UNIT FILES
enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
                nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
                systemd-network-generator udisks2 upower
enabled-runtime systemd-remount-fs
disabled       blk-availability canberra-system-bootup canberra-system-shutdown
                canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell kvm_stat
                man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmdb-rebuild serial-getty@
                sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect       sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

```

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## Platform Notes (Continued)

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.40 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

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## Platform Notes (Continued)

### 18. OS release

```
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)
```

### 19. Disk information

```
SPEC is set to: /home/CPU2017
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      1.4T      530G  885G  38% /home
```

### 20. /sys/devices/virtual/dmi/id

```
Vendor:         IEI
Product:        NF5280M6
Product Family: Not specified
Serial:         000000000
```

### 21. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2666

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:    American Megatrends Inc.
BIOS Version:   07.01.00
BIOS Date:      02/10/2023
BIOS Revision:  5.22
```

## Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)  
      | 657.xz_s(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

```
=====  
C++    | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)  
      | 641.leela_s(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

```
=====  
Fortran | 648.exchange2_s(base, peak)  
=====
```

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201

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## Compiler Version Notes (Continued)

Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC\_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-m64 -std=c++14 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

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# SPEC CPU®2017 Integer Speed Result

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Inspur Electronic Information Industry Co., Ltd.  
NF5280M6 (Intel Xeon Gold 5318N)

SPECspeed®2017\_int\_base = 11.6

SPECspeed®2017\_int\_peak = 11.8

CPU2017 License: 3358

Test Sponsor: Inspur Electronic Information Industry Co., Ltd.

Tested by: Inspur Electronic Information Industry Co., Ltd.

Test Date: Aug-2023

Hardware Availability: May-2021

Software Availability: Dec-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

605.mcf\_s: basepeak = yes

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## Peak Optimization Flags (Continued)

```
625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP  
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.2.xml>

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