



# SPEC CPU®2017 Integer Rate Result

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## Supermicro

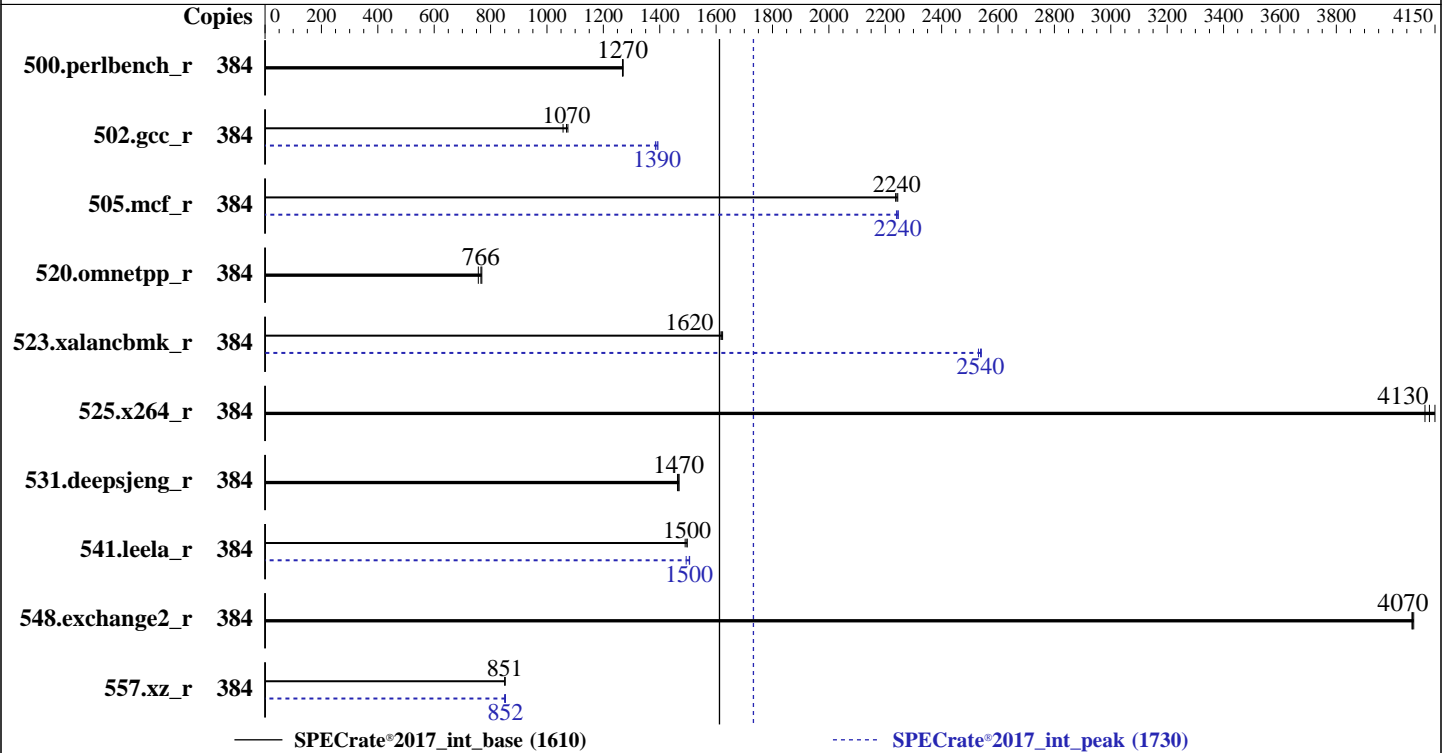
A+ Server 2125HS-TNR  
(H13DSH , AMD EPYC 9654)

SPECrate®2017\_int\_base = 1610

SPECrate®2017\_int\_peak = 1730

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Oct-2022  
Hardware Availability: Nov-2022  
Software Availability: Nov-2022



### Hardware

CPU Name: AMD EPYC 9654  
Max MHz: 3700  
Nominal: 2400  
Enabled: 192 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores  
Other: None  
Memory: 2304 GB (24 x 96 GB 2Rx4 PC5-4800B-R)  
Storage: 1.2 TB on tmpfs  
Other: None

### Software

OS: Ubuntu 22.04.1 LTS  
Kernel 5.15.0-50-generic  
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
Parallel: No  
Firmware: Version 0.10 released Oct-2022  
File System: tmpfs  
System State: Run level 5 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base   |                   |                    |                   |                    |                   |                   | Peak   |                   |                    |                   |                    |                   |                    |
|-----------------|--------|-------------------|--------------------|-------------------|--------------------|-------------------|-------------------|--------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
|                 | Copies | Seconds           | Ratio              | Seconds           | Ratio              | Seconds           | Ratio             | Copies | Seconds           | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              |
| 500.perlbench_r | 384    | 482               | 1270               | <b><u>482</u></b> | <b><u>1270</u></b> | 481               | 1270              | 384    | 482               | 1270               | <b><u>482</u></b> | <b><u>1270</u></b> | 481               | 1270               |
| 502.gcc_r       | 384    | <b><u>509</u></b> | <b><u>1070</u></b> | 514               | 1060               | 506               | 1070              | 384    | 393               | 1380               | <b><u>391</u></b> | <b><u>1390</u></b> | 390               | 1390               |
| 505.mcf_r       | 384    | 276               | 2240               | <b><u>277</u></b> | <b><u>2240</u></b> | 277               | 2240              | 384    | 276               | 2250               | 277               | 2240               | <b><u>276</u></b> | <b><u>2240</u></b> |
| 520.omnetpp_r   | 384    | 655               | 769                | 666               | 756                | <b><u>658</u></b> | <b><u>766</u></b> | 384    | 655               | 769                | 666               | 756                | <b><u>658</u></b> | <b><u>766</u></b>  |
| 523.xalancbmk_r | 384    | 251               | 1620               | <b><u>250</u></b> | <b><u>1620</u></b> | 250               | 1620              | 384    | 160               | 2530               | 160               | 2540               | <b><u>160</u></b> | <b><u>2540</u></b> |
| 525.x264_r      | 384    | 162               | 4150               | <b><u>163</u></b> | <b><u>4130</u></b> | 163               | 4110              | 384    | 162               | 4150               | <b><u>163</u></b> | <b><u>4130</u></b> | 163               | 4110               |
| 531.deepsjeng_r | 384    | 300               | 1470               | <b><u>300</u></b> | <b><u>1470</u></b> | 301               | 1460              | 384    | 300               | 1470               | <b><u>300</u></b> | <b><u>1470</u></b> | 301               | 1460               |
| 541.leela_r     | 384    | <b><u>425</u></b> | <b><u>1500</u></b> | 425               | 1500               | 427               | 1490              | 384    | <b><u>423</u></b> | <b><u>1500</u></b> | 426               | 1490               | 422               | 1510               |
| 548.exchange2_r | 384    | 247               | 4070               | <b><u>247</u></b> | <b><u>4070</u></b> | 247               | 4070              | 384    | 247               | 4070               | <b><u>247</u></b> | <b><u>4070</u></b> | 247               | 4070               |
| 557.xz_r        | 384    | 488               | 850                | <b><u>487</u></b> | <b><u>851</u></b>  | 487               | 851               | 384    | 488               | 850                | <b><u>487</u></b> | <b><u>852</u></b>  | 486               | 853                |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

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## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/dev/shm/ppog/CPU2017/amd\_rate\_aocc400\_genoa\_B\_lib/lib:/dev/shm/ppog/CP  
U2017/amd\_rate\_aocc400\_genoa\_B\_lib/lib32:"  
MALLOC\_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:  
MALLOC\_CONF = "thp:never"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Settings:  
Determinism Control = Manual  
Determinism Enable = Disable Performance Determinism  
cTDP Control = Manual  
cTDP = 400  
Package Power Limit Control = Manual  
Package Power Limit = 400

Sysinfo program /dev/shm/ppog/CPU2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on sysv Thu Oct 20 19:19:22 2022

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## Platform Notes (Continued)

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 9654 96-Core Processor
 2 "physical id"s (chips)
 384 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 96
siblings : 192
physical 0: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53
54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81
82 83 84 85 86 87 88 89 90 91 92 93 94 95
physical 1: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53
54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81
82 83 84 85 86 87 88 89 90 91 92 93 94 95
```

From lscpu from util-linux 2.37.2:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 384
On-line CPU(s) list: 0-383
Vendor ID: AuthenticAMD
Model name: AMD EPYC 9654 96-Core Processor
CPU family: 25
Model: 17
Thread(s) per core: 2
Core(s) per socket: 96
Socket(s): 2
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3709.0000
CPU min MHz: 400.0000
BogoMIPS: 4800.30
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
```

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### Platform Notes (Continued)

bpext perfctr\_llc mwaitx cpb cat\_l3 cdp\_l3 invpcid\_single hw\_pstate ssbd mba ibrs  
ibpb stibp vmcall fsgsbase bmil avx2 smep bmi2 erms invpcid cqm rdt\_a avx512f  
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw  
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local avx512\_bf16 clzero irperf xsaveprtr rdpru wbnoinvd amd\_ppin cppc arat  
npt lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists  
pausefilter pfthreshold avic v\_vmsave\_vmload vgif v\_spec\_ctrl avx512vbmi umip pku  
ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq  
la57 rdpid overflow\_recov succor smca fsrm flush\_lld

Virtualization: AMD-V  
L1d cache: 6 MiB (192 instances)  
L1i cache: 6 MiB (192 instances)  
L2 cache: 192 MiB (192 instances)  
L3 cache: 768 MiB (24 instances)  
NUMA node(s): 2  
NUMA node0 CPU(s): 0-95,192-287  
NUMA node1 CPU(s): 96-191,288-383  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP always-on, RSB filling, PBRSE-eIBRS Not affected  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 32K      | 6M       | 8    | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 6M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 192M     | 8    | Unified     | 2     | 2048  | 1        | 64             |
| L3   | 32M      | 768M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

/proc/cpuinfo cache data  
cache size : 1024 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27  
28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56

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### Platform Notes (Continued)

```
57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85
86 87 88 89 90 91 92 93 94 95 192 193 194 195 196 197 198 199 200 201 202 203 204 205
206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227
228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249
250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271
272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287
```

```
node 0 size: 1160854 MB
node 0 free: 1151297 MB
```

```
node 1 cpus: 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114
115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136
137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158
159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180
181 182 183 184 185 186 187 188 189 190 191 288 289 290 291 292 293 294 295 296 297 298
299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320
321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342
343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364
365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383
```

```
node 1 size: 1161087 MB
node 1 free: 1156344 MB
```

```
node distances:
node  0  1
  0:  10  32
  1:  32  10
```

```
From /proc/meminfo
MemTotal:      2377668680 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

```
/usr/bin/lsb_release -d
Ubuntu 22.04.1 LTS
```

```
From /etc/*release* /etc/*version*
debian_version: bookworm/sid
os-release:
  PRETTY_NAME="Ubuntu 22.04.1 LTS"
  NAME="Ubuntu"
  VERSION_ID="22.04"
  VERSION="22.04.1 LTS (Jammy Jellyfish)"
  VERSION_CODENAME=jammy
  ID=ubuntu
  ID_LIKE=debian
  HOME_URL="https://www.ubuntu.com/"
```

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### Platform Notes (Continued)

```
uname -a:
Linux sysv 5.15.0-50-generic #56-Ubuntu SMP Tue Sep 20 13:23:26 UTC 2022 x86_64 x86_64
x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

|  |  |
|--|--|
| CVE-2018-12207 (iTLB Multihit):                        | Not affected   |
| CVE-2018-3620 (L1 Terminal Fault):                     | Not affected   |
| Microarchitectural Data Sampling:                      | Not affected   |
| CVE-2017-5754 (Meltdown):                              | Not affected   |
| mmio_stale_data:                                       | Not affected   |
| retbleed:  | Not affected   |
| CVE-2018-3639 (Speculative Store Bypass):              | Mitigation: Speculative Store Bypass disabled via prctl and seccomp  |
| CVE-2017-5753 (Spectre variant 1):                     | Mitigation: usercopy/swaps barriers and __user pointer sanitization  |
| CVE-2017-5715 (Spectre variant 2):                     | Mitigation: Retpolines, IBPB: conditional, IBRS_FW, STIBP: always-on, RSB filling, PBRSE-eIBRS: Not affected |
| CVE-2020-0543 (Special Register Buffer Data Sampling): | Not affected   |
| CVE-2019-11135 (TSX Asynchronous Abort):               | Not affected   |

```
run-level 5 Oct 19 22:59
```

```
SPEC is set to: /dev/shm/ppog/CPU2017
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs            tmpfs    1.2T  4.8G  1.2T   1% /dev/shm
```

```
From /sys/devices/virtual/dmi/id
Vendor:          Supermicro
Product:         Super Server
Product Family: SMC H13
Serial:         123456789
```

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
Memory:
7x SK Hynix HMCGM4MEBRB175N 96 GB 2 rank 4800
17x SK Hynix HMCGM4MEBRB233N 96 GB 2 rank 4800
```

```
BIOS:
BIOS Vendor:    American Megatrends International, LLC.
```

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### Platform Notes (Continued)

BIOS Version: 0.10  
BIOS Date: 10/18/2022  
BIOS Revision: 5.27

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 502.gcc\_r(peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base, peak)  
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### Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====  
C++ | 523.xalancbmk\_r(peak)

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C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base)  
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Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

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# SPEC CPU®2017 Integer Rate Result

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## Supermicro

A+ Server 2125HS-TNR  
(H13DSH , AMD EPYC 9654)

SPECrate®2017\_int\_base = 1610

SPECrate®2017\_int\_peak = 1730

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2022  
**Hardware Availability:** Nov-2022  
**Software Availability:** Nov-2022

### Compiler Version Notes (Continued)

Fortran | 548.exchange2\_r(base, peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on  
LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

### Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

### Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

### Base Optimization Flags

C benchmarks:

-m64 -fltto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math  
-fstruct-layout=7 -mllvm -unroll-threshold=50

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## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3  
-march=znver4 -fveclib=AMDLIBM -ffast-math  
-mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt  
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang  
-lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions  
-mllvm -optimize-strided-mem-cost -floop-transform  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm  
-lflang -lamdalloc
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

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## Supermicro

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**Tested by:** Supermicro

**Test Date:** Oct-2022  
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**Software Availability:** Nov-2022

## Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: basepeak = yes
```

```
502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

```
505.mcf_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc
```

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## Peak Optimization Flags (Continued)

525.x264\_r: basepeak = yes

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

```
523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdalloc-ext
```

531.deepsjeng\_r: basepeak = yes

```
541.leela_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

```
502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v118/aocc4/b1/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

C++ benchmarks (except as noted below):

-Wno-unused-command-line-argument

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## Peak Other Flags (Continued)

523.xalancbmk\_r: -L/usr/lib32 -Wno-unused-command-line-argument  
-L/home/work/cpu2017/v118/aocc4/bl/rate/amd\_rate\_aocc400\_genoa\_B\_lib/lib32

Fortran benchmarks:  
-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>  
<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>  
<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Genoa-revB.xml>

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