



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

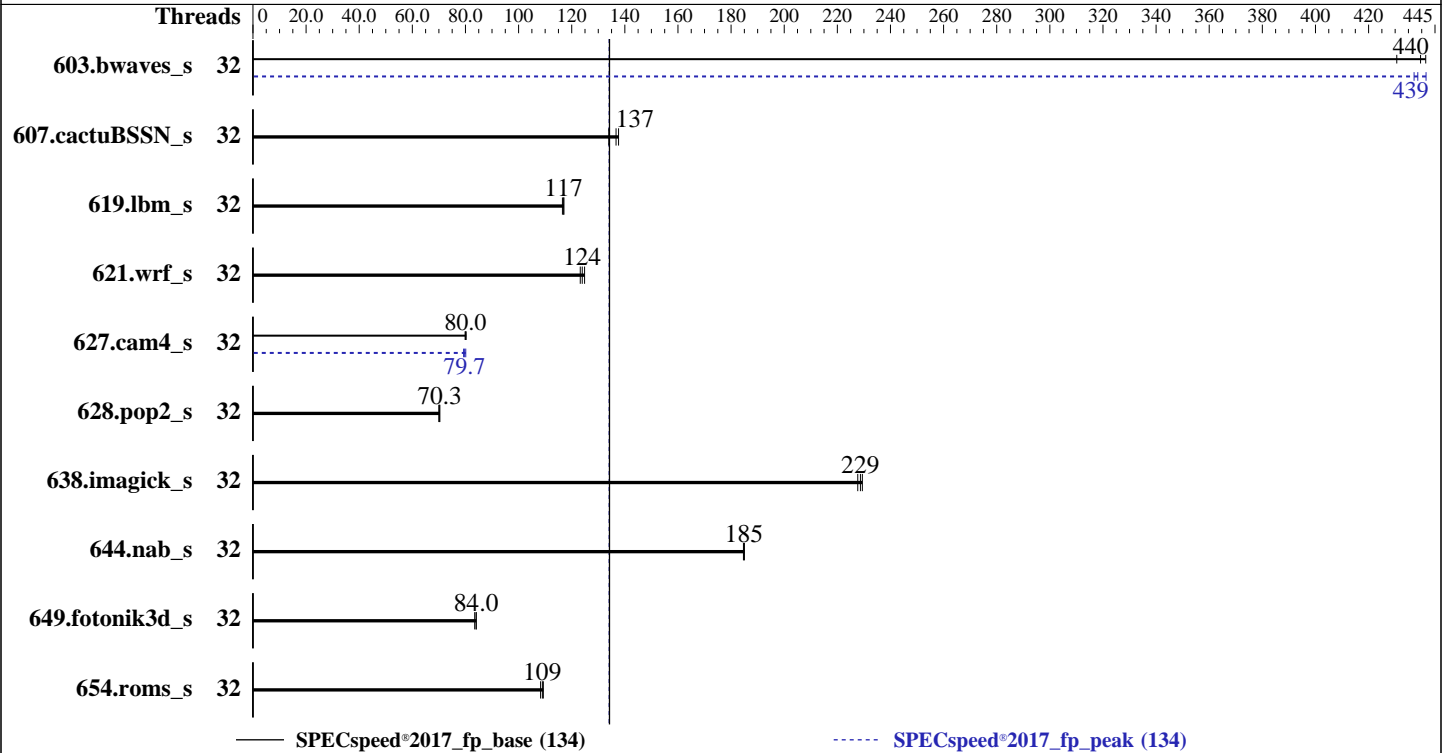
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022



Hardware

CPU Name: Intel Xeon Silver 4309Y
 Max MHz: 3600
 Nominal: 2800
 Enabled: 16 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 12 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)
 Storage: 1 x 1 TB SATA SSD
 Other: None

Software

OS: Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.25.1.el8_4.x86_64
 Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Version 0802 released Apr-2022
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECSpeed®2017_fp_base = 134

SPECSpeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	137	431	<u>134</u>	<u>440</u>	134	442	32	134	442	135	437	<u>135</u>	<u>439</u>
607.cactuBSSN_s	32	124	134	121	138	<u>122</u>	<u>137</u>	32	124	134	121	138	<u>122</u>	<u>137</u>
619.lbm_s	32	45.0	117	<u>44.8</u>	<u>117</u>	44.8	117	32	45.0	117	<u>44.8</u>	<u>117</u>	44.8	117
621.wrf_s	32	106	125	<u>107</u>	<u>124</u>	107	123	32	106	125	<u>107</u>	<u>124</u>	107	123
627.cam4_s	32	<u>111</u>	<u>80.0</u>	110	80.3	111	79.9	32	111	80.1	<u>111</u>	<u>79.7</u>	112	79.3
628.pop2_s	32	170	70.0	169	70.3	<u>169</u>	<u>70.3</u>	32	170	70.0	169	70.3	<u>169</u>	<u>70.3</u>
638.imagick_s	32	<u>63.1</u>	<u>229</u>	62.9	229	63.4	228	32	<u>63.1</u>	<u>229</u>	62.9	229	63.4	228
644.nab_s	32	94.6	185	<u>94.5</u>	<u>185</u>	94.4	185	32	94.6	185	<u>94.5</u>	<u>185</u>	94.4	185
649.fotonik3d_s	32	109	83.5	<u>108</u>	<u>84.0</u>	108	84.0	32	109	83.5	<u>108</u>	<u>84.0</u>	108	84.0
654.roms_s	32	145	108	<u>145</u>	<u>109</u>	144	109	32	145	108	<u>145</u>	<u>109</u>	144	109

SPECSpeed®2017_fp_base = **134**

SPECSpeed®2017_fp_peak = **134**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

OS set to performance mode via cpupower frequency-set -g performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/ic22ul/lib/intel64:/home/ic22ul/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:

VT-d = Disabled

Patrol Scrub = Disabled

Engine Boost = Aggressive

SR-IOV Support = Disabled

BMC Configuration:

Fan mode = Full speed mode

Sysinfo program /home/ic22u1/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Fri Aug 26 08:20:03 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7

physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 32

On-line CPU(s) list: 0-31

Thread(s) per core: 2

Core(s) per socket: 8

Socket(s): 2

NUMA node(s): 2

Vendor ID: GenuineIntel

BIOS Vendor ID: Intel

CPU family: 6

Model: 106

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Platform Notes (Continued)

```

Model name: Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
BIOS Model name: Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
Stepping: 6
CPU MHz: 801.172
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 12288K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window
hwp_epp hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 12288 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 515668 MB
node 0 free: 514594 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 516052 MB
node 1 free: 508720 MB
node distances:
node 0 1
0: 10 20
1: 20 10

```

From /proc/meminfo

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECSpeed®2017_fp_base = 134

SPECSpeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Platform Notes (Continued)

MemTotal: 1056482336 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux"

VERSION="8.4 (Ootpa)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="8.4"

PLATFORM_ID="platform:el8"

PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"

ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:

Linux localhost.localdomain 4.18.0-305.25.1.el8_4.x86_64 #1 SMP Mon Oct 18 14:34:11
EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

Not affected

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store
Bypass disabled via prctl and
seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swaps
barriers and __user pointer
sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):

Not affected

CVE-2019-11135 (TSX Asynchronous Abort):

Not affected

run-level 3 Aug 26 04:22

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Platform Notes (Continued)

SPEC is set to: /home/ic22u1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	878G	125G	754G	15%	/home

From /sys/devices/virtual/dmi/id

Vendor: ASUSTeK COMPUTER INC.

Product: RS720-E10-RS12

Product Family: Server

Serial: 012345678901

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666

BIOS:

BIOS Vendor: American Megatrends Inc.

BIOS Version: 0802

BIOS Date: 04/29/2022

BIOS Revision: 8.2

(End of data from sysinfo program)

Compiler Version Notes

```

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
  | 644.nab_s(base, peak)
=====

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----

```

```

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

```

```

-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Compiler Version Notes (Continued)

2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
| 628.pop2_s(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Base Portability Flags (Continued)

```

619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

Fortran benchmarks:

```

-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

Benchmarks using both Fortran and C:

```

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

Benchmarks using Fortran, C, and C++:

```

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECspeed®2017_fp_base = 134

SPECspeed®2017_fp_peak = 134

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2022

Hardware Availability: Apr-2022

Software Availability: May-2022

Peak Optimization Flags (Continued)

627.cam4_s (continued):

`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.html>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.2022-10-12.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.xml>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.2022-10-12.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-08-26 08:20:02-0400.

Report generated on 2022-10-12 17:01:14 by CPU2017 PDF formatter v6442.

Originally published on 2022-10-11.