# SPEC CPU®2017 Integer Rate Result

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## Inspur Corporation

### Inspur I24LM6 (Intel Xeon Platinum 8360Y)

$SPECrate^{®}2017\_int\_base = 478$

$SPECrate^{®}2017\_int\_peak = 498$
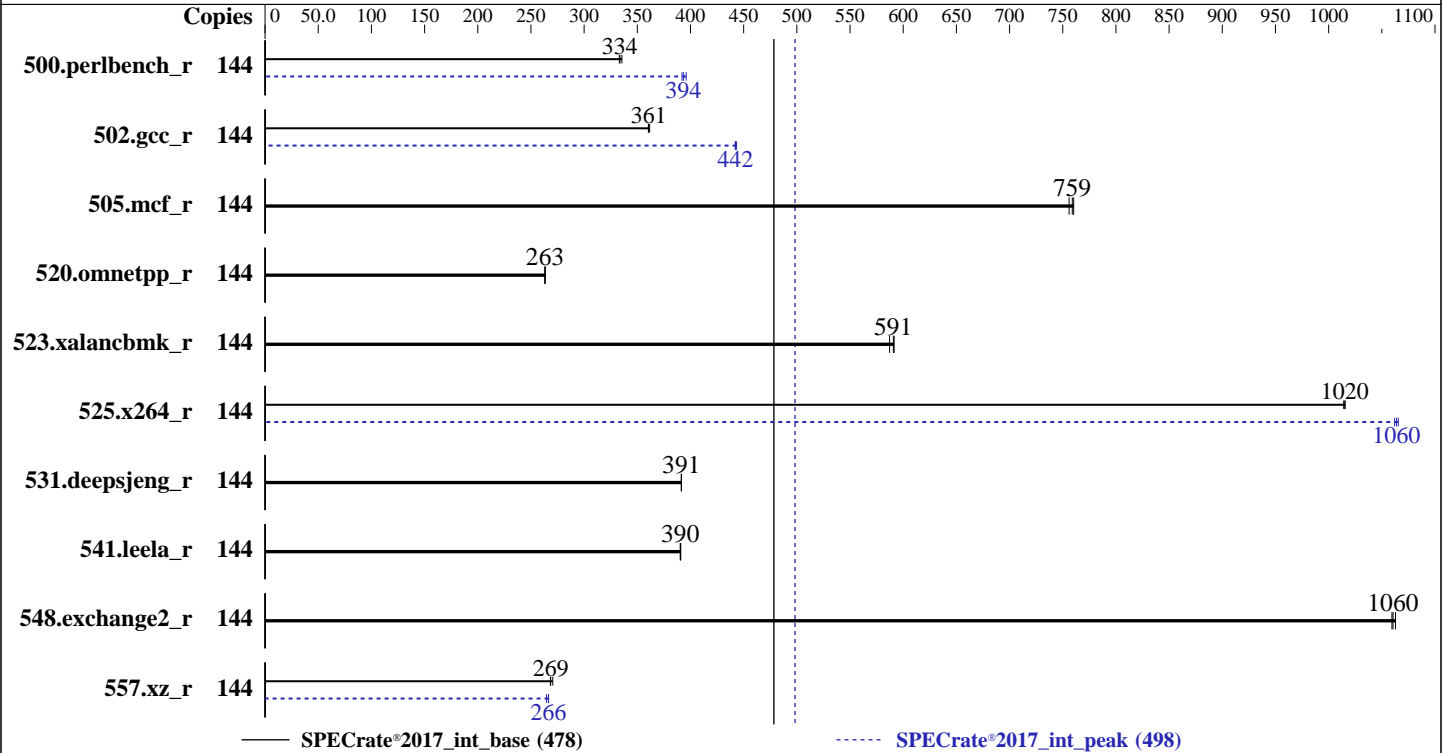
**CPU2017 License:** 3358

**Test Sponsor:** Inspur Corporation

**Tested by:** Inspur Corporation

**Test Date:** Feb-2022

**Hardware Availability:** May-2021

**Software Availability:** Nov-2021

| Benchmark | Copies | base | peak |
|---|---|---|---|
| 500.perlbench_r | 144 | 334 | 394 |
| 502.gcc_r | 144 | 361 | 442 |
| 505.mcf_r | 144 | 759 | |
| 520.omnetpp_r | 144 | 263 | |
| 523.xalancbmk_r | 144 | 591 | |
| 525.x264_r | 144 | 1020 | 1060 |
| 531.deepsjeng_r | 144 | 391 | |
| 541.leela_r | 144 | 390 | |
| 548.exchange2_r | 144 | 1060 | |
| 557.xz_r | 144 | 269 | 266 |

SPECrate®2017_int_base (478)   SPECrate®2017_int_peak (498)

## Hardware

CPU Name: Intel Xeon Platinum 8360Y

Max MHz: 3500

Nominal: 2400

Enabled: 72 cores, 2 chips, 2 threads/core

Orderable: 1,2 chips

Cache L1: 32 KB I + 48 KB D on chip per core

L2: 1.25 MB I+D on chip per core

L3: 54 MB I+D on chip per chip

Other: None

Memory: 256 GB (16 x 16 GB 2Rx8 PC4-3200AA-R)

Storage: 1 x 480 GB SATA SSD

Other: None

## Software

OS: Red Hat Enterprise Linux release 8.5 (Ootpa) 4.18.0-348.el8.x86_64

Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux

Parallel: No

Firmware: Version 05.01.02 released Aug-2021

File System: xfs

System State: Run level 3 (multi-user)

Base Pointers: 64-bit

Peak Pointers: 32/64-bit

Other: jemalloc memory allocator V5.0.1

Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 144 | 688 | 333 | 683 | 336 | **687** | **334** | 144 | 579 | 396 | 585 | 392 | **582** | **394** |
| 502.gcc_r | 144 | **565** | **361** | 564 | 362 | 566 | 360 | 144 | 460 | 443 | **461** | **442** | 461 | 442 |
| 505.mcf_r | 144 | 306 | 760 | **307** | **759** | 308 | 756 | 144 | 306 | 760 | **307** | **759** | 308 | 756 |
| 520.omnetpp_r | 144 | 717 | 263 | 718 | 263 | **718** | **263** | 144 | 717 | 263 | 718 | 263 | **718** | **263** |
| 523.xalancbmk_r | 144 | 259 | 587 | 257 | 591 | **257** | **591** | 144 | 259 | 587 | 257 | 591 | **257** | **591** |
| 525.x264_r | 144 | **248** | **1020** | 249 | 1010 | 248 | 1020 | 144 | 237 | 1070 | 237 | 1060 | **237** | **1060** |
| 531.deepsjeng_r | 144 | **422** | **391** | 422 | 391 | 421 | 392 | 144 | **422** | **391** | 422 | 391 | 421 | 392 |
| 541.leela_r | 144 | 610 | 391 | 611 | 390 | **611** | **390** | 144 | 610 | 391 | 611 | 390 | **611** | **390** |
| 548.exchange2_r | 144 | **356** | **1060** | 356 | 1060 | 355 | 1060 | 144 | **356** | **1060** | 356 | 1060 | 355 | 1060 |
| 557.xz_r | 144 | **579** | **269** | 575 | 270 | 580 | 268 | 144 | **584** | **266** | 583 | 267 | 588 | 265 |
| **SPECrate®2017_int_base =** | **478** | | | | | | | | | | | | | |
| **SPECrate®2017_int_peak =** | **498** | | | | | | | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
 The numactl mechanism was used to bind copies to processors. The config file option 'submit'
 was used to generate numactl commands to bind each copy to a specific processor.
 For details, please see the config file.
```

## Operating System Notes

```
 Stack size set to unlimited using "ulimit -s unlimited"
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-
     32"
MALLOC_CONF = "retain:true"
```

## General Notes

```
 Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
 memory using Red Hat Enterprise Linux 8.1
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>        /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
```

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## General Notes (Continued)

```
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or
https://github.com/jemalloc/jemalloc/releases
```

## Platform Notes

```
BIOS and OS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Sub NUMA Cluster (SNC) set to enable
Scaling_Governor set to Performance

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Wed Feb 23 14:28:28 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8360Y CPU @ 2.40GHz
      2  "physical id"s (chips)
      144 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 36
      siblings  : 72
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35

From lscpu from util-linux 2.32.1:
```

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## Platform Notes (Continued)

```
        Architecture:          x86_64
        CPU op-mode(s):        32-bit, 64-bit
        Byte Order:            Little Endian
        CPU(s):                144
        On-line CPU(s) list:   0-143
        Thread(s) per core:    2
        Core(s) per socket:    36
        Socket(s):             2
        NUMA node(s):          8
        Vendor ID:             GenuineIntel
        BIOS Vendor ID:        Intel(R) Corporation
        CPU family:            6
        Model:                 106
        Model name:            Intel(R) Xeon(R) Platinum 8360Y CPU @ 2.40GHz
        BIOS Model name:       Intel(R) Xeon(R) Platinum 8360Y CPU @ 2.40GHz
        Stepping:              6
        CPU MHz:               3500.000
        CPU max MHz:           3500.0000
        CPU min MHz:           800.0000
        BogoMIPS:              4800.00
        Virtualization:        VT-x
        L1d cache:             48K
        L1i cache:             32K
        L2 cache:              1280K
        L3 cache:              55296K
        NUMA node0 CPU(s):     0-8,72-80
        NUMA node1 CPU(s):     9-17,81-89
        NUMA node2 CPU(s):     18-26,90-98
        NUMA node3 CPU(s):     27-35,99-107
        NUMA node4 CPU(s):     36-44,108-116
        NUMA node5 CPU(s):     45-53,117-125
        NUMA node6 CPU(s):     54-62,126-134
        NUMA node7 CPU(s):     63-71,135-143
        Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
        pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
        aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
        pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
        rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_ppin
        ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust sgx bmi1 hle avx2 smep
        bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
        cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm
        ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid sgx_lc fsrm md_clear
        pconfig flush_l1d arch_capabilities
```

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## Platform Notes (Continued)

```
/proc/cpuinfo cache data
   cache size : 55296 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 72 73 74 75 76 77 78 79 80
  node 0 size: 32250 MB
  node 0 free: 27051 MB
  node 1 cpus: 9 10 11 12 13 14 15 16 17 81 82 83 84 85 86 87 88 89
  node 1 size: 31845 MB
  node 1 free: 28336 MB
  node 2 cpus: 18 19 20 21 22 23 24 25 26 90 91 92 93 94 95 96 97 98
  node 2 size: 32252 MB
  node 2 free: 28685 MB
  node 3 cpus: 27 28 29 30 31 32 33 34 35 99 100 101 102 103 104 105 106 107
  node 3 size: 32252 MB
  node 3 free: 28559 MB
  node 4 cpus: 36 37 38 39 40 41 42 43 44 108 109 110 111 112 113 114 115 116
  node 4 size: 32252 MB
  node 4 free: 28722 MB
  node 5 cpus: 45 46 47 48 49 50 51 52 53 117 118 119 120 121 122 123 124 125
  node 5 size: 32252 MB
  node 5 free: 28781 MB
  node 6 cpus: 54 55 56 57 58 59 60 61 62 126 127 128 129 130 131 132 133 134
  node 6 size: 32214 MB
  node 6 free: 28737 MB
  node 7 cpus: 63 64 65 66 67 68 69 70 71 135 136 137 138 139 140 141 142 143
  node 7 size: 32247 MB
  node 7 free: 28763 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  20  20  20  20  20  20  20
    1:  20  10  20  20  20  20  20  20
    2:  20  20  10  20  20  20  20  20
    3:  20  20  20  10  20  20  20  20
    4:  20  20  20  20  10  20  20  20
    5:  20  20  20  20  20  10  20  20
    6:  20  20  20  20  20  20  10  20
    7:  20  20  20  20  20  20  20  10

From /proc/meminfo
   MemTotal:       263748840 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/sbin/tuned-adm active
```

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## Platform Notes (Continued)

```
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.5 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.5"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

 uname -a:
    Linux localhost.localdomain 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 CVE-2018-12207 (iTLB Multihit):                   Not affected
 CVE-2018-3620 (L1 Terminal Fault):                Not affected
 Microarchitectural Data Sampling:                 Not affected
 CVE-2017-5754 (Meltdown):                         Not affected
 CVE-2018-3639 (Speculative Store Bypass):         Mitigation: Speculative Store
                                                   Bypass disabled via prctl and
                                                   seccomp
 CVE-2017-5753 (Spectre variant 1):                Mitigation: usercopy/swapgs
                                                   barriers and __user pointer
                                                   sanitization
 CVE-2017-5715 (Spectre variant 2):                Mitigation: Enhanced IBRS, IBPB:
                                                   conditional, RSB filling
 CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
 CVE-2019-11135 (TSX Asynchronous Abort):          Not affected

 run-level 3 Feb 22 16:40

 SPEC is set to: /home/CPU2017
    Filesystem            Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   372G   81G  291G  22% /home

 From /sys/devices/virtual/dmi/id
```

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## Platform Notes (Continued)

```
        Vendor:         Inspur
        Product:        NS5160LM6
        Product Family: Family
        Serial:         221484372

 Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
 interpret this section. The 'dmidecode' program reads system data which is "intended to
 allow hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     16x Samsung M393A2K43DB3-CWE 16 GB 2 rank 3200

 BIOS:
    BIOS Vendor:        American Megatrends Inc.
    BIOS Version:       05.01.02
    BIOS Date:          08/13/2021
    BIOS Revision:      5.22

 (End of data from sysinfo program)
```

## Compiler Version Notes

```
==============================================================================
C        | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
  2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

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# Compiler Version Notes (Continued)

```
==============================================================================
C        | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
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------------------------------------------------------------------------------


==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
  2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(peak) 557.xz_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version
  2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C        | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
```

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## Compiler Version Notes (Continued)

```
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
  Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifort`

## Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`

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## Base Portability Flags (Continued)

557.xz_r: `-DSPEC_LP64`

## Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icx`

500.perlbench_r: `icc`

557.xz_r: `icc`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifort`

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**Software Availability:** Nov-2021

# Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

# Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-Wl,-z,muldefs` -prof-gen(pass 1) -prof-use(pass 2)
`-xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4 -fno-strict-overflow`
`-mbranches-within-32B-boundaries`
`-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
`-lqkmalloc`

502.gcc_r: `-m32`
`-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin`
`-std=gnu89 -Wl,-z,muldefs` -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) `-xCORE-AVX512 -flto`
-Ofast(pass 1) `-O3 -ffast-math -qopt-mem-layout-trans=4`
`-mbranches-within-32B-boundaries`
`-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc`

505.mcf_r: basepeak = yes

525.x264_r: `-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto`
`-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias`
`-mbranches-within-32B-boundaries`
`-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
`-lqkmalloc`

557.xz_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries`
`-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
`-lqkmalloc`

## Inspur Corporation

Inspur I24LM6 (Intel Xeon Platinum 8360Y)

SPECrate®2017_int_base = 478

SPECrate®2017_int_peak = 498

**CPU2017 License:** 3358
**Test Sponsor:** Inspur Corporation
**Tested by:** Inspur Corporation

**Test Date:** Feb-2022
**Hardware Availability:** May-2021
**Software Availability:** Nov-2021

# Peak Optimization Flags (Continued)

C++ benchmarks:

520.omnetpp_r: `basepeak = yes`

523.xalancbmk_r: `basepeak = yes`

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html`
`http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.4.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml`
`http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.4.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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