



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

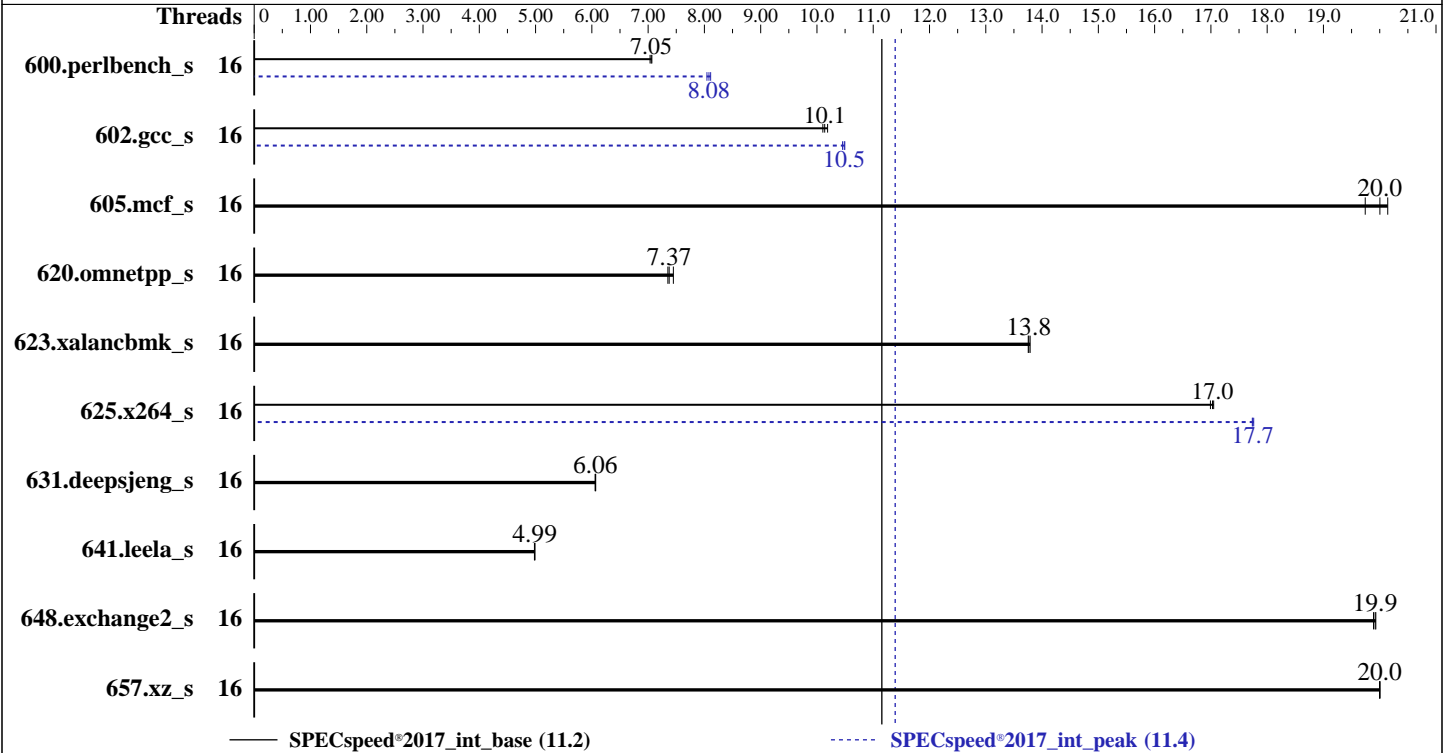
Test Date: Nov-2021

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2020

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Jan-2021



## Hardware

CPU Name: Intel Xeon Gold 5315Y  
 Max MHz: 3600  
 Nominal: 3200  
 Enabled: 16 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 12 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200V-R, running at 2933)  
 Storage: 1 x 600GB 10000RPM SAS HDD  
 Other: None

## Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
 Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
 C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
 Parallel: Yes  
 Firmware: Version 5.34 released Sep-2021 BIOS  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2021

Hardware Availability: Sep-2020

Software Availability: Jan-2021

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	<b><u>252</u></b>	<b><u>7.05</u></b>	252	7.03	251	7.07	16	219	8.11	221	8.05	<b><u>220</u></b>	<b><u>8.08</u></b>
602.gcc_s	16	394	10.1	<b><u>393</u></b>	<b><u>10.1</u></b>	391	10.2	16	381	10.5	<b><u>380</u></b>	<b><u>10.5</u></b>	379	10.5
605.mcf_s	16	239	19.7	234	20.1	<b><u>236</u></b>	<b><u>20.0</u></b>	16	239	19.7	234	20.1	<b><u>236</u></b>	<b><u>20.0</u></b>
620.omnetpp_s	16	219	7.45	222	7.35	<b><u>221</u></b>	<b><u>7.37</u></b>	16	219	7.45	222	7.35	<b><u>221</u></b>	<b><u>7.37</u></b>
623.xalancbmk_s	16	103	13.8	103	13.8	<b><u>103</u></b>	<b><u>13.8</u></b>	16	103	13.8	103	13.8	<b><u>103</u></b>	<b><u>13.8</u></b>
625.x264_s	16	103	17.0	104	17.0	<b><u>104</u></b>	<b><u>17.0</u></b>	16	99.5	17.7	<b><u>99.4</u></b>	<b><u>17.7</u></b>	99.3	17.8
631.deepsjeng_s	16	<b><u>236</u></b>	<b><u>6.06</u></b>	237	6.06	236	6.07	16	<b><u>236</u></b>	<b><u>6.06</u></b>	237	6.06	236	6.07
641.leela_s	16	342	4.99	343	4.98	<b><u>342</u></b>	<b><u>4.99</u></b>	16	342	4.99	343	4.98	<b><u>342</u></b>	<b><u>4.99</u></b>
648.exchange2_s	16	148	19.9	148	19.9	<b><u>148</u></b>	<b><u>19.9</u></b>	16	148	19.9	148	19.9	<b><u>148</u></b>	<b><u>19.9</u></b>
657.xz_s	16	309	20.0	309	20.0	<b><u>309</u></b>	<b><u>20.0</u></b>	16	309	20.0	309	20.0	<b><u>309</u></b>	<b><u>20.0</u></b>

SPECspeed®2017\_int\_base = **11.2**

SPECspeed®2017\_int\_peak = **11.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

```
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2021

Hardware Availability: Sep-2020

Software Availability: Jan-2021

## General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Set Hyper-Threading to Disabled  
Set Power Performance Tuning to BIOS Controls EPB  
Set Energy Performance BIAS to Performance  
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on localhost.localdomain Thu Nov 11 15:13:37 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz  
2 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 8  
physical 0: cores 0 1 2 3 4 5 6 7  
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 16  
On-line CPU(s) list: 0-15  
Thread(s) per core: 1  
Core(s) per socket: 8  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 106  
Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz  
Stepping: 6  
CPU MHz: 2753.258  
CPU max MHz: 3600.0000

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Nov-2021  
**Hardware Availability:** Sep-2020  
**Software Availability:** Jan-2021

## Platform Notes (Continued)

```

CPU min MHz:      800.0000
BogoMIPS:         6400.00
Virtualization:   VT-x
L1d cache:        48K
L1i cache:        32K
L2 cache:         1280K
L3 cache:         12288K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
Flags:            fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d
arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 12288 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 515242 MB
node 0 free: 514344 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 516091 MB
node 1 free: 515179 MB
node distances:
node  0  1
  0:  10  20
  1:  20  10

```

```

From /proc/meminfo
MemTotal:      1056085548 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/sbin/tuned-adm active

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2021

Hardware Availability: Sep-2020

Software Availability: Jan-2021

## Platform Notes (Continued)

Current active profile: throughput-performance

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux"

VERSION="8.2 (Ootpa)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="8.2"

PLATFORM\_ID="platform:el8"

PRETTY\_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"

ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.2:ga

uname -a:

Linux localhost.localdomain 4.18.0-193.el8.x86\_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020 x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	No status reported
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 3 Nov 11 15:12

SPEC is set to: /home/speccpu

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	504G	49G	455G	10%	/home

From /sys/devices/virtual/dmi/id

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Nov-2021  
**Hardware Availability:** Sep-2020  
**Software Availability:** Jan-2021

## Platform Notes (Continued)

Product Family: Rack

Additional information from dmidecode 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Hynix HMAA8GR7AJR4N-XN 64 GB 2 rank 3200, configured at 2933  
16x NO DIMM NO DIMM

BIOS:

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 5.34  
BIOS Date: 09/11/2021  
BIOS Revision: 5.22

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
625.x264\_s(base, peak) 657.xz\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----  
=====

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Nov-2021  
**Hardware Availability:** Sep-2020  
**Software Availability:** Jan-2021

## Compiler Version Notes (Continued)

C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2021

Hardware Availability: Sep-2020

Software Availability: Jan-2021

## Base Portability Flags (Continued)

625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-DSPEC\_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512  
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-DSPEC\_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64\_lin/  
-lqkmalloc

Fortran benchmarks:

-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries

## Peak Compiler Invocation

C benchmarks (except as noted below):

icx

600.perlbench\_s: icc

C++ benchmarks:

icpx

Fortran benchmarks:

ifort





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2021

Hardware Availability: Sep-2020

Software Availability: Jan-2021

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

```
641.leela_s: basepeak = yes
```

Fortran benchmarks:

```
648.exchange2_s: basepeak = yes
```



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 11.2

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

SPECspeed®2017\_int\_peak = 11.4

**CPU2017 License:** 9066

**Test Date:** Nov-2021

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Hardware Availability:** Sep-2020

**Tested by:** New H3C Technologies Co., Ltd.

**Software Availability:** Jan-2021

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevD.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevD.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevD.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevD.xml)

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2021-11-11 02:13:37-0500.

Report generated on 2021-12-07 16:57:06 by CPU2017 PDF formatter v6442.

Originally published on 2021-12-07.