



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

### H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

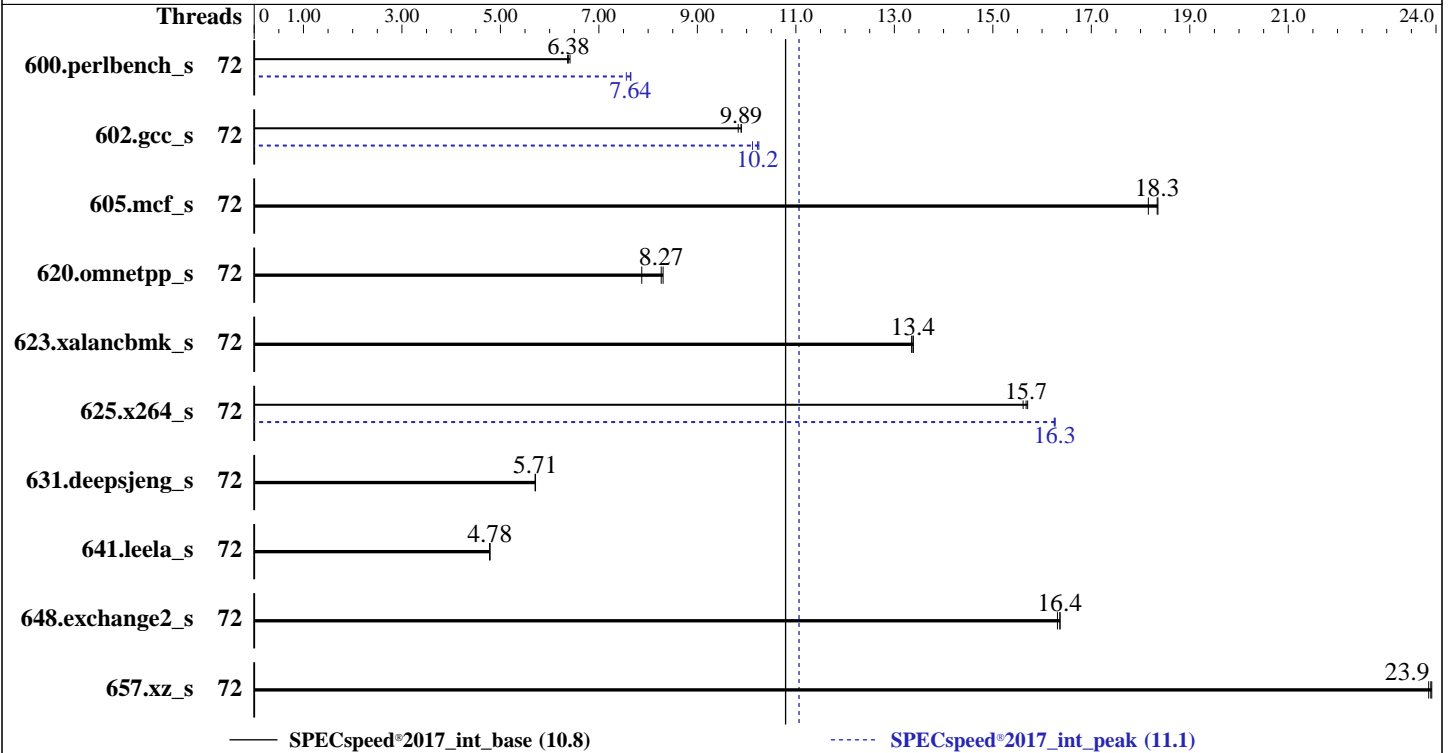
Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020



### Hardware

CPU Name: Intel Xeon Gold 5318H  
 Max MHz: 3800  
 Nominal: 2500  
 Enabled: 72 cores, 4 chips  
 Orderable: 1,2,3,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-3200V-R, running at 2666)  
 Storage: 1 x 1.0 TB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux;  
 Parallel: Yes  
 Firmware: Version 5.14 released Feb-2021 BIOS  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	72	279	6.36	<b><u>278</u></b>	<b><u>6.38</u></b>	277	6.41	72	<b><u>232</u></b>	<b><u>7.64</u></b>	232	7.65	235	7.56
602.gcc_s	72	403	9.89	<b><u>403</u></b>	<b><u>9.89</u></b>	405	9.83	72	389	10.2	<b><u>390</u></b>	<b><u>10.2</u></b>	394	10.1
605.mcf_s	72	260	18.2	<b><u>257</u></b>	<b><u>18.3</u></b>	257	18.4	72	260	18.2	<b><u>257</u></b>	<b><u>18.3</u></b>	257	18.4
620.omnetpp_s	72	<b><u>197</u></b>	<b><u>8.27</u></b>	196	8.30	207	7.87	72	<b><u>197</u></b>	<b><u>8.27</u></b>	196	8.30	207	7.87
623.xalancbmk_s	72	106	13.4	106	13.4	<b><u>106</u></b>	<b><u>13.4</u></b>	72	106	13.4	106	13.4	<b><u>106</u></b>	<b><u>13.4</u></b>
625.x264_s	72	113	15.6	112	15.7	<b><u>113</u></b>	<b><u>15.7</u></b>	72	109	16.2	<b><u>109</u></b>	<b><u>16.3</u></b>	108	16.3
631.deepsjeng_s	72	251	5.71	<b><u>251</u></b>	<b><u>5.71</u></b>	251	5.71	72	251	5.71	<b><u>251</u></b>	<b><u>5.71</u></b>	251	5.71
641.leela_s	72	356	4.79	<b><u>357</u></b>	<b><u>4.78</u></b>	357	4.78	72	356	4.79	<b><u>357</u></b>	<b><u>4.78</u></b>	357	4.78
648.exchange2_s	72	<b><u>180</u></b>	<b><u>16.4</u></b>	180	16.3	180	16.4	72	<b><u>180</u></b>	<b><u>16.4</u></b>	180	16.3	180	16.4
657.xz_s	72	<b><u>259</u></b>	<b><u>23.9</u></b>	259	23.9	259	23.9	72	<b><u>259</u></b>	<b><u>23.9</u></b>	259	23.9	259	23.9

SPECspeed®2017\_int\_base = **10.8**

SPECspeed®2017\_int\_peak = **11.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

```
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Date: Mar-2021

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2020

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2020

## General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Set Hyper-Threading to Disabled  
Set Power Performance Tuning to BIOS Controls EPB  
Set Energy Performance BIAS to Performance  
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on localhost.localdomain Thu Mar 18 20:22:47 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5318H CPU @ 2.50GHz  
4 "physical id"s (chips)  
72 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 18  
siblings : 18  
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 72  
On-line CPU(s) list: 0-71  
Thread(s) per core: 1  
Core(s) per socket: 18  
Socket(s): 4  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 5318H CPU @ 2.50GHz  
Stepping: 11

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Date: Mar-2021

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2020

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2020

## Platform Notes (Continued)

```

CPU MHz: 3134.729
CPU max MHz: 3800.0000
CPU min MHz: 1000.0000
BogoMIPS: 5000.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-17
NUMA node1 CPU(s): 18-35
NUMA node2 CPU(s): 36-53
NUMA node3 CPU(s): 54-71
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx512_bf16 dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req pku ospke avx512_vnni md_clear flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17
node 0 size: 191857 MB
node 0 free: 191075 MB
node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
node 1 size: 193531 MB
node 1 free: 193051 MB
node 2 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53
node 2 size: 193504 MB
node 2 free: 193289 MB
node 3 cpus: 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
node 3 size: 193531 MB
node 3 free: 193299 MB
node distances:
node 0 1 2 3
0: 10 20 20 20

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020

## Platform Notes (Continued)

```

1: 20 10 20 20
2: 20 20 10 20
3: 20 20 20 10

```

From /proc/meminfo

```

MemTotal:      790962712 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/sbin/tuned-adm active

Current active profile: throughput-performance

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

From /etc/\*release\* /etc/\*version\*

os-release:

```

NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"

```

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.2:ga

uname -a:

```

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Date: Mar-2021

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2020

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2020

## Platform Notes (Continued)

CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported  
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

SPEC is set to: /home/speccpu

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	876G	251G	625G	29%	/home

From /sys/devices/virtual/dmi/id  
Product Family: SYSTEM\_FAMILY

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

48x Micron 18ASF2G72PDZ-3G2E1 16 GB 2 rank 3200, configured at 2666

BIOS:

BIOS Vendor:	American Megatrends International, LLC.
BIOS Version:	5.14
BIOS Date:	02/05/2021
BIOS Revision:	5.19

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 600.perlbench\_s(peak)

-----  
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----  
=====

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020

## Compiler Version Notes (Continued)

C | 600.perlbench\_s(peak)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 648.exchange2\_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icx

600.perlbench\_s: icc

C++ benchmarks:

icpx

(Continued on next page)





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Date: Mar-2021

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2020

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Dec-2020

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed®2017\_int\_base = 10.8

H3C UniServer R6900 G5 (Intel Xeon Gold 5318H)

SPECspeed®2017\_int\_peak = 11.1

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Mar-2021

Hardware Availability: Sep-2020

Software Availability: Dec-2020

## Peak Optimization Flags (Continued)

641.leela\_s:basepeak = yes

Fortran benchmarks:

648.exchange2\_s:basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevB.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-V1.0-CPX-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevB.xml)

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.5 on 2021-03-18 08:22:47-0400.

Report generated on 2021-04-14 14:12:56 by CPU2017 PDF formatter v6442.

Originally published on 2021-04-13.