



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

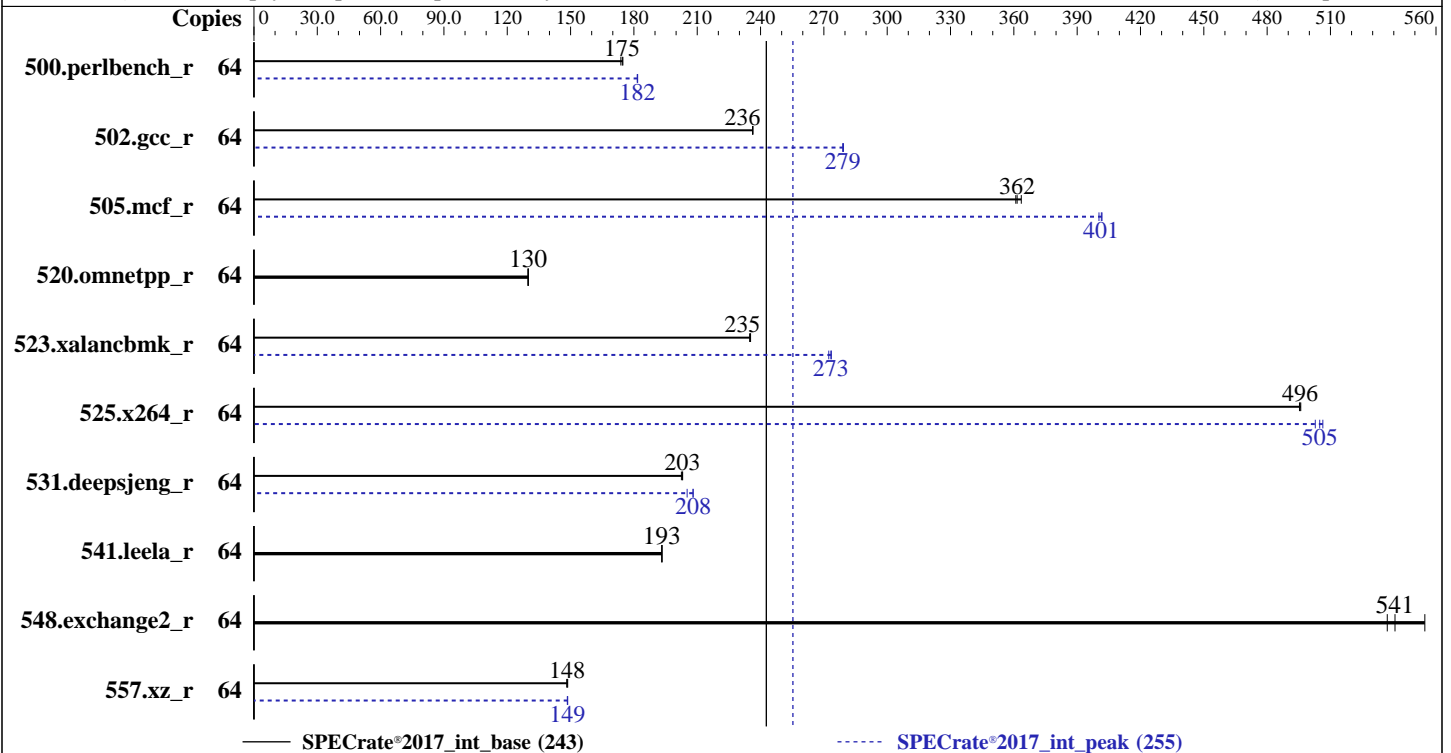
Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020



## Hardware

CPU Name: AMD EPYC 7302  
 Max MHz: 3300  
 Nominal: 3000  
 Enabled: 32 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 128 MB I+D on chip per chip, 16 MB shared / 2 cores  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200V-L)  
 Storage: 1 x 960 GB SSD SATA III  
 Other: None

## Software

OS: CentOS Linux 8.2  
 kernel version 4.18.0-193.19.1.el8\_2.x86\_64  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 3003 released Jul-2020  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.2.0  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	64	586	174	583	175	<b>584</b>	<b>175</b>	64	561	182	<b>561</b>	<b>182</b>	561	182
502.gcc_r	64	<b>384</b>	<b>236</b>	384	236	383	236	64	<b>325</b>	<b>279</b>	325	279	324	279
505.mcf_r	64	<b>286</b>	<b>362</b>	285	364	287	361	64	<b>258</b>	<b>401</b>	258	400	257	402
520.omnetpp_r	64	<b>646</b>	<b>130</b>	647	130	646	130	64	<b>646</b>	<b>130</b>	647	130	646	130
523.xalancbmk_r	64	288	235	<b>287</b>	<b>235</b>	287	235	64	247	274	<b>247</b>	<b>273</b>	248	272
525.x264_r	64	<b>226</b>	<b>496</b>	226	496	226	495	64	<b>222</b>	<b>505</b>	221	506	223	503
531.deepsjeng_r	64	<b>361</b>	<b>203</b>	361	203	362	203	64	<b>353</b>	<b>208</b>	352	208	357	205
541.leela_r	64	<b>548</b>	<b>193</b>	548	193	548	193	64	<b>548</b>	<b>193</b>	548	193	548	193
548.exchange2_r	64	<b>310</b>	<b>541</b>	312	537	302	555	64	<b>310</b>	<b>541</b>	312	537	302	555
557.xz_r	64	<b>466</b>	<b>148</b>	465	149	466	148	64	466	148	465	149	<b>465</b>	<b>149</b>

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were  
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Operating System Notes (Continued)

Transparent huge pages set to 'always' for this run (OS default)

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/CPU2017-1.1.0/amd_rate_aocc200_rome_C_lib/64:/home/CPU2017-1.1.0/  
    amd_rate_aocc200_rome_C_lib/32:"  
MALLOCONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -fltto  
jemalloc 5.2.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS settings:  
Power phase shedding = Disabled  
SVM Mode = Disabled  
SR-IOV support = Disabled  
DRAM Scrub time = Disabled  
NUMA nodes per socket = NPS4  
Determinism Slider = Power

Sysinfo program /home/CPU2017-1.1.0/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on Zyxel Tue Oct 20 06:48:57 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Platform Notes (Continued)

From /proc/cpuinfo

model name : AMD EPYC 7302 16-Core Processor

2 "physical id"s (chips)

64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 32

physical 0: cores 0 1 4 5 8 9 12 13 16 17 20 21 24 25 28 29

physical 1: cores 0 1 4 5 8 9 12 13 16 17 20 21 24 25 28 29

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 64

On-line CPU(s) list: 0-63

Thread(s) per core: 2

Core(s) per socket: 16

Socket(s): 2

NUMA node(s): 8

Vendor ID: AuthenticAMD

CPU family: 23

Model: 49

Model name: AMD EPYC 7302 16-Core Processor

Stepping: 0

CPU MHz: 2325.282

CPU max MHz: 3000.0000

CPU min MHz: 1500.0000

BogoMIPS: 6000.47

Virtualization: AMD-V

L1d cache: 32K

L1i cache: 32K

L2 cache: 512K

L3 cache: 16384K

NUMA node0 CPU(s): 0-3,32-35

NUMA node1 CPU(s): 4-7,36-39

NUMA node2 CPU(s): 8-11,40-43

NUMA node3 CPU(s): 12-15,44-47

NUMA node4 CPU(s): 16-19,48-51

NUMA node5 CPU(s): 20-23,52-55

NUMA node6 CPU(s): 24-27,56-59

NUMA node7 CPU(s): 28-31,60-63

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm

constant\_tsc rep\_good nopl nonstop\_tsc cpuid extd\_apicid aperfmperf pni pclmulqdq

monitor ssse3 fma cx16 sse4\_1 sse4\_2 movbe popcnt aes xsave avx f16c rdrand lahf\_lm

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Platform Notes (Continued)

```
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs
skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
cdp_l3 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep
bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd
arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor
smca
```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 32 33 34 35
node 0 size: 64063 MB
node 0 free: 63900 MB
node 1 cpus: 4 5 6 7 36 37 38 39
node 1 size: 64508 MB
node 1 free: 64219 MB
node 2 cpus: 8 9 10 11 40 41 42 43
node 2 size: 64481 MB
node 2 free: 64229 MB
node 3 cpus: 12 13 14 15 44 45 46 47
node 3 size: 64496 MB
node 3 free: 64336 MB
node 4 cpus: 16 17 18 19 48 49 50 51
node 4 size: 64508 MB
node 4 free: 64379 MB
node 5 cpus: 20 21 22 23 52 53 54 55
node 5 size: 64508 MB
node 5 free: 64399 MB
node 6 cpus: 24 25 26 27 56 57 58 59
node 6 size: 64508 MB
node 6 free: 64398 MB
node 7 cpus: 28 29 30 31 60 61 62 63
node 7 size: 64509 MB
node 7 free: 64400 MB
```

```
node distances:
node  0  1  2  3  4  5  6  7
  0:  10  12  12  12  32  32  32  32
  1:  12  10  12  12  32  32  32  32
  2:  12  12  10  12  32  32  32  32
  3:  12  12  12  10  32  32  32  32
  4:  32  32  32  32  10  12  12  12
  5:  32  32  32  32  12  10  12  12
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Platform Notes (Continued)

```
6: 32 32 32 32 12 12 10 12
7: 32 32 32 32 12 12 12 10
```

From /proc/meminfo

MemTotal: 527961084 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

centos-release: CentOS Linux release 8.2.2004 (Core)

centos-release-upstream: Derived from Red Hat Enterprise Linux 8.2 (Source)

os-release:

NAME="CentOS Linux"

VERSION="8 (Core)"

ID="centos"

ID\_LIKE="rhel fedora"

VERSION\_ID="8"

PLATFORM\_ID="platform:el8"

PRETTY\_NAME="CentOS Linux 8 (Core)"

ANSI\_COLOR="0;31"

redhat-release: CentOS Linux release 8.2.2004 (Core)

system-release: CentOS Linux release 8.2.2004 (Core)

system-release-cpe: cpe:/o:centos:centos:8

uname -a:

Linux Zyxel 4.18.0-193.19.1.el8\_2.x86\_64 #1 SMP Mon Sep 14 14:37:00 UTC 2020 x86\_64  
x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

```
itlb_multihit: Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB:
conditional, IBRS_FW, STIBP: conditional, RSB
filling
srbds: Not affected
tsx_async_abort: Not affected
```

run-level 3 Oct 20 05:15

SPEC is set to: /home/CPU2017-1.1.0

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/cl_zyxel-home	xfs	839G	9.7G	829G	2%	/home

```

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 3003 07/10/2020
Vendor: ASUSTeK COMPUTER INC.
Product: KNPP-D32-R Series
Product Family: Server
Serial: System Serial Number

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
  16x Micron Technology 36ASF4G72PZ-3G2E2 32 kB 2 rank 3200
  16x Unknown Unknown

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C      | 502.gcc_r(peak)
-----

```

```

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base, peak)
-----

```

```

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C      | 502.gcc_r(peak)
-----

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Compiler Version Notes (Continued)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base)  
| 531.deepsjeng\_r(base, peak) 541.leela\_r(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++ | 523.xalancbmk\_r(peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Compiler Version Notes (Continued)

```

=====
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0.B191.2019_07_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
Fortran  | 548.exchange2_r(base, peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0.B191.2019_07_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

## Base Portability Flags

```

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Base Portability Flags (Continued)

541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50  
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc  
-lflang
```

C++ benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC  
-mllvm -unroll-threshold=100 -flv-function-specialization  
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm  
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops  
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs  
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=150 -lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Compiler Invocation

C benchmarks:

clang

(Continued on next page)



# SPEC CPU<sup>®</sup>2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate<sup>®</sup>2017\_int\_base = 243

SPECrate<sup>®</sup>2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang

502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Peak Optimization Flags (Continued)

502.gcc\_r (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -fgnu89-inline -ljemalloc
```

505.mcf\_r: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang
```

525.x264\_r: Same as 500.perlbench\_r

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -ljemalloc
```

531.deepsjeng\_r: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa  
eterio 217 RZ1 (AMD Epyc 7302, 3.0 GHz)  
AMD EPYC 7302

SPECrate®2017\_int\_base = 243

SPECrate®2017\_int\_peak = 255

CPU2017 License: 9081

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Test Date: Oct-2020

Hardware Availability: Aug-2019

Software Availability: Sep-2020

## Peak Optimization Flags (Continued)

531.deepsjeng\_r (continued):

```
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -flv-function-specialization  
-mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -vector-library=LIBMVEC  
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc  
-lflang
```

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

502.gcc\_r: -L/sppo/dev/cpu2017/v110/amd\_rate\_aocc200\_rome\_C\_lib/32

C++ benchmarks:

523.xalancbmk\_r: -L/sppo/dev/cpu2017/v110/amd\_rate\_aocc200\_rome\_C\_lib/32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.html>

[http://www.spec.org/cpu2017/flags/Epsilon\\_platform\\_amd\\_RZ1\\_rome.html](http://www.spec.org/cpu2017/flags/Epsilon_platform_amd_RZ1_rome.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.xml>

[http://www.spec.org/cpu2017/flags/Epsilon\\_platform\\_amd\\_RZ1\\_rome.xml](http://www.spec.org/cpu2017/flags/Epsilon_platform_amd_RZ1_rome.xml)

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2020-10-20 06:48:57-0400.

Report generated on 2020-11-10 15:20:42 by CPU2017 PDF formatter v6255.

Originally published on 2020-11-10.