



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_int\_base = 8.28

PowerEdge R640 (Intel Xeon Silver 4214R, 2.40 GHz)

SPECspeed®2017\_int\_peak = 8.39

CPU2017 License: 55

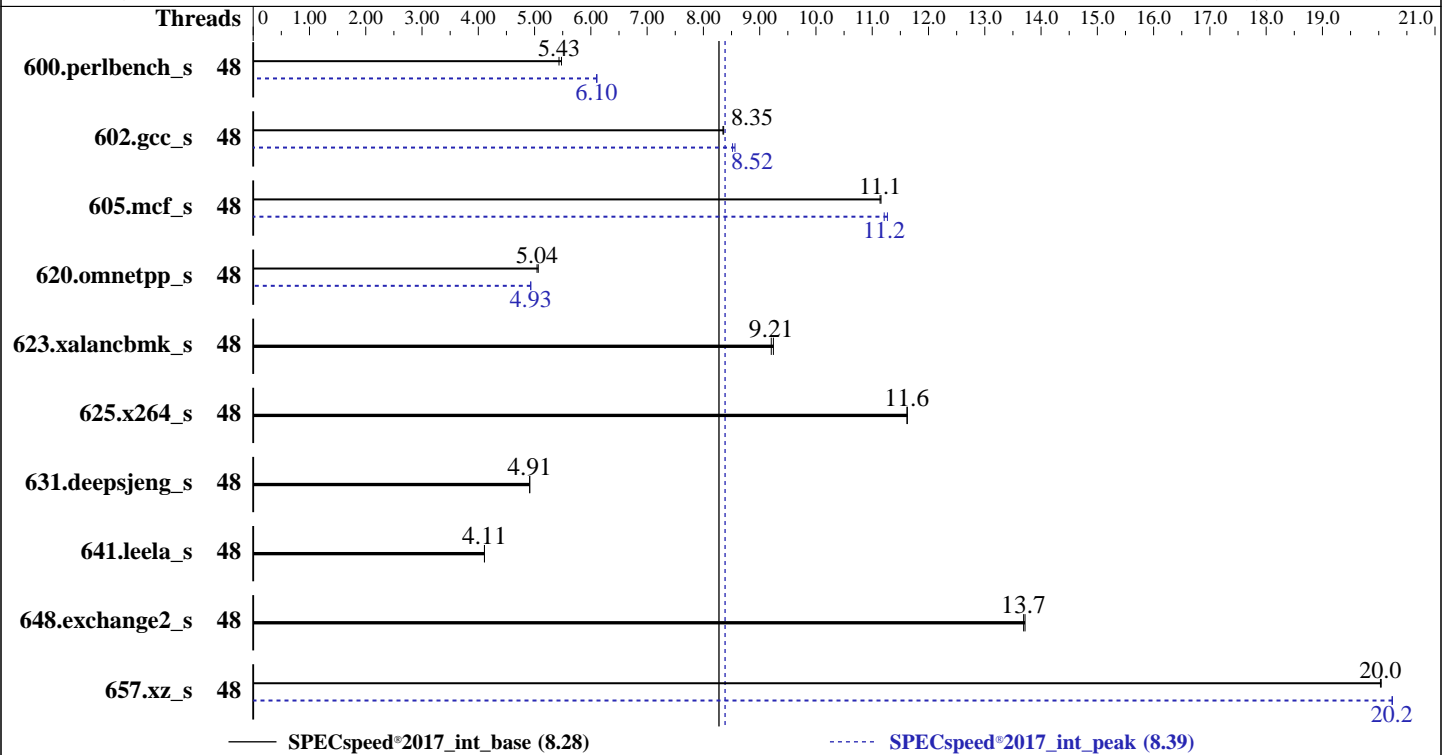
Test Date: May-2020

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2020

Tested by: Dell Inc.

Software Availability: Nov-2019



### Hardware

CPU Name: Intel Xeon Silver 4214R  
 Max MHz: 3500  
 Nominal: 2400  
 Enabled: 24 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2933V-R, running at 2400)  
 Storage: 1 x 1.92 TB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.1  
 kernel 4.18.0-147.el8.x86\_64  
 Compiler: C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 2.5.4 released Jan-2020  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	<b><u>327</u></b>	<b><u>5.43</u></b>	324	5.48			48	<b><u>291</u></b>	<b><u>6.10</u></b>	291	6.11		
602.gcc_s	48	<b><u>477</u></b>	<b><u>8.35</u></b>	476	8.36			48	<b><u>467</u></b>	<b><u>8.52</u></b>	465	8.56		
605.mcf_s	48	<b><u>424</u></b>	<b><u>11.1</u></b>	423	11.2			48	419	11.3	<b><u>421</u></b>	<b><u>11.2</u></b>		
620.omnetpp_s	48	<b><u>323</u></b>	<b><u>5.04</u></b>	322	5.07			48	331	4.93	<b><u>331</u></b>	<b><u>4.93</u></b>		
623.xalancbmk_s	48	<b><u>154</u></b>	<b><u>9.21</u></b>	153	9.24			48	<b><u>154</u></b>	<b><u>9.21</u></b>	153	9.24		
625.x264_s	48	152	11.6	<b><u>152</u></b>	<b><u>11.6</u></b>			48	152	11.6	<b><u>152</u></b>	<b><u>11.6</u></b>		
631.deepsjeng_s	48	292	4.91	<b><u>292</u></b>	<b><u>4.91</u></b>			48	292	4.91	<b><u>292</u></b>	<b><u>4.91</u></b>		
641.leela_s	48	415	4.11	<b><u>415</u></b>	<b><u>4.11</u></b>			48	415	4.11	<b><u>415</u></b>	<b><u>4.11</u></b>		
648.exchange2_s	48	<b><u>215</u></b>	<b><u>13.7</u></b>	214	13.7			48	<b><u>215</u></b>	<b><u>13.7</u></b>	214	13.7		
657.xz_s	48	308	20.0	<b><u>309</u></b>	<b><u>20.0</u></b>			48	<b><u>305</u></b>	<b><u>20.2</u></b>	305	20.2		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017/lib/intel64:/mnt/ramdisk/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-9900K CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

jemalloc, a general purpose malloc implementation

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## General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS settings:

Sub NUMA Cluster disabled  
Virtualization Technology disabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1E disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub set to standard  
Logical Processor enabled  
CPU Interconnect Bus Link Power Management disabled  
PCI ASPM L1 Link Power Management disabled  
UPI Prefetch enabled  
LLC Prefetch disabled  
Dead Line LLC Alloc enabled  
Directory AtoS disabled

Sysinfo program /mnt/ramdisk/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on rhel-8-1-sut Fri May 15 15:17:56 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Silver 4214R CPU @ 2.40GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
```

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## Platform Notes (Continued)

```

CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4214R CPU @ 2.40GHz
Stepping: 7
CPU MHz: 1286.815
CPU max MHz: 3500.0000
CPU min MHz: 1000.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 16896 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44
node 0 size: 95281 MB
node 0 free: 94797 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45

```

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## Platform Notes (Continued)

```

node 1 size: 96765 MB
node 1 free: 80953 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46
node 2 size: 96765 MB
node 2 free: 96103 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47
node 3 size: 96764 MB
node 3 free: 96646 MB
node distances:
node  0  1  2  3
  0:  10  21  11  21
  1:  21  10  21  11
  2:  11  21  10  21
  3:  21  11  21  10

```

From /proc/meminfo

```

MemTotal:      394831136 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

```

uname -a:

```

Linux rhel-8-1-sut 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

CVE-2018-3620 (L1 Terminal Fault):      Not affected
Microarchitectural Data Sampling:      Not affected
CVE-2017-5754 (Meltdown):              Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):      Mitigation: usercopy/swaps barriers and __user
pointer sanitization

```

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## Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 May 15 15:14 last=5

SPEC is set to: /mnt/ramdisk/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	200G	7.5G	193G	4%	/mnt/ramdisk

From /sys/devices/virtual/dmi/id

BIOS: Dell Inc. 2.5.4 01/13/2020

Vendor: Dell Inc.

Product: PowerEdge R640

Product Family: PowerEdge

Serial: FPFXCH2

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

10x 002C069D002C 18ASF2G72PDZ-2G9E1 16 GB 2 rank 2933

4x 00AD00B300AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933

8x 00AD00B300AD HMA82GR7CJR8N-XN 16 GB 2 rank 3200

2x 00AD063200AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
=====

```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,

Version 19.0.5.281 Build 20190815

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

```

=====
C++   | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
      | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
=====

```

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,

Version 19.0.5.281 Build 20190815

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

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## Compiler Version Notes (Continued)

-----  
=====  
Fortran | 648.exchange2\_s(base, peak)  
-----

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-m64 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.5.281/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs
```

## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)  
-prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib  
-ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)  
-prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div  
-DSPEC_SUPPRESS_OPENMP -L/usr/local/je5.0.1-64/lib
```

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## Peak Optimization Flags (Continued)

602.gcc\_s (continued):

-ljemalloc

605.mcf\_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)

-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div

-qopt-mem-layout-trans=4 -DSPEC\_SUPPRESS\_OPENMP -qopenmp

-DSPEC\_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264\_s: basepeak = yes

657.xz\_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)

-prof-use(pass 2) -O2 -xCORE-AVX512

-qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div

-DSPEC\_SUPPRESS\_OPENMP -qopenmp -DSPEC\_OPENMP

-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp\_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)

-ipo -xCORE-AVX512 -O3 -no-prec-div

-qopt-mem-layout-trans=4 -DSPEC\_SUPPRESS\_OPENMP

-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.5.281/linux/compiler/lib/intel64\_lin

-lqkmallo

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64\\_revD.html](http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.html)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE10.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64\\_revD.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.xml)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE10.xml>



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Tested with SPEC CPU<sup>®</sup>2017 v1.1.0 on 2020-05-15 16:17:55-0400.  
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