



# SPEC® CPU2017 Floating Point Rate Result

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## NEC Corporation

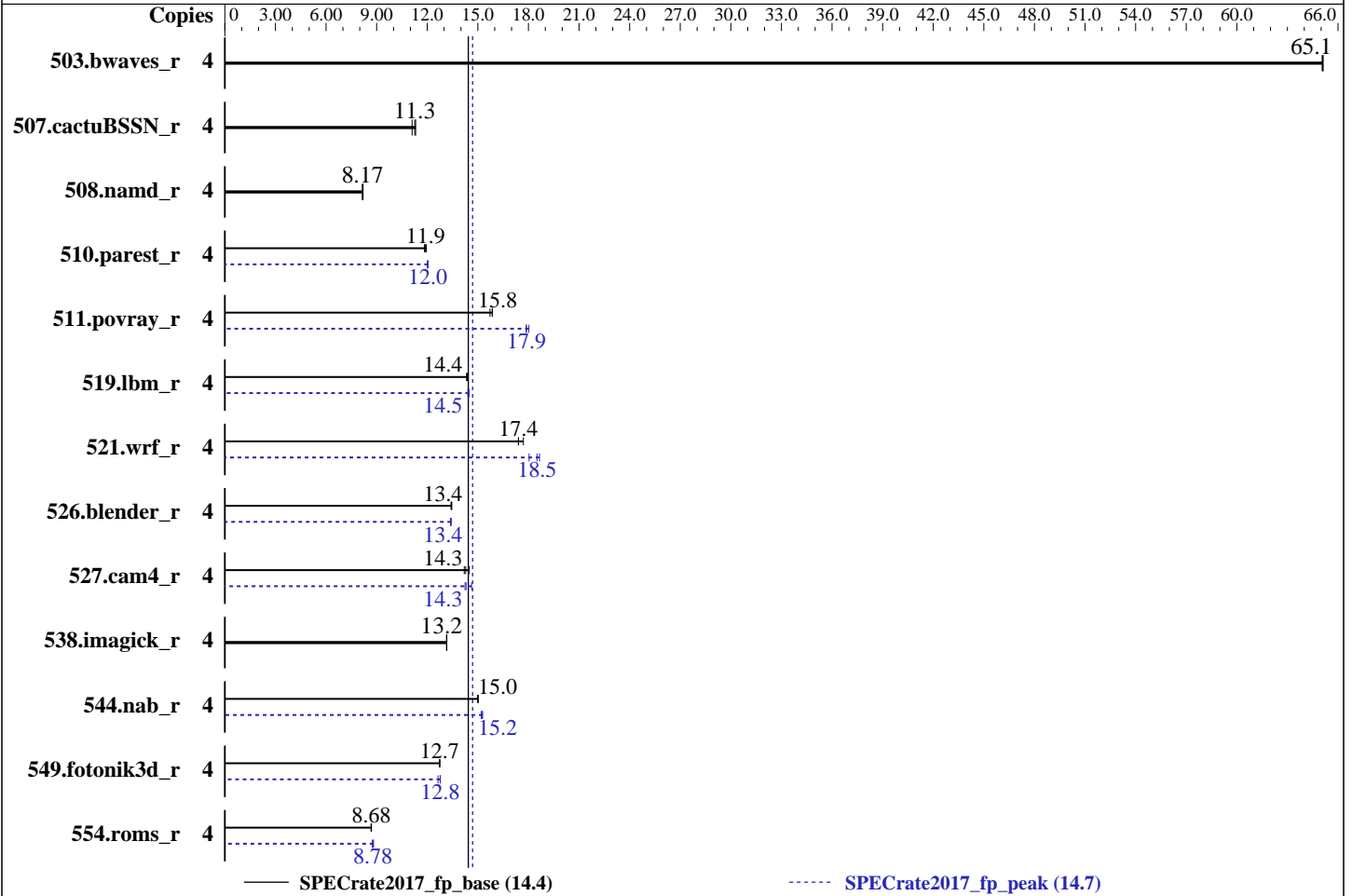
SPECrate2017\_fp\_base = 14.4

### Express5800/T110i (Intel Pentium G4560)

SPECrate2017\_fp\_peak = 14.7

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Jan-2019  
Hardware Availability: Apr-2017  
Software Availability: Mar-2018



### Hardware

CPU Name: Intel Pentium G4560  
Max MHz.: 3500  
Nominal: 3500  
Enabled: 2 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 256 KB I+D on chip per core  
L3: 3 MB I+D on chip per chip  
Other: None  
Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)  
Storage: 1 x 1 TB SATA, 7200 RPM  
Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
Kernel 3.10.0-693.21.1.el7.x86\_64  
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
Parallel: No  
Firmware: NEC BIOS Version 5.0.4008 06/07/2018 released Aug-2018  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	4	<b>616</b>	<b>65.1</b>	616	65.1	617	65.1	4	<b>616</b>	<b>65.1</b>	616	65.1	617	65.1
507.cactuBSSN_r	4	<b>450</b>	<b>11.3</b>	456	11.1	448	11.3	4	<b>450</b>	<b>11.3</b>	456	11.1	448	11.3
508.namd_r	4	466	8.15	464	8.19	<b>465</b>	<b>8.17</b>	4	466	8.15	464	8.19	<b>465</b>	<b>8.17</b>
510.parest_r	4	<b>880</b>	<b>11.9</b>	876	11.9	885	11.8	4	869	12.0	871	12.0	<b>870</b>	<b>12.0</b>
511.povray_r	4	589	15.9	<b>589</b>	<b>15.8</b>	594	15.7	4	<b>522</b>	<b>17.9</b>	518	18.0	523	17.9
519.lbm_r	4	293	14.4	<b>293</b>	<b>14.4</b>	294	14.3	4	291	14.5	293	14.4	<b>292</b>	<b>14.5</b>
521.wrf_r	4	506	17.7	<b>515</b>	<b>17.4</b>	515	17.4	4	480	18.7	497	18.0	<b>484</b>	<b>18.5</b>
526.blender_r	4	454	13.4	<b>453</b>	<b>13.4</b>	453	13.5	4	454	13.4	<b>455</b>	<b>13.4</b>	455	13.4
527.cam4_r	4	483	14.5	493	14.2	<b>490</b>	<b>14.3</b>	4	478	14.6	491	14.2	<b>489</b>	<b>14.3</b>
538.imagick_r	4	757	13.1	756	13.2	<b>756</b>	<b>13.2</b>	4	757	13.1	756	13.2	<b>756</b>	<b>13.2</b>
544.nab_r	4	448	15.0	<b>449</b>	<b>15.0</b>	449	15.0	4	440	15.3	443	15.2	<b>442</b>	<b>15.2</b>
549.fotonik3d_r	4	1223	12.7	<b>1224</b>	<b>12.7</b>	1225	12.7	4	<b>1221</b>	<b>12.8</b>	1221	12.8	1233	12.6
554.roms_r	4	731	8.69	733	8.67	<b>733</b>	<b>8.68</b>	4	<b>724</b>	<b>8.78</b>	727	8.74	721	8.81

SPECrate2017\_fp\_base = 14.4

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

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## General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Settings:

Power Management Policy: Custom  
Energy Performance: Performance  
DCU Streamer Prefetcher: Disabled  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on t110i Tue Jan 8 02:55:01 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Pentium(R) CPU G4560 @ 3.50GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 2
siblings       : 4
physical 0:    cores 0 1
```

From lscpu:

```
Architecture:    x86_64
CPU op-mode(s):  32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          4
On-line CPU(s) list:  0-3
Thread(s) per core:  2
Core(s) per socket:  2
Socket(s):       1
NUMA node(s):    1
Vendor ID:       GenuineIntel
CPU family:      6
Model:           158
Model name:      Intel(R) Pentium(R) CPU G4560 @ 3.50GHz
Stepping:        9
CPU MHz:         3500.000
CPU max MHz:     3500.0000
```

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## Platform Notes (Continued)

CPU min MHz: 800.0000  
BogoMIPS: 7008.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 256K  
L3 cache: 3072K  
NUMA node0 CPU(s): 0-3

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc  
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx est tm2 ssse3 cx16 xtpr  
pdcmm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave rdrand  
lahf\_lm abm 3dnowprefetch epb invpcid\_single intel\_pt spec\_ctrl ibpb\_support  
tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust smep erms invpcid mpx  
rdseed smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp hwp\_notify  
hwp\_act\_window hwp\_epp

```
/proc/cpuinfo cache data
cache size : 3072 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 65480 MB
node 0 free: 63619 MB
node distances:
node 0
0: 10
```

From /proc/meminfo

```
MemTotal: 65921396 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
```

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## Platform Notes (Continued)

```
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
```

```
uname -a:
Linux t110i 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 8 02:49
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  909G  125G  738G  15% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. 5.0.4008 06/07/2018
Memory:
4x Micron 18ASF2G72AZ-2G3B1 16 GB 2 rank 2400
```

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
-----
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CC 519.lbm_r(peak) 544.nab_r(peak)
-----
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CXXC 508.namd_r(base) 510.parest_r(base)
-----
```

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

(Continued on next page)



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## Compiler Version Notes (Continued)

=====  
CXXC 508.namd\_r(peak) 510.parest\_r(peak)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CC 511.povray\_r(base) 526.blender\_r(base)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CC 511.povray\_r(peak) 526.blender\_r(peak)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
FC 507.cactuBSSN\_r(base)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
FC 507.cactuBSSN\_r(peak)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
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## Compiler Version Notes (Continued)

=====  
FC 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 554.roms\_r(peak)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 521.wrf\_r(peak) 527.cam4\_r(peak)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

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## Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpc icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

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## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

## Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using both C and C++:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

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## Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:  
icpc icc

Benchmarks using Fortran, C, and C++:  
icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xSSE4.2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

538.imagick\_r: basepeak = yes

544.nab\_r: Same as 519.lbm\_r

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xSSE4.2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3  
-nostandard-realloc-lhs -align array32byte

554.roms\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xSSE4.2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs  
-align array32byte

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xSSE4.2 -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3  
-nonstandard-realloc-lhs -align array32byte
```

Benchmarks using both C and C++:

```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xSSE4.2 -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
507.cactuBSSN_r: basepeak = yes
```

## Peak Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11
```

Benchmarks using both C and C++:

```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-110i-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-110i-RevA.xml>



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Tested with SPEC CPU2017 v1.0.2 on 2019-01-07 12:55:00-0500.

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