



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 117

## Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

CPU2017 License: 3175

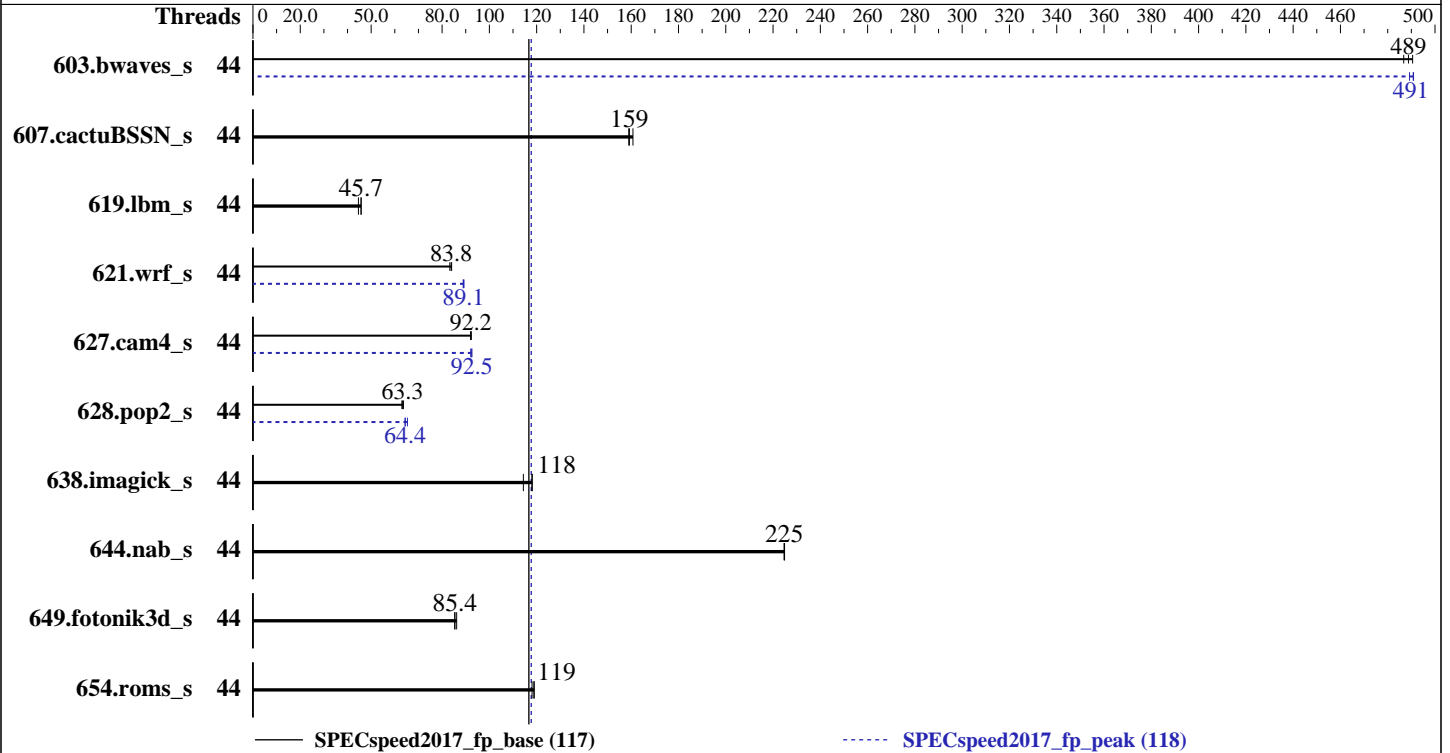
Test Sponsor: Huawei

Tested by: Huawei

Test Date: Aug-2018

Hardware Availability: Aug-2018

Software Availability: Mar-2018



### Hardware

CPU Name: Intel Xeon Gold 6152  
 Max MHz.: 3700  
 Nominal: 2100  
 Enabled: 44 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 30.25 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
 Storage: 1 x 1800 GB SAS, 10000 RPM  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
 3.10.0-693.11.6.el7.x86\_64  
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 0.86 Released Aug-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Huawei

SPECSpeed2017\_fp\_base = 117

## Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECSpeed2017\_fp\_peak = 118

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: Aug-2018  
Hardware Availability: Aug-2018  
Software Availability: Mar-2018

### Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	44	121	487	120	491	<u>121</u>	<u>489</u>	44	121	489	120	491	<u>120</u>	<u>491</u>
607.cactuBSSN_s	44	105	159	<u>105</u>	<u>159</u>	104	161	44	105	159	<u>105</u>	<u>159</u>	104	161
619.lbm_s	44	117	44.6	114	45.8	<u>115</u>	<u>45.7</u>	44	117	44.6	114	45.8	<u>115</u>	<u>45.7</u>
621.wrf_s	44	159	83.3	157	84.0	<u>158</u>	<u>83.8</u>	44	<u>148</u>	<u>89.1</u>	149	88.9	148	89.3
627.cam4_s	44	96.0	92.3	<u>96.1</u>	<u>92.2</u>	96.3	92.1	44	95.7	92.6	96.2	92.1	<u>95.8</u>	<u>92.5</u>
628.pop2_s	44	<u>188</u>	<u>63.3</u>	187	63.6	188	63.0	44	<u>184</u>	<u>64.4</u>	182	65.3	185	64.3
638.imagick_s	44	<u>122</u>	<u>118</u>	122	118	126	114	44	<u>122</u>	<u>118</u>	122	118	126	114
644.nab_s	44	77.8	225	77.7	225	<u>77.7</u>	<u>225</u>	44	77.8	225	77.7	225	<u>77.7</u>	<u>225</u>
649.fotonik3d_s	44	<u>107</u>	<u>85.4</u>	107	85.3	106	86.2	44	<u>107</u>	<u>85.4</u>	107	85.3	106	86.2
654.roms_s	44	132	119	<u>133</u>	<u>119</u>	133	118	44	132	119	<u>133</u>	<u>119</u>	133	118

SPECSpeed2017\_fp\_base = **117**

SPECSpeed2017\_fp\_peak = **118**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64:/spec2017/je5.0.1-32:/spec2017/je5.0.1-64"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 117

## Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2018  
**Software Availability:** Mar-2018

### Platform Notes

BIOS configuration:  
Power Policy Set to Load Balance  
Hyper-Threading Set to Disable  
XPT Prefetch Set to Enabled  
Sysinfo program /spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on localhost.localdomain Fri Aug 10 00:21:32 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz  
2 "physical id"s (chips)  
44 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 22  
siblings : 22  
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28  
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 44  
On-line CPU(s) list: 0-43  
Thread(s) per core: 1  
Core(s) per socket: 22  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz  
Stepping: 4  
CPU MHz: 2101.000  
CPU max MHz: 2101.0000  
CPU min MHz: 1000.0000  
BogoMIPS: 4200.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 30976K

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 117

## Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2018  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

```
NUMA node0 CPU(s):      0-21
NUMA node1 CPU(s):      22-43
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts
```

```
/proc/cpuinfo cache data
cache size : 30976 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
node 0 size: 194741 MB
node 0 free: 189318 MB
node 1 cpus: 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43
node 1 size: 196608 MB
node 1 free: 191457 MB
node distances:
node  0  1
  0:  10  21
  1:  21  10
```

```
From /proc/meminfo
MemTotal:      394174880 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Huawei

SPECspeed2017\_fp\_base = 117

## Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2018  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.4:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-693.11.6.el7.x86\_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 9 19:24

SPEC is set to: /spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xf	553G	5.6G	547G	2%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.86 08/06/2018

Memory:

4x NO DIMM NO DIMM

12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
CC 619.lbm\_s(peak)  
=====

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
FC 607.cactuBSSN\_s(base, peak)  
=====

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 117

Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: Aug-2018  
Hardware Availability: Aug-2018  
Software Availability: Mar-2018

## Compiler Version Notes (Continued)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base, peak)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 621.wrf\_s(peak) 628.pop2\_s(peak)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 117

Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Aug-2018

Hardware Availability: Aug-2018

Software Availability: Mar-2018

## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
```



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 117

Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: Aug-2018  
Hardware Availability: Aug-2018  
Software Availability: Mar-2018

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: basepeak = yes
```

```
638.imagick_s: basepeak = yes
```

```
644.nab_s: basepeak = yes
```

Fortran benchmarks:

```
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP  
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-qopenmp -nostandard-realloc-lhs
```

```
649.fotonik3d_s: basepeak = yes
```

```
654.roms_s: basepeak = yes
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
```

(Continued on next page)





# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017\_fp\_base = 117

Huawei XH628 V5 (Intel Xeon Gold 6152)

SPECspeed2017\_fp\_peak = 118

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Aug-2018

Hardware Availability: Aug-2018

Software Availability: Mar-2018

## Peak Optimization Flags (Continued)

621.wrf\_s (continued):

-DSPEC\_OPENMP -nostandard-realloc-lhs

627.cam4\_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp

-DSPEC\_OPENMP -nostandard-realloc-lhs

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-08-09 20:21:31-0400.

Report generated on 2019-01-22 16:42:54 by CPU2017 PDF formatter v6067.

Originally published on 2019-01-22.