



SPEC® CPU2017 Integer Rate Result

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Supermicro

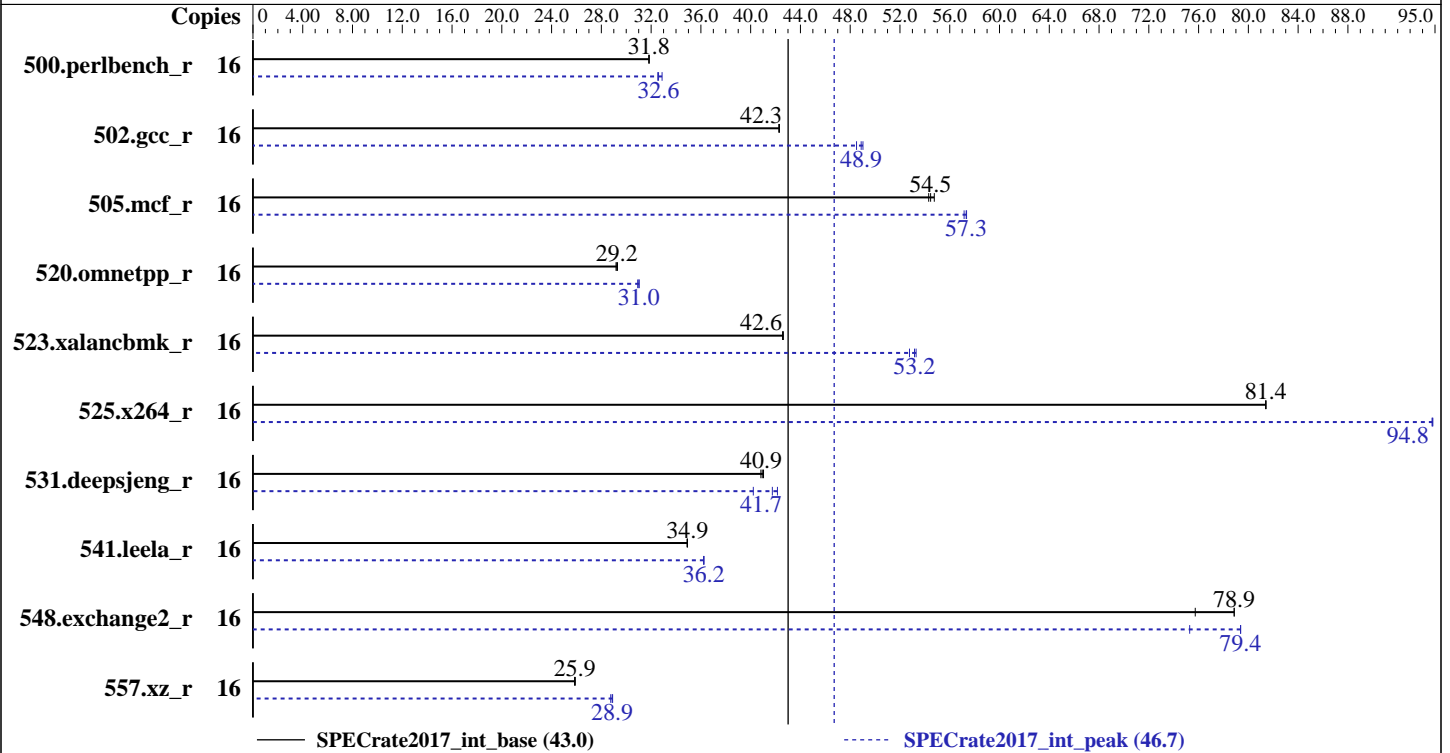
A+ Server 1013S-MTR
(H11SSL-i, AMD EPYC 7251)

SPECrate2017_int_base = 43.0

SPECrate2017_int_peak = 46.7

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Jul-2018
Hardware Availability: Jun-2017
Software Availability: Feb-2018



Hardware

CPU Name: AMD EPYC 7251
Max MHz.: 2900
Nominal: 2100
Enabled: 8 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 64 KB I + 32 KB D on chip per core
L2: 512 KB I+D on chip per core
L3: 32 MB I+D on chip per chip
Other: None
Memory: 512 GB (8 x 64 GB 4Rx4 PC4-2666V-L, running at 2400)
Storage: 1 x 200 GB SATAIII SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)
kernel 4.4.114-94.11-default
Compiler: C/C++: Version 1.0.0 of AOCC
Fortran: Version 4.8.2 of GCC
Parallel: No
Firmware: Supermicro BIOS version 1.0b released Apr-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc general purpose malloc implementation V4.5.0



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|-----------------|--------|------------|-------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 16 | 800 | 31.8 | 800 | 31.8 | 800 | 31.8 | 16 | 783 | 32.5 | 781 | 32.6 | 775 | 32.9 |
| 502.gcc_r | 16 | 535 | 42.3 | 536 | 42.3 | 536 | 42.3 | 16 | 467 | 48.5 | 463 | 48.9 | 462 | 49.0 |
| 505.mcf_r | 16 | 472 | 54.8 | 476 | 54.3 | 475 | 54.5 | 16 | 451 | 57.3 | 451 | 57.4 | 452 | 57.2 |
| 520.omnetpp_r | 16 | 718 | 29.2 | 717 | 29.3 | 719 | 29.2 | 16 | 679 | 30.9 | 677 | 31.0 | 676 | 31.0 |
| 523.xalancbmk_r | 16 | 397 | 42.6 | 396 | 42.6 | 397 | 42.6 | 16 | 318 | 53.2 | 320 | 52.8 | 317 | 53.3 |
| 525.x264_r | 16 | 344 | 81.4 | 344 | 81.4 | 344 | 81.4 | 16 | 295 | 94.8 | 296 | 94.8 | 296 | 94.8 |
| 531.deepsjeng_r | 16 | 448 | 40.9 | 449 | 40.8 | 447 | 41.0 | 16 | 456 | 40.2 | 435 | 42.2 | 439 | 41.7 |
| 541.leela_r | 16 | 759 | 34.9 | 759 | 34.9 | 759 | 34.9 | 16 | 731 | 36.2 | 731 | 36.2 | 731 | 36.2 |
| 548.exchange2_r | 16 | 532 | 78.9 | 554 | 75.7 | 532 | 78.9 | 16 | 528 | 79.4 | 557 | 75.3 | 528 | 79.4 |
| 557.xz_r | 16 | 668 | 25.9 | 667 | 25.9 | 669 | 25.8 | 16 | 598 | 28.9 | 601 | 28.8 | 599 | 28.9 |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages were enabled for this run (OS default)

Huge pages were not configured for this run.



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General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/amd1704-rate-libs-revC/64;/home/cpu2017/amd1704-rate-libs-revC/32:"
MALLOCONF = "lg_chunk:26"

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

The AOCC Gold Linker plugin was installed and used for the link stage.

The AOCC Fortran Plugin version 1.0 was used to leverage AOCC optimizers with gfortran. It is available here:
<http://developer.amd.com/amd-aocc/>

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using RHEL 7.4

jemalloc, a general purpose malloc implementation, was obtained at
<https://github.com/jemalloc/jemalloc/releases/download/4.5.0/jemalloc-4.5.0.tar.bz2>
jemalloc was built with GCC v4.8.5 in RHEL v7.2 under default conditions.

jemalloc uses environment variable MALLOCONF with values narenas and lg_chunk:
narenas: sets the maximum number of arenas to use for automatic multiplexing of threads and arenas.

lg_chunk: set the virtual memory chunk size (log base 2). For example,
lg_chunk:21 sets the default chunk size to $2^{21} = 2\text{MiB}$.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:

Determinism Slider = Power

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on linux-pm02 Fri Jul 13 13:58:44 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7251 8-Core Processor

1 "physical id"s (chips)

16 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 4 8 12 16 20 24 28
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:   0-15
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             1
NUMA node(s):         4
Vendor ID:             AuthenticAMD
CPU family:            23
Model:                1
Model name:            AMD EPYC 7251 8-Core Processor
Stepping:              2
CPU MHz:               2100.000
CPU max MHz:           2100.0000
CPU min MHz:           1200.0000
BogoMIPS:              4199.76
Virtualization:        AMD-V
L1d cache:             32K
L1i cache:             64K
L2 cache:              512K
L3 cache:              4096K
NUMA node0 CPU(s):    0,1,8,9
NUMA node1 CPU(s):    2,3,10,11
NUMA node2 CPU(s):    4,5,12,13
NUMA node3 CPU(s):    6,7,14,15
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc extd_apicid amd_dcm aperfmperf eagerfpu pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx arat cpb
hw_pstate retpoline retpoline_amd npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
flushbyasid decodeassists pausefilter pfthreshold vmmcall avic fsgsbase bmi1 avx2
smep bmi2 rdseed adx smap clflushopt sha_ni xsaveopt xsavec xgetbv1 clzero irperf
ibpb overflow_recov succor smca
```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

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Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0 1 8 9
node 0 size: 128841 MB
node 0 free: 128696 MB
node 1 cpus: 2 3 10 11
node 1 size: 129019 MB
node 1 free: 128879 MB
node 2 cpus: 4 5 12 13
node 2 size: 129019 MB
node 2 free: 128851 MB
node 3 cpus: 6 7 14 15
node 3 size: 129017 MB
node 3 free: 128889 MB
node distances:
node  0  1  2  3
  0:  10  16  16  16
  1:  16  10  16  16
  2:  16  16  10  16
  3:  16  16  16  10
```

```
From /proc/meminfo
MemTotal:      528278540 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux linux-pm02 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

run-level 3 Jul 13 11:57

SPEC is set to: /home/cpu2017

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda4 | xfs | 145G | 3.0G | 142G | 3% | /home |

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0b 04/27/2018

Memory:

8x Samsung M386A8K40BM2-CTD 64 GB 4 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 502.gcc_r(peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
CXXC 523.xalancbmk_r(peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin
=====

=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base) 557.xz_r(base, peak)
=====

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu

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Compiler Version Notes (Continued)

Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CC 500.perlbench_r(peak) 525.x264_r(peak)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
CXXC 541.leela_r(peak)

AOCC.LLVM.4.0.0.B35.2017_04_26 clang version 4.0.0 (CLANG:) (based on LLVM
AOCC.LLVM.4.0.0.B35.2017_04_26)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/AOCC-1.0-Compiler/bin

=====
FC 548.exchange2_r(base, peak)

GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
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under the terms of the GNU General Public License.
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Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

clang gfortran

Base Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-flto -Wl, -plugin-opt= -merge-constant -lsr-in-nested-loop
-disable-vect-cmp -O3 -ffast-math -march=znver1 -fstruct-layout=2
-mlvm -unroll-threshold=100 -fremap-arrays -mno-avx2
-inline-threshold=1000 -z muldefs -ljemalloc
```

C++ benchmarks:

```
-flto -Wl, -plugin-opt= -merge-constant -lsr-in-nested-loop
-disable-vect-cmp -O3 -march=znver1 -mlvm -unroll-threshold=100
-finline-aggressive -fremap-arrays -inline-threshold=1000 -z muldefs
-ljemalloc
```

Fortran benchmarks:

```
-flto -Wl, -plugin-opt= -merge-constant -lsr-in-nested-loop
-disable-vect-cmp -O3(gfortran) -O3(clang) -mavx -madox
-funroll-loops -ffast-math -z muldefs -Ofast -fdefault-integer-8
-fplugin=dragonegg.so -fplugin-arg-dragonegg-llvm-option="
-enable-iv-split -inline-threshold:1000 -disable-vect-cmp" -ljemalloc
```

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-lgfortran -lamdlibm

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
clang gfortran

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -flto -Wl, -plugin-opt= -merge-constant
-lsr-in-nested-loop -fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver1
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively
-mno-avx2 -unroll-threshold=100 -fremap-arrays
-inline-threshold=1000 -ljemalloc
```

```
502.gcc_r: -m32 -flto -Wl, -plugin-opt= -merge-constant
-lsr-in-nested-loop -Ofast -march=znver1
```

(Continued on next page)



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Peak Optimization Flags (Continued)

502.gcc_r (continued):

```
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively  
-mno-avx2 -unroll-threshold=100 -fremap-arrays  
-inline-threshold=1000 -fgnu89-inline  
-L/root/work/lib/jemalloc/lib32 -ljemalloc
```

```
505.mcf_r: -flto -Wl, -plugin-opt= -merge-constant  
-lsr-in-nested-loop -Ofast -march=znver1  
-fstruct-layout=3 -mllvm -vectorize-memory-aggressively  
-mno-avx2 -unroll-threshold=100 -fremap-arrays  
-inline-threshold=1000 -ljemalloc
```

525.x264_r: Same as 500.perlbench_r

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

```
520.omnetpp_r: -flto -Wl, -plugin-opt= -merge-constant  
-lsr-in-nested-loop -Ofast -march=znver1  
-finline-aggressive -mllvm -unroll-threshold=100  
-fremap-arrays -inline-threshold=1000 -ljemalloc
```

```
523.xalancbmk_r: -m32 -flto -Wl, -plugin-opt= -merge-constant  
-lsr-in-nested-loop -Ofast -march=znver1  
-finline-aggressive -mllvm -unroll-threshold=100  
-fremap-arrays -inline-threshold=1000  
-L/root/work/lib/jemalloc/lib32 -ljemalloc
```

531.deepsjeng_r: Same as 520.omnetpp_r

```
541.leela_r: -flto -Wl, -plugin-opt= -merge-constant  
-lsr-in-nested-loop -fprofile-instr-generate(pass 1)  
-fprofile-instr-use(pass 2) -Ofast -march=znver1 -mllvm  
-unroll-count=8 -unroll-threshold=100 -ljemalloc
```

Fortran benchmarks:

```
-flto -Wl, -plugin-opt= -merge-constant -lsr-in-nested-loop  
-O3(gfortran) -O3(clang) -mavx2 -madx -funroll-loops -ffast-math  
-Ofast -fdefault-integer-8 -fplugin=dragonegg.so  
-fplugin-arg-dragonegg-llvm-option=" -enable-iv-split  
-inline-threshold:1000 -disable-vect-cmp" -ljemalloc -lgfortran  
-lamdlibm
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/gcc.2018-02-16.html>
<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.2018-02-16.html>
<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Naples-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/gcc.2018-02-16.xml>
<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.2018-02-16.xml>
<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Naples-revD.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-07-13 01:58:43-0400.
Report generated on 2019-02-21 17:42:55 by CPU2017 PDF formatter v6067.
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