



SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19

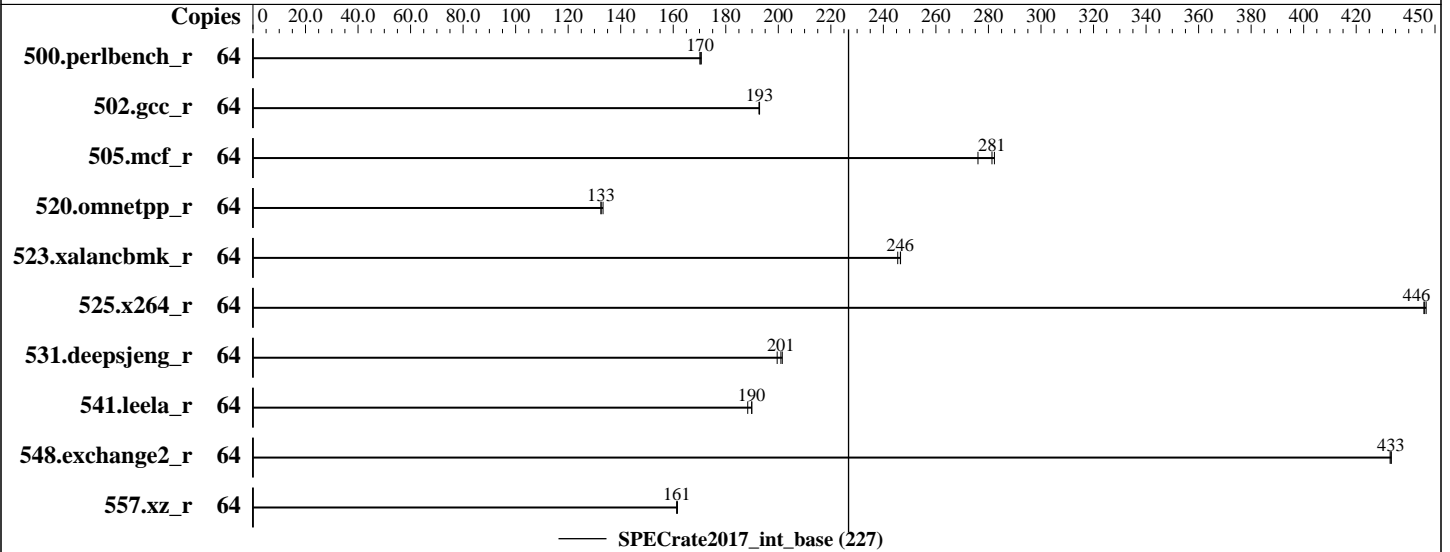
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Apr-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018



Hardware

CPU Name: Intel Xeon Gold 6144
 Max MHz.: 4200
 Nominal: 3500
 Enabled: 32 cores, 4 chips, 2 threads/core
 Orderable: 2,4 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 24.75 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (48 x 16 GB 2Rx4 PC4-2666V-R)
 Storage: 384 GB tmpfs
 Other: 1 x SAS HDD, 1.2 TB, 10500 RPM, used for swap

Software

OS: SUSE Linux Enterprise Server 12 SP2
 4.4.114-92.64-default
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
 Parallel: No
 Firmware: Fujitsu BIOS Version V5.0.0.12 R1.9.0 for D3753-A1x. Released Mar-2018
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator library V5.0.1



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|-----------------|--------|------------|------------|------------|------------|------------|------------|--------|---------|-------|---------|-------|---------|-------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 500.perlbench_r | 64 | 599 | 170 | 597 | 171 | 598 | 170 | | | | | | | | | |
| 502.gcc_r | 64 | 470 | 193 | 470 | 193 | 470 | 193 | | | | | | | | | |
| 505.mcf_r | 64 | 368 | 281 | 366 | 282 | 375 | 276 | | | | | | | | | |
| 520.omnetpp_r | 64 | 630 | 133 | 634 | 133 | 634 | 133 | | | | | | | | | |
| 523.xalancbmk_r | 64 | 274 | 246 | 275 | 245 | 274 | 247 | | | | | | | | | |
| 525.x264_r | 64 | 251 | 446 | 251 | 446 | 251 | 447 | | | | | | | | | |
| 531.deepsjeng_r | 64 | 368 | 200 | 365 | 201 | 364 | 202 | | | | | | | | | |
| 541.leela_r | 64 | 563 | 188 | 558 | 190 | 558 | 190 | | | | | | | | | |
| 548.exchange2_r | 64 | 387 | 433 | 387 | 433 | 387 | 433 | | | | | | | | | |
| 557.xz_r | 64 | 428 | 162 | 428 | 161 | 428 | 161 | | | | | | | | | |

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter: nohz_full=1-63
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Set tmpfs filesystem with:
mkdir /home/memory
mount -t tmpfs -o size=384g,rw tmpfs /home/memory
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
```

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/memory/speccpu/lib/ia32:/home/memory/speccpu/lib/intel64"
LD_LIBRARY_PATH = "\$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-32"
LD_LIBRARY_PATH = "\$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Apr-2018

Hardware Availability: Jul-2017

Software Availability: Mar-2018

General Notes (Continued)

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
DCU Streamer Prefetcher = Disabled
Intel Virtualization Technology = Disabled
HWPM Support = Disabled
Package C State limit = C0
LLC Dead Line Alloc = Disabled
Stale AtoS = Enabled
Patrol Scrub = Disabled
IMC Interleaving = 1-way
Sub NUMA Clustering = Enabled
Fan Control = Full
Sysinfo program /home/memory/speccpu/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on RX4770M4 Fri Apr 20 02:44:59 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
4 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes (Continued)

```
siblings : 16
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 0 2 3 9 16 19 26 27
physical 2: cores 0 2 3 9 16 19 26 27
physical 3: cores 0 4 5 6 16 19 20 22
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
Stepping: 4
CPU MHz: 3501.000
CPU max MHz: 3501.0000
CPU min MHz: 1200.0000
BogoMIPS: 6983.56
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,1,3,4,32,33,35,36
NUMA node1 CPU(s): 2,5-7,34,37-39
NUMA node2 CPU(s): 8,9,11,12,40,41,43,44
NUMA node3 CPU(s): 10,13-15,42,45-47
NUMA node4 CPU(s): 16,17,19,20,48,49,51,52
NUMA node5 CPU(s): 18,21-23,50,53-55
NUMA node6 CPU(s): 24,25,28,29,56,57,60,61
NUMA node7 CPU(s): 26,27,30,31,58,59,62,63
```

```
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnm flexpriority
ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes (Continued)

```
xsavvec xgetbv1 cqm_llc cqm_occup_llc
```

```
/proc/cpuinfo cache data  
cache size : 25344 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)  
node 0 cpus: 0 1 3 4 32 33 35 36  
node 0 size: 95018 MB  
node 0 free: 94499 MB  
node 1 cpus: 2 5 6 7 34 37 38 39  
node 1 size: 96760 MB  
node 1 free: 96609 MB  
node 2 cpus: 8 9 11 12 40 41 43 44  
node 2 size: 96760 MB  
node 2 free: 90564 MB  
node 3 cpus: 10 13 14 15 42 45 46 47  
node 3 size: 96760 MB  
node 3 free: 94481 MB  
node 4 cpus: 16 17 19 20 48 49 51 52  
node 4 size: 96760 MB  
node 4 free: 96525 MB  
node 5 cpus: 18 21 22 23 50 53 54 55  
node 5 size: 96760 MB  
node 5 free: 96612 MB  
node 6 cpus: 24 25 28 29 56 57 60 61  
node 6 size: 96760 MB  
node 6 free: 95868 MB  
node 7 cpus: 26 27 30 31 58 59 62 63  
node 7 size: 96609 MB  
node 7 free: 96466 MB  
node distances:  
node 0 1 2 3 4 5 6 7  
0: 10 11 21 21 21 21 21 21  
1: 11 10 21 21 21 21 21 21  
2: 21 21 10 11 21 21 21 21  
3: 21 21 11 10 21 21 21 21  
4: 21 21 21 21 10 11 21 21  
5: 21 21 21 21 11 10 21 21  
6: 21 21 21 21 21 21 10 11  
7: 21 21 21 21 21 21 11 10
```

```
From /proc/meminfo  
MemTotal: 790723368 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144, 3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Platform Notes (Continued)

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
```

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

```
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux RX4770M4 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 20 00:55
```

```
SPEC is set to: /home/memory/speccpu
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs            tmpfs     384G   8.9G 376G   3% /home/memory
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.9.0 for D3753-A1x
03/29/2018
```

```
Memory:
48x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144,
3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
FC 548.exchange2_r(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M4, Intel Xeon Gold 6144,
3.50GHz

SPECrate2017_int_base = 227

SPECrate2017_int_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2018
Hardware Availability: Jul-2017
Software Availability: Mar-2018

Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevE.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-04-20 02:44:58-0400.

Report generated on 2018-10-31 17:18:51 by CPU2017 PDF formatter v6067.

Originally published on 2018-05-29.