SPEC® CPU2017 Integer Rate Result

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Lenovo Global Technology

SPECrate2017_int_base = 270

ThinkSystem SR850
(2.20 GHz, Intel Xeon Gold 5120T)

SPECrate2017_int_peak = 288

CPU2017 License: 9017　　　　　　　　　　　　　　　　　　　　　**Test Date:**　Dec-2017

Test Sponsor:　Lenovo Global Technology　　　　　　　　　　**Hardware Availability:** Aug-2017

Tested by:　Lenovo Global Technology　　　　　　　　　　　**Software Availability:**　Sep-2017

Copies		Value
500.perlbench_r	112	205
		256
502.gcc_r	112	225
		273
505.mcf_r	112	336
		335
520.omnetpp_r	112	176
		176
523.xalancbmk_r	112	274
		332
525.x264_r	112	511
		531
531.deepsjeng_r	112	236
		237
541.leela_r	112	216
		220
548.exchange2_r	112	513
		513
557.xz_r	112	203
		203

—— SPECrate2017_int_base (270)　　　　　- - - - SPECrate2017_int_peak (288)

Hardware

CPU Name:　Intel Xeon Gold 5120T

  Max MHz.:　3200

  Nominal:　2200

Enabled:　56 cores, 4 chips, 2 threads/core

Orderable:　2,4 chips

Cache L1:　32 KB I + 32 KB D on chip per core

  L2:　1 MB I+D on chip per core

  L3:　19.25 MB I+D on chip per chip

  Other:　None

Memory:　1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)

Storage:　1 x 800 GB SAS SSD

Other:　None

Software

OS:　SUSE Linux Enterprise Server 12 SP2 (x86_64)
　　　Kernel 4.4.21-69-default

Compiler:　C/C++: Version 18.0.0.128 of Intel C/C++
　　　Compiler for Linux;
　　　Fortran: Version 18.0.0.128 of Intel Fortran
　　　Compiler for Linux

Parallel:　No

Firmware:　Lenovo BIOS Version TEE115E 1.01 released Aug-2017

File System:　xfs

System State:　Run level 3 (multi-user)

Base Pointers:　64-bit

Peak Pointers:　32/64-bit

Other:　jemalloc: jemalloc memory allocator library
　　　V5.0.1;
　　　jemalloc: configured and built at default for
　　　32bit (i686) and 64bit (x86_64) targets;
　　　jemalloc: built with the RedHat Enterprise 7.4,
　　　and the system compiler gcc 4.8.5;
　　　jemalloc: sources avilable from jemalloc.net or
　　　releases

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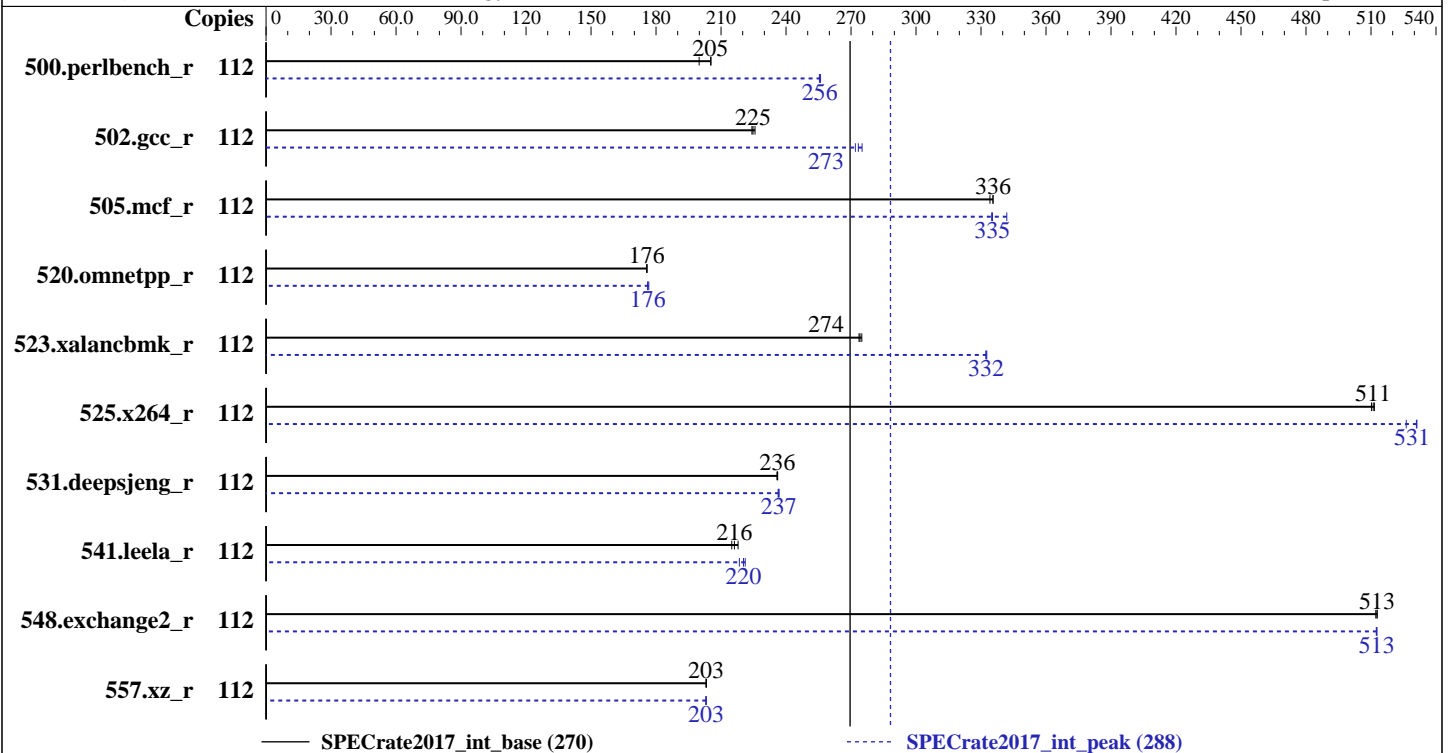
SPECrate2017_int_peak =   288

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Software Availability: Sep-2017

Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
500.perlbench_r	112	**869**	**205**	868	205	892	200	112	698	255	**698**	**256**	697	256
502.gcc_r	112	707	224	703	226	**706**	**225**	112	583	272	576	275	**580**	**273**
505.mcf_r	112	**539**	**336**	539	336	542	334	112	540	335	**540**	**335**	529	342
520.omnetpp_r	112	836	176	**836**	**176**	836	176	112	832	177	834	176	**834**	**176**
523.xalancbmk_r	112	430	275	432	274	**431**	**274**	112	**356**	**332**	356	333	356	332
525.x264_r	112	**384**	**511**	384	510	383	512	112	**369**	**531**	373	526	369	531
531.deepsjeng_r	112	544	236	**544**	**236**	544	236	112	**542**	**237**	543	236	542	237
541.leela_r	112	851	218	**858**	**216**	863	215	112	839	221	**842**	**220**	849	218
548.exchange2_r	112	**572**	**513**	573	512	572	513	112	573	512	**572**	**513**	572	513
557.xz_r	112	**595**	**203**	596	203	595	203	112	**596**	**203**	595	203	596	203
SPECrate2017_int_base =		**270**												
SPECrate2017_int_peak =		**288**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>   /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

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Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
DCU Streamer Prefetcher set to Disable
MONITORMWAIT set to Enable
Execute Disable Bit set to Disable
XPT Prefetcher set to Enable
 Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on Electron-node-02 Wed Dec  6 17:19:39 2017

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5120T CPU @ 2.20GHz
       4  "physical id"s (chips)
       112 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 14
       siblings  : 28
       physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
       physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
       physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
       physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

 From lscpu:
       Architecture:          x86_64
       CPU op-mode(s):        32-bit, 64-bit
       Byte Order:            Little Endian
       CPU(s):                112
       On-line CPU(s) list:   0-111
       Thread(s) per core:    2
       Core(s) per socket:    14
       Socket(s):             4
       NUMA node(s):          8
       Vendor ID:             GenuineIntel
       CPU family:            6
       Model:                 85
       Model name:            Intel(R) Xeon(R) Gold 5120T CPU @ 2.20GHz
       Stepping:              4
       CPU MHz:               2194.841
       BogoMIPS:              4389.68
       Virtualization:        VT-x
       L1d cache:             32K
```

(Continued on next page)

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Platform Notes (Continued)

```
         L1i cache:            32K
         L2 cache:             1024K
         L3 cache:             19712K
         NUMA node0 CPU(s):    0-3,7-9,56-59,63-65
         NUMA node1 CPU(s):    4-6,10-13,60-62,66-69
         NUMA node2 CPU(s):    14-17,21-23,70-73,77-79
         NUMA node3 CPU(s):    18-20,24-27,74-76,80-83
         NUMA node4 CPU(s):    28-31,35-37,84-87,91-93
         NUMA node5 CPU(s):    32-34,38-41,88-90,94-97
         NUMA node6 CPU(s):    42-45,49-51,98-101,105-107
         NUMA node7 CPU(s):    46-48,52-55,102-104,108-111
         Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
     aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
     xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
     avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
     tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
     erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
     avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
    cache size : 19712 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 7 8 9 56 57 58 59 63 64 65
   node 0 size: 193131 MB
   node 0 free: 192561 MB
   node 1 cpus: 4 5 6 10 11 12 13 60 61 62 66 67 68 69
   node 1 size: 193528 MB
   node 1 free: 193015 MB
   node 2 cpus: 14 15 16 17 21 22 23 70 71 72 73 77 78 79
   node 2 size: 193528 MB
   node 2 free: 193031 MB
   node 3 cpus: 18 19 20 24 25 26 27 74 75 76 80 81 82 83
   node 3 size: 193528 MB
   node 3 free: 192988 MB
   node 4 cpus: 28 29 30 31 35 36 37 84 85 86 87 91 92 93
   node 4 size: 193528 MB
   node 4 free: 193036 MB
   node 5 cpus: 32 33 34 38 39 40 41 88 89 90 94 95 96 97
   node 5 size: 193528 MB
   node 5 free: 192994 MB
   node 6 cpus: 42 43 44 45 49 50 51 98 99 100 101 105 106 107
   node 6 size: 193528 MB
```

(Continued on next page)

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Platform Notes (Continued)

```
  node 6 free: 193023 MB
  node 7 cpus: 46 47 48 52 53 54 55 102 103 104 108 109 110 111
  node 7 size: 193525 MB
  node 7 free: 193009 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  11  21  21  21  21  31  31
    1:  11  10  21  21  21  21  31  31
    2:  21  21  10  11  31  31  21  21
    3:  21  21  11  10  31  31  21  21
    4:  21  21  31  31  10  11  21  21
    5:  21  21  31  31  11  10  21  21
    6:  31  31  21  21  21  21  10  11
    7:  31  31  21  21  21  21  11  10

From /proc/meminfo
   MemTotal:       1584975460 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux Electron-node-02 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 6 17:17

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda4       xfs   688G  130G  559G  19% /home

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
```

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Platform Notes (Continued)

```
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS Lenovo -[TEE115E-1.01]- 08/11/2017
   Memory:
    48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
 CC   500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 CC   500.perlbench_r(peak) 502.gcc_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
      541.leela_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
      541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
 FC   548.exchange2_r(base, peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
```

(Continued on next page)

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Compiler Version Notes (Continued)

```
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
----------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

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Base Other Flags

C benchmarks:
```
-m64 -std=c11
```

C++ benchmarks:
```
-m64
```

Fortran benchmarks:
```
-m64
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-fno-strict-overflow -L/usr/local/je5.0.1-64/lib`

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Peak Optimization Flags (Continued)

500.perlbench_r (continued):
```
-ljemalloc
```

502.gcc_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
`-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

505.mcf_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib`
`-ljemalloc`

525.x264_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -fno-alias`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

557.xz_r: `Same as 505.mcf_r`

C++ benchmarks:

520.omnetpp_r: `-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

523.xalancbmk_r: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`
`-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: `Same as 520.omnetpp_r`

541.leela_r: `Same as 520.omnetpp_r`

Fortran benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Peak Other Flags

C benchmarks (except as noted below):
```
-m64 -std=c11
```

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Peak Other Flags (Continued)

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-12-06 04:19:38-0500.
Report generated on 2018-10-31 13:54:37 by CPU2017 PDF formatter v6067.
Originally published on 2017-12-26.