



SPEC® CPU2017 Integer Rate Result

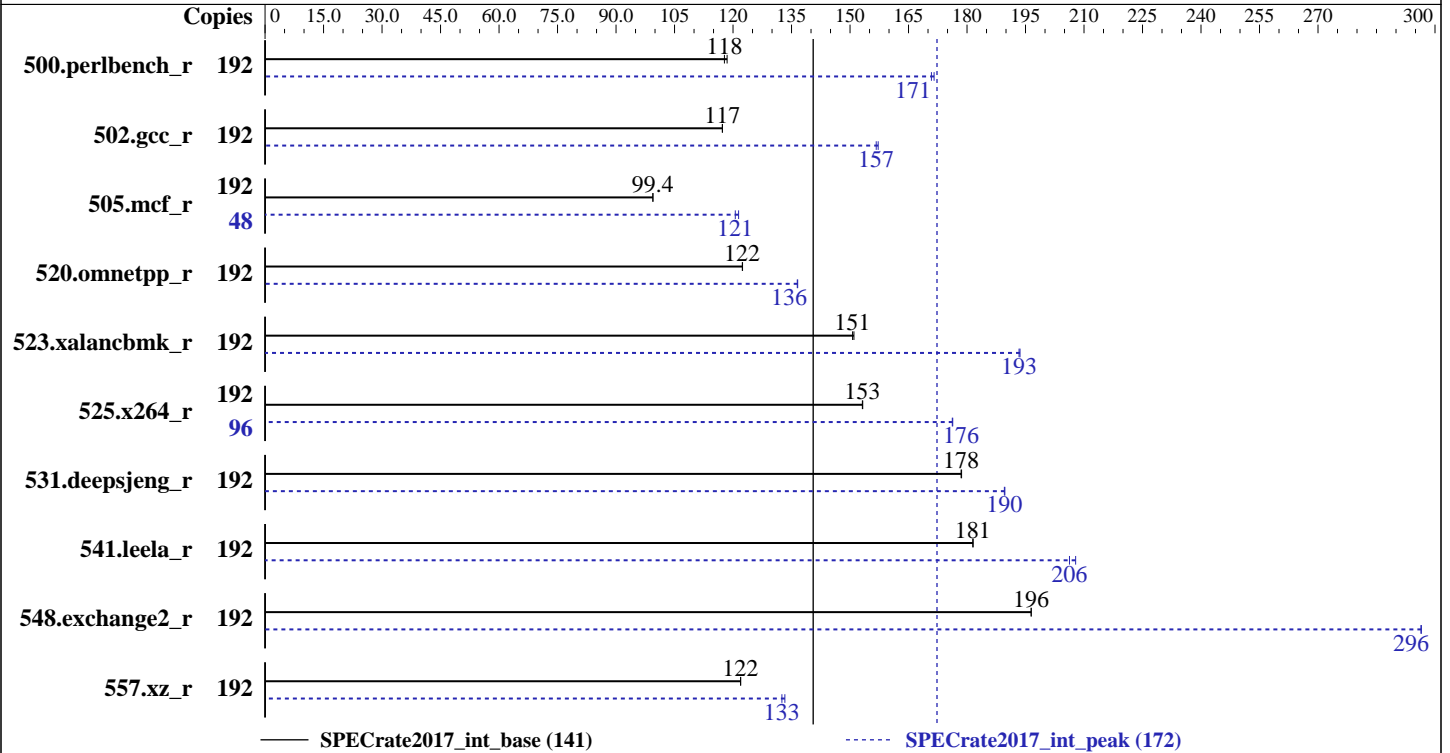
Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017



Hardware

CPU Name: SPARC64 XII
 Max MHz.: 3900
 Nominal: 3900
 Enabled: 24 cores, 2 chips, 8 threads/core
 Orderable: 1 or 2 CPU chips; 2, 3, 4, .. 24 cores
 Cache L1: 64 KB I + 64 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 32 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (32 x 32 GB 2Rx4 PC4-2400T-R)
 Storage: 1 x 600 GB 10K RPM SAS (for system disk)
 Other: None

Software

OS: Oracle Solaris 11.3 SRU 24.4
 Compiler: C/C++/Fortran: Version 12.6 of Oracle Developer Studio
 Parallel: No
 Firmware: Fujitsu HCP Version 3040 released Oct-2017
 File System: tmpfs
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other: None



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	192	2580	118	<u>2595</u>	<u>118</u>			192	<u>1789</u>	<u>171</u>	1782	172		
502.gcc_r	192	<u>2319</u>	<u>117</u>	2318	117			192	<u>1735</u>	<u>157</u>	1729	157		
505.mcf_r	192	3119	99.5	<u>3120</u>	<u>99.4</u>			48	<u>643</u>	<u>121</u>	639	121		
520.omnetpp_r	192	2057	122	<u>2058</u>	<u>122</u>			192	<u>1846</u>	<u>136</u>	1844	137		
523.xalancbmk_r	192	<u>1346</u>	<u>151</u>	1343	151			192	<u>1049</u>	<u>193</u>	1047	194		
525.x264_r	192	<u>2195</u>	<u>153</u>	2194	153			96	<u>954</u>	<u>176</u>	953	176		
531.deepsjeng_r	192	<u>1233</u>	<u>178</u>	1232	179			192	<u>1161</u>	<u>190</u>	1160	190		
541.leela_r	192	<u>1752</u>	<u>181</u>	1751	182			192	1530	208	<u>1541</u>	<u>206</u>		
548.exchange2_r	192	<u>2561</u>	<u>196</u>	2561	196			192	1697	297	<u>1697</u>	<u>296</u>		
557.xz_r	192	<u>1701</u>	<u>122</u>	1700	122			192	1556	133	<u>1565</u>	<u>133</u>		

SPECrate2017_int_base = 141

SPECrate2017_int_peak = 172

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

Processes were assigned to specific processors using 'pbind' commands. The config file option 'submit' was used, along with a list of processors in the 'BIND' variable, to generate the pbind commands. (For details, please see the config file.)

Operating System Notes

Shell Environments:

ulimit -s 131072 was used to limit the space consumed by the stack (and therefore make more space available to the heap).

The "Logical Domains Manager" service was turned off using the command "svcadm disable ldmd".

System Tunables:

(/etc/system parameters)

autoup = 86400

Causes pages older than the listed number of seconds to be written by fsflush.
doiflush = 0

Controls whether file system metadata syncs will be executed during fsflush invocations.
dopageflush = 0

Controls whether memory is examined for modified pages during fsflush invocations.
zfs:zfs_arc_max=1073741824

Determines the maximum size of the ZFS Adaptive Replacement Cache (ARC).



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

General Notes

File System:
tmpfs: output_root was used to put run directories in /tmp/cpu2017
zfs: operating system

Platform Notes

Sysinfo program /export/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on H2S-156-D0 Fri Oct 27 05:15:28 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /usr/sbin/psrinfo
SPARC64-XII (chipid 0, clock 3900 MHz)
SPARC64-XII (chipid 1, clock 3900 MHz)
2 chips
192 threads
3900 MHz

From kstat: 24 cores

From prtconf: 1046016 Megabytes

/etc/release:
Oracle Solaris 11.3 SPARC
uname -a:
SunOS H2S-156-D0 5.11 11.3 sun4v sparc sun4v

disk: df -h /export/cpu2017
Filesystem Size Used Available Capacity Mounted on
rpool/export 547G 6.0G 77G 8% /export

(End of data from sysinfo program)

Compiler Version Notes

=====
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base)
541.leela_r(base)
=====

CC: Studio 12.6 Sun C++ 5.15 SunOS_sparc 2017/05/30
=====

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Compiler Version Notes (Continued)

=====
CXXC 520.omnetpp_r(peak) 531.deepsjeng_r(peak) 541.leela_r(peak)

CC: Studio 12.6 Sun C++ 5.15 SunOS_sparc 2017/05/30

=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)

cc: Studio 12.6 Sun C 5.15 SunOS_sparc 2017/05/30

=====
CC 500.perlbench_r(peak) 502.gcc_r(peak) 505.mcf_r(peak) 525.x264_r(peak)
557.xz_r(peak)

cc: Studio 12.6 Sun C 5.15 SunOS_sparc 2017/05/30

=====
FC 548.exchange2_r(base)

f90: Studio 12.6 Fortran 95 8.8 SunOS_sparc 2017/05/30

=====
FC 548.exchange2_r(peak)

f90: Studio 12.6 Fortran 95 8.8 SunOS_sparc 2017/05/30

Base Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

Fortran benchmarks:

f90



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Base Portability Flags

```
500.perlbench_r: -DSPEC_SOLARIS_SPARC
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -D_FILE_OFFSET_BITS=64
520.omnetpp_r: -D_FILE_OFFSET_BITS=64
523.xalancbmk_r: -DSPEC_SOLARIS -D_FILE_OFFSET_BITS=64
525.x264_r: -D_FILE_OFFSET_BITS=64
531.deepsjeng_r: -D_FILE_OFFSET_BITS=64
541.leela_r: -D_FILE_OFFSET_BITS=64
548.exchange2_r: -D_FILE_OFFSET_BITS=64
557.xz_r: -D_FILE_OFFSET_BITS=64
```

Base Optimization Flags

C benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M
-xsegment_align=4M -xthroughput
```

C++ benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M
-xsegment_align=4M -xthroughput -library=stdcxx4 -template=extdef
-lfast
```

Fortran benchmarks:

```
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M
-xsegment_align=4M -xthroughput
```

Base Other Flags

C benchmarks:

```
-xjobs=8
```

C++ benchmarks:

```
-xjobs=8
```

Fortran benchmarks:

```
-xjobs=8
```



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Peak Compiler Invocation

C benchmarks:

cc

C++ benchmarks:

CC

Fortran benchmarks:

f90

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -x04  
-xalias_level=layout -xinline_param=level:3 -lfast
```

```
502 gcc_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xtarget=sparc64xplus  
-xipo=1 -xinline_param=level:3 -xprefetch=no%auto  
-xthroughput=no -lfast
```

```
505.mcf_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=strong  
-xprefetch=no%auto -xthroughput=no
```

```
525.x264_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xunroll=3  
-xprefetch=no%auto -W2,-Afully_unroll:always=on
```

```
557.xz_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=std  
-xprefetch=latx:0.4 -lbsdmalloc
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141
SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Peak Optimization Flags (Continued)

C++ benchmarks:

```
520.omnetpp_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=compatible  
-xprefetch=latx:0.4 -library=stdcxx4 -template=extdef  
-lfast
```

```
523.xalancbmk_r: -m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -library=stlport4  
-xprefetch=no%auto -xunroll=2 -lfast
```

```
531.deepsjeng_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=compatible  
-xinline_param=level:1 -xunroll=2 -xprefetch=no%auto  
-library=stlport4
```

```
541.leela_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32  
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput  
-xinline_param=max_growth:500 -xprefetch=no%auto  
-std=c++03 -library=no%stlport4 -xthroughput=no  
-Wc,-Qiselect-funcalign=4  
-Qoption iropt -Afully_unroll:always=on  
-xinline_param=level:3
```

Fortran benchmarks:

```
-xprofile=collect:./feedback -xprofile=use:./feedback -m32 -fast  
-xtarget=sparc64xii -xipo=2 -xpagesize=256M -xsegment_align=256M  
-xthroughput -xtarget=sparc64xplus -xprefetch=no%auto  
-Qoption iropt -Afully_unroll:always=on
```

Peak Other Flags

C benchmarks:

```
-xjobs=8
```

C++ benchmarks:

```
-xjobs=8
```

(Continued on next page)



SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Fujitsu
Fujitsu SPARC M12-2

SPECrate2017_int_base = 141

SPECrate2017_int_peak = 172

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Oct-2017
Hardware Availability: Apr-2017
Software Availability: Jul-2017

Peak Other Flags (Continued)

Fortran benchmarks:
-xjobs=8

The flags files that were used to format this result can be browsed at
<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.html>
<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.html>

You can also download the XML flags sources by saving the following links:
<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.xml>
<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-10-27 01:15:26-0400.
Report generated on 2018-10-31 13:46:19 by CPU2017 PDF formatter v6067.
Originally published on 2017-12-26.