



# SPEC® CPU2017 Integer Rate Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380 Gen10

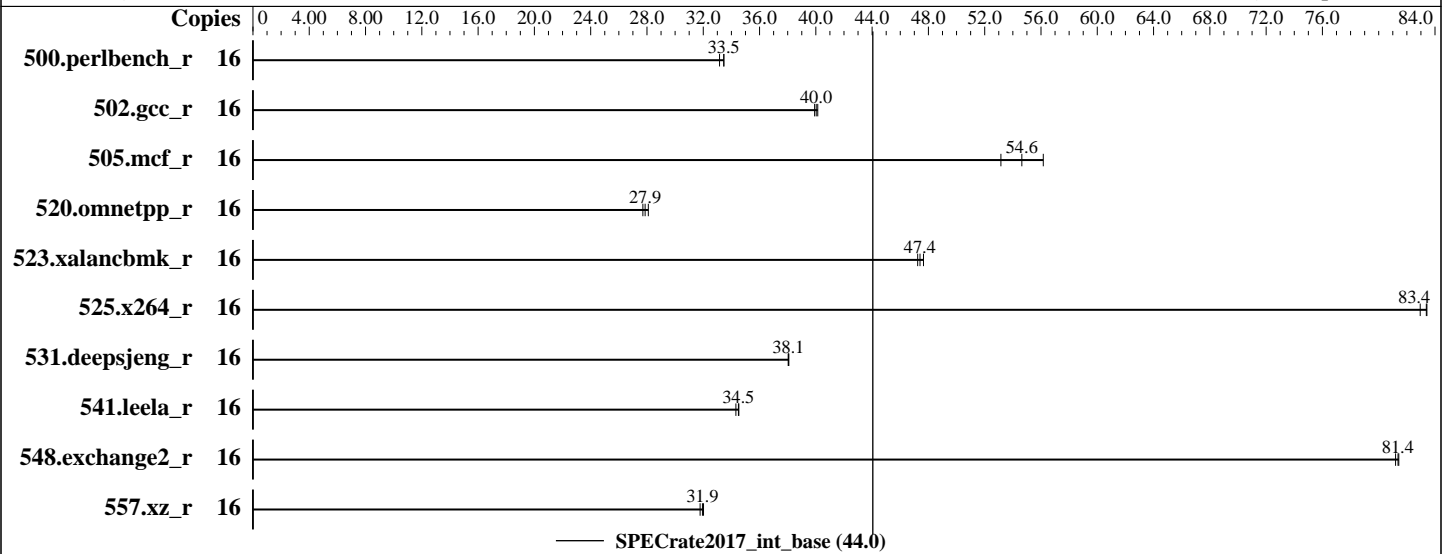
(2.60 GHz, Intel Xeon Silver 4112)

SPECrate2017\_int\_base = 44.0

SPECrate2017\_int\_peak = Not Run

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Oct-2017  
Hardware Availability: Oct-2017  
Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Silver 4112  
 Max MHz.: 3000  
 Nominal: 2600  
 Enabled: 8 cores, 2 chips, 2 threads/core  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 8.25 MB I+D on chip per chip  
 Other: None  
 Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R, running at 2400)  
 Storage: 1 x 960 GB SATA SSD, RAID 0  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: HPE BIOS Version U30 released Oct-2017 (tested with U30 9/29/2017)  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc: jemalloc memory allocator library V5.0.1; configured and built at default for 32bit (i686) and 64bit (x86\_64) targets; built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	16	<b>761</b>	<b>33.5</b>	761	33.5	768	33.2							
502.gcc_r	16	<b>566</b>	<b>40.0</b>	564	40.1	568	39.9							
505.mcf_r	16	460	56.2	<b>473</b>	<b>54.6</b>	486	53.2							
520.omnetpp_r	16	<b>753</b>	<b>27.9</b>	758	27.7	747	28.1							
523.xalancbmk_r	16	358	47.2	<b>356</b>	<b>47.4</b>	355	47.6							
525.x264_r	16	<b>336</b>	<b>83.4</b>	336	83.4	338	82.9							
531.deepsjeng_r	16	482	38.0	481	38.1	<b>482</b>	<b>38.1</b>							
541.leela_r	16	772	34.3	<b>768</b>	<b>34.5</b>	767	34.5							
548.exchange2_r	16	515	81.4	516	81.2	<b>515</b>	<b>81.4</b>							
557.xz_r	16	544	31.8	540	32.0	<b>541</b>	<b>31.9</b>							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/spec2017/lib/ia32:/home/spec2017/lib/intel64:/home/spec2017/je5.0.1-32"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/spec2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4



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## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetcher set to Enabled  
LLC Dead Line Allocation set to Disabled  
Workload Profile set to Throughput Frequency Compute  
Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on sys1-dl380-lic-srvr Wed Oct 18 16:58:44 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 4
  siblings       : 8
  physical 0     : cores 1 2 4 5
  physical 1     : cores 0 2 3 4
```

From lscpu:

```
Architecture:    x86_64
CPU op-mode(s):  32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          16
On-line CPU(s) list:  0-15
Thread(s) per core:  2
Core(s) per socket:  4
Socket(s):       2
NUMA node(s):    2
Vendor ID:       GenuineIntel
CPU family:      6
Model:           85
Model name:      Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping:        4
CPU MHz:         2593.934
BogoMIPS:        5187.86
Virtualization:  VT-x
L1d cache:       32K
L1i cache:       32K
```

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## Platform Notes (Continued)

L2 cache: 1024K  
L3 cache: 8448K  
NUMA node0 CPU(s): 0-3,8-11  
NUMA node1 CPU(s): 4-7,12-15

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel\_pt tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm\_llc cqm\_occup\_llc

```
/proc/cpuinfo cache data
cache size : 8448 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 8 9 10 11
node 0 size: 96277 MB
node 0 free: 90243 MB
node 1 cpus: 4 5 6 7 12 13 14 15
node 1 size: 96650 MB
node 1 free: 91266 MB
node distances:
node 0 1
0: 10 21
1: 21 10
```

```
From /proc/meminfo
MemTotal: 197558352 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
```

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## Platform Notes (Continued)

```

NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

uname -a:

```

Linux sys1-dl380-lic-srvr 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 17 17:38

SPEC is set to: /home/spec2017

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   400G  8.9G  391G   3% /home

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
-----

```

icc (ICC) 18.0.0 20170811

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```

=====
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
-----

```

icpc (ICC) 18.0.0 20170811

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## Compiler Version Notes (Continued)

=====  
FC 548.exchange2\_r(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

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## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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