



# SPEC CPU®2017 Integer Rate Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380 Gen10

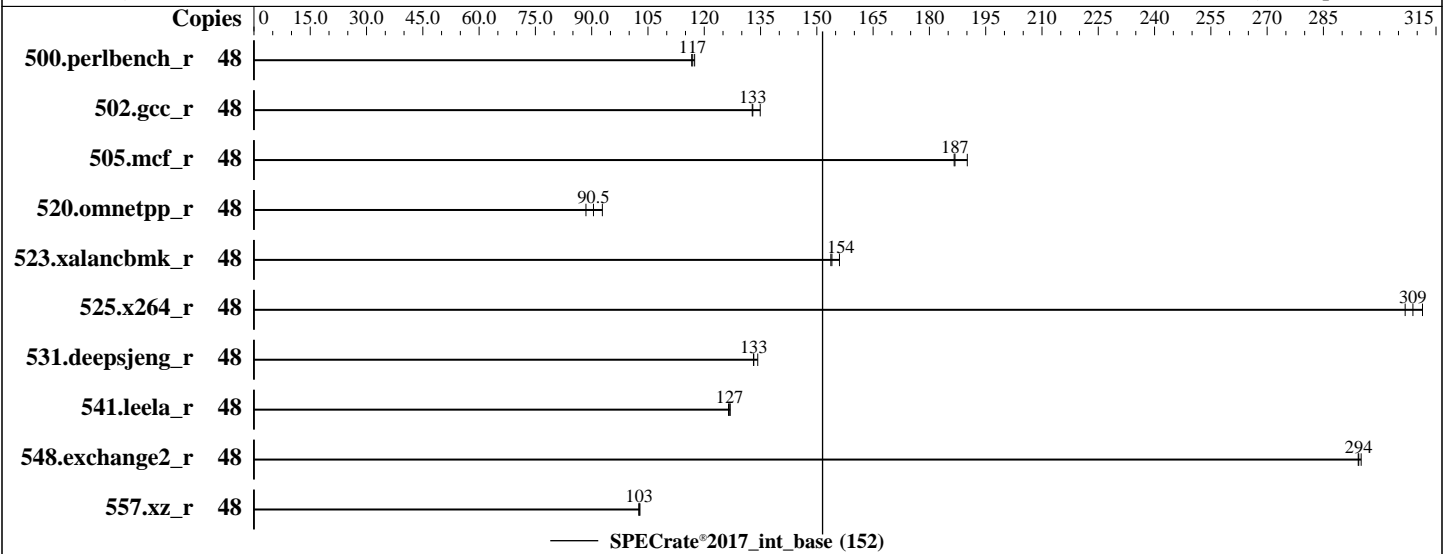
(3.00 GHz, Intel Xeon Platinum 8158)

SPECrate®2017\_int\_base = 152

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Oct-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8158  
Max MHz: 3700  
Nominal: 3000  
Enabled: 24 cores, 2 chips, 2 threads/core  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 24.75 MB I+D on chip per chip  
Other: None  
Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)  
Storage: 1 x 960 GB SATA SSD, RAID 0  
Other: None

### Software

OS: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
Kernel 4.4.21-69-default  
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
Parallel: No  
Firmware: HPE BIOS Version U30 released Oct-2017 (tested with U30 9/29/2017)  
File System: xfs  
System State: Run level 5 (multi-user w/GUI)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc: jemalloc memory allocator library V5.0.1;  
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  
jemalloc: sources available from jemalloc.net or releases  
Power Management: --



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	<b>654</b>	<b>117</b>	655	117	651	117							
502.gcc_r	48	504	135	<b>511</b>	<b>133</b>	512	133							
505.mcf_r	48	408	190	416	187	<b>415</b>	<b>187</b>							
520.omnetpp_r	48	678	92.8	<b>696</b>	<b>90.5</b>	712	88.5							
523.xalancbmk_r	48	325	156	330	154	<b>329</b>	<b>154</b>							
525.x264_r	48	270	311	274	307	<b>272</b>	<b>309</b>							
531.deepsjeng_r	48	410	134	<b>413</b>	<b>133</b>	413	133							
541.leela_r	48	626	127	<b>627</b>	<b>127</b>	629	126							
548.exchange2_r	48	<b>427</b>	<b>294</b>	427	294	426	295							
557.xz_r	48	504	103	506	103	<b>505</b>	<b>103</b>							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

## General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/spec2017/je5.0.1-32:/spec2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4



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## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetcher set to Enabled  
LLC Dead Line Allocation set to Disabled  
Workload Pofile set to Throughput Frequency Compute  
Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on dl380-sys2-sles Tue Oct 10 20:42:10 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Platinum 8158 CPU @ 3.00GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores      : 12
  siblings       : 24
 physical 0     : cores 0 1 3 9 10 16 18 19 24 25 26 27
 physical 1     : cores 0 1 2 3 4 9 10 16 18 19 25 26
```

From lscpu:

```
Architecture:    x86_64
CPU op-mode(s):  32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          48
On-line CPU(s) list:  0-47
Thread(s) per core:  2
Core(s) per socket: 12
Socket(s):       2
NUMA node(s):    4
Vendor ID:       GenuineIntel
CPU family:      6
Model:           85
Model name:      Intel(R) Xeon(R) Platinum 8158 CPU @ 3.00GHz
Stepping:        4
CPU MHz:         2992.976
BogoMIPS:        5985.95
Virtualization:  VT-x
L1d cache:       32K
L1i cache:       32K
```

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## Platform Notes (Continued)

```

L2 cache:          1024K
L3 cache:          25344K
NUMA node0 CPU(s): 0-5,24-29
NUMA node1 CPU(s): 6-11,30-35
NUMA node2 CPU(s): 12-17,36-41
NUMA node3 CPU(s): 18-23,42-47

```

```

Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 24 25 26 27 28 29
node 0 size: 47892 MB
node 0 free: 46354 MB
node 1 cpus: 6 7 8 9 10 11 30 31 32 33 34 35
node 1 size: 48382 MB
node 1 free: 47367 MB
node 2 cpus: 12 13 14 15 16 17 36 37 38 39 40 41
node 2 size: 48382 MB
node 2 free: 47645 MB
node 3 cpus: 18 19 20 21 22 23 42 43 44 45 46 47
node 3 size: 48265 MB
node 3 free: 47688 MB
node distances:
node   0   1   2   3
 0:   10  21  31  31
 1:   21  10  31  31
 2:   31  31  10  21
 3:   31  31  21  10

```

```

From /proc/meminfo
MemTotal:          197552052 kB
HugePages_Total:      0
Hugepagesize:       2048 kB

```

```

/usr/bin/lsb_release -d

```

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## Platform Notes (Continued)

SUSE Linux Enterprise Server 12 SP2

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION\_ID="12.2"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux dl380-sys2-sles 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 5 Oct 9 16:11

SPEC is set to: /home/spec2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdb4	xfs	400G	8.9G	391G	3%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U30 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
      | 525.x264_r(base) 557.xz_r(base)
-----
```

icc (ICC) 18.0.0 20170811

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## Compiler Version Notes (Continued)

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
| 541.leela\_r(base)  
=====

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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