



CINT2000 Result

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Hewlett-Packard Company

ProLiant ML350 G5 (1.86 GHz, Intel Xeon processor E5320)

SPECint_rate2000 = 77.8

SPECint_rate_base2000 = 77.4

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Feb-2007 Hardware Avail: Nov-2006 Software Avail: Nov-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.zip	4	125	51.8	4	125	51.9
175.vpr	4	115	56.6	4	113	57.5
176.gcc	4	59.3	86.1	4	59.3	86.1
181.mcf	4	127	65.6	4	125	66.7
186.crafty	4	58.9	78.8	4	59.0	78.7
197.parser	4	131	63.6	4	129	64.6
252.eon	4	53.6	113	4	53.5	113
253.perlbnk	4	83.5	100	4	82.7	101
254.gap	4	77.2	66.1	4	77.2	66.2
255.vortex	4	67.1	131	4	67.4	131
256.bzip2	4	123	56.8	4	123	56.8
300.twolf	4	139	100	4	139	100

Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4 MB L2 shared, 1066 MHz bus)
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Parallel: No
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8 GB (4x2 GB PC2-5300F CL5)
 Disk Subsystem: 4x36 GB 10 K SAS
 Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise X64 Edition (x86_64) SP1
 Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z)
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 MicroQuill Smartheap Library 8.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.zip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

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256.bzip2:    basepeak=1
300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch disabled
Other Configuration Notes
  The start /b /wait /affinity command is used to bind CPU(s) to processes.
```