



CINT2000 Result

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Acer Incorporated

Acer Altos R710 (Dual-Core Intel Xeon 2.8 GHz, 2x2MB L2, 800MHz system bus)

SPECint_rate2000 = 58.6

SPECint_rate_base2000 = 58.6

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2005 | Hardware Avail: Dec-2005 | Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	231	56.4	8	231	56.4
175.vpr	8	352	36.9	8	346	37.5
176.gcc	8	122	83.6	8	122	83.6
181.mcf	8	693	24.1	8	693	24.1
186.crafty	8	194	47.8	8	194	47.8
197.parser	8	270	61.8	8	270	61.9
252.eon	8	151	79.9	8	152	79.6
253.perlbnk	8	233	71.8	8	233	71.6
254.gap	8	174	58.7	8	174	58.7
255.vortex	8	170	104	8	170	104
256.bzip2	8	280	49.6	8	282	49.4
300.twolf	8	363	76.7	8	364	76.4

Hardware

CPU: Dual-Core Intel Xeon (2.8 GHz, 2x2MB L2, 800MHz system bus)
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip(Hyper-Threading Technology Enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 9.0 Build 20050430Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO

300.twolf: -fast +FDO shlw32M.lib

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>