



# CINT2000 Result

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## IBM Corporation

IBM eServer xSeries 346 (3.0 GHz Xeon, 2MB L2 Cache)

SPECint\_rate2000 = 33.3

SPECint\_rate\_base2000 = 33.1

SPEC license #: 11 | Tested by: IBM Corporation | Test date: Dec-2005 | Hardware Avail: Sep-2005 | Software Avail: Jun-2005

| Benchmark   | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip    | 2           | 144          | 22.6       | 2      | 142     | 22.9  |
| 175.vpr     | 2           | 142          | 22.8       | 2      | 139     | 23.4  |
| 176.gcc     | 2           | 64.3         | 39.7       | 2      | 64.3    | 39.7  |
| 181.mcf     | 2           | 119          | 35.1       | 2      | 119     | 35.1  |
| 186.crafty  | 2           | 89.8         | 25.8       | 2      | 90.2    | 25.7  |
| 197.parser  | 2           | 146          | 28.7       | 2      | 145     | 28.7  |
| 252.eon     | 2           | 68.9         | 43.8       | 2      | 69.2    | 43.6  |
| 253.perlbnk | 2           | 102          | 41.0       | 2      | 101     | 41.4  |
| 254.gap     | 2           | 75.5         | 33.8       | 2      | 73.3    | 34.8  |
| 255.vortex  | 2           | 74.2         | 59.4       | 2      | 74.7    | 59.0  |
| 256.bzip2   | 2           | 137          | 25.4       | 2      | 136     | 25.5  |
| 300.twolf   | 2           | 195          | 35.7       | 2      | 195     | 35.7  |

### Hardware

CPU: Intel Xeon Processor 3.0Hz with 2MB L2 Cache(800 MHz system bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12K(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 x 512 MB ECC PC2-3200 Single Rank  
Disk Subsystem: 73GB SCSI 10K RPM  
Other Hardware:

### Software

Operating System: Windows Server 2003 Standard Edition  
Compiler: Intel C/C++ Compiler 9.0 (20050624Z) for 32-bit applications  
Intel Fortran Compiler 9.0 (20050624Z) for 32-bit applications  
Microsoft Visual Studio .NET 13.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.30  
File System: NTFS  
System State: Default

## Notes/Tuning Information

### GENERAL

ONESTEP=yes  
+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
186.crafty: -DNT\_i386  
253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### BASE TUNING

C: -fast -Qansi\_alias +FDO shlW32M.lib  
C++: -fast -Qcxx\_features +FDO

### PEAK TUNING

164.gzip: -fast +FDO  
175.vpr: -fast +FDO  
176.gcc: basepeak=yes  
181.mcf: -fast +FDO shlW32M.lib  
186.crafty: -fast -Oa +FDO shlW32M.lib  
197.parser: -fast +FDO  
252.eon: -fast +FDO  
253.perlbnk: -fast -Oa +FDO shlW32M.lib



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## Notes/Tuning Information (Continued)

```
254.gap:      -fast +FDO
255.vortex    -fast +FDO shlw32M.lib
256.bzip2:    -fast -Qunroll1 -Oa +FDO shlw32M.lib
300.twolf:    -fast +FDO shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib:  MicroQuill SmartHeap Library 7.3
              www.microquill.com
```