



CINT2000 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S2, Intel Xeon processor 3.80GHz, 2MB L2

SPECint_rate2000 = 22.6

SPECint_rate_base2000 = 22.6

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Sep-2005

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 2 | 166 | 19.5 | 2 | 166 | 19.5 |
| 175.vpr | 2 | 217 | 14.9 | 2 | 217 | 14.9 |
| 176.gcc | 2 | 85.1 | 30.0 | 2 | 85.1 | 30.0 |
| 181.mcf | 2 | 208 | 20.1 | 2 | 208 | 20.1 |
| 186.crafy | 2 | 141 | 16.4 | 2 | 141 | 16.4 |
| 197.parser | 2 | 199 | 21.0 | 2 | 199 | 21.0 |
| 252.eon | 2 | 122 | 24.7 | 2 | 122 | 24.7 |
| 253.perlbnk | 2 | 175 | 23.9 | 2 | 175 | 23.9 |
| 254.gap | 2 | 96.8 | 26.4 | 2 | 96.8 | 26.4 |
| 255.vortex | 2 | 122 | 36.2 | 2 | 122 | 36.2 |
| 256.bzip2 | 2 | 180 | 19.3 | 2 | 180 | 19.3 |
| 300.twolf | 2 | 261 | 26.7 | 2 | 261 | 26.7 |

Hardware

CPU: Intel Xeon processor 3.80GHz, 2MB L2, 800MHz system bus
CPU MHz: 3800
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12k micro-ops I + 16KBD on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8x512 MB DDRII-RAM PC2-3200R
Disk Subsystem: Seagate ST336753LC(U320 SCSI, 15krpm)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1
Compiler: Intel C++ Compiler 9.0
Build 20050722Z (for 32-bit applications)
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries)
SmartHeap Library Version 7.4
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: Approved srcalt stdcpp is used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlW32M.lib
for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

New 32-bit Windows tools used, approved in May-2005

The system bus runs at 800 MHz



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Notes/Tuning Information (Continued)

This result was measured with 32-bit binaries using the 32-bit version of the operating system.

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