



# CFP2000 Result

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## Acer Incorporated

Acer Altos R710 (3.6 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECfp\_rate2000 = 21.9

SPECfp\_rate\_base2000 = 21.7

SPEC license #: 97 Tested by: Acer Incorporated Test date: Nov-2005 Hardware Avail: Oct-2005 Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	124	29.8	2	125	29.8
171.swim	2	264	27.2	2	263	27.3
172.mgrid	2	211	19.8	2	211	19.8
173.applu	2	297	16.4	2	297	16.4
177.mesa	2	174	18.7	2	174	18.7
178.galgel	2	184	36.5	2	184	36.6
179.art	2	161	37.5	2	161	37.5
183.quake	2	86.8	34.7	2	86.9	34.7
187.facerec	2	173	25.5	2	173	25.5
188.amp	2	371	13.8	2	321	15.9
189.lucas	2	225	20.6	2	224	20.7
191.fma3d	2	291	16.7	2	291	16.7
200.sixtrack	2	244	10.5	2	252	10.1
301.apsi	2	350	17.2	2	351	17.2

### Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Enable)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 512MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ 9.0 Build 20050430Z for IA32 and Fortran Compiler 9.0 Build 20050722Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
192.fma3d: Approved windowsdp src.alt used
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
```



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## Notes/Tuning Information (Continued)

189.lucas: -fast -Qprefetch- +FDO

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>