



CINT2000 Result

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IBM Corporation
IBM System p5 505 (1650 MHz, 1 CPU)

SPECint2000 = 1297
SPECint_base2000 = 1259

SPEC license #: 11 | Tested by: IBM | Test date: Sep-2005 | Hardware Avail: Oct-2005 | Software Avail: Oct-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	187	747	187	747	
175.vpr	1400	136	1029	136	1026	
176.gcc	1100	79.8	1378	79.8	1378	
181.mcf	1800	71.7	2512	65.1	2763	
186.crafty	1000	98.4	1016	98.4	1016	
197.parser	1800	160	1123	160	1122	
252.eon	1300	92.6	1404	91.1	1428	
253.perlbnk	1800	198	910	180	1000	
254.gap	1100	94.9	1159	94.9	1159	
255.vortex	1900	89.3	2129	83.3	2282	
256.bzip2	1500	127	1179	122	1232	
300.twolf	3000	213	1407	206	1457	

Hardware

CPU: POWER5
 CPU MHz: 1650
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 2 cores/chip (SMT off)
 CPU(s) orderable: 2
 Parallel: None
 Primary Cache: 64KBI+32KBD (on chip)/core
 Secondary Cache: 1920KB unified (on chip)/chip
 L3 Cache: 36MB unified (off-chip)/DCM, 1 DCM/SUT
 Other Cache: None
 Memory: 8x2GB
 Disk Subsystem: 1x73GB SCSI, 15K RPM
 Other Hardware: None

Software

Operating System: AIX 5L V5.3
 Compiler: XL C/C++ Enterprise Edition Version 8.0 for AIX
 File System: AIX/JFS2
 System State: Multi-user

Notes/Tuning Information

Portability Flags:

```
176.gcc: -ma -DHOST_WORDS_BIG_ENDIAN
186.crafty: -DAIX
252.eon: srcalt=fmax_errno
253.perlbnk: -DSPEC_CPU2000_AIX
254.gap: -DSYS_IS_BSD -DSYS_STRING_H
          -DSYS_HAS_MALLOC_PROTO -DSYS_HAS_CALLOC_PROTO
300.twolf: -DHAVE_SIGNED_CHAR
```

Base Optimization Flags:

```
C: -qpdf1/pdf2
   -O5 -blpdata -D_ILS_MACROS
C++: -qpdf1/pdf2
      -O5 -lhm -qalign=natural
```

Peak Optimization Flags

```
164.gzip: basepeak=1
175.vpr: -qpdf1/pdf2
         -O5 -lhm -blpdata -D_ILS_MACROS
176.gcc: basepeak=1
```

