



CFP2000 Result

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Acer Incorporated
Altos R510 (3.0 GHz Intel Xeon)

SPECfp2000 = 1404
SPECfp_base2000 = 1388

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2005 Hardware Avail: May-2005 Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	73.5	2175	73.6	2175	
171.swim	3100	157	1975	157	1980	
172.mgrid	1800	143	1261	143	1260	
173.applu	2100	164	1281	164	1281	
177.mesa	1400	105	1331	105	1331	
178.galgel	2900	145	1994	145	1995	
179.art	2600	110	2359	110	2359	
183.earthquake	1300	69.7	1864	69.8	1862	
187.facerec	1900	124	1529	124	1529	
188.amp	2200	309	711	263	835	
189.lucas	2000	115	1741	115	1744	
191.fma3d	2100	170	1232	170	1232	
200.sixtrack	1100	205	537	203	541	
301.apsi	2600	269	966	270	965	

Hardware

CPU: Intel Xeon (3.0 GHz, 1MB L2, 800MHz system bus)
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 512MB ECC DDR-333 SDRAM
Disk Subsystem: 1 x 80GB 7200RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran Programs : -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.earthquake:Base for C Program: -fast +FDO shlw32M.lib
188.amp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.earthquake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```



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Notes/Tuning Information (Continued)

187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
191.fma3d: basepeak=yes
200.sixtrack: -Qipo -QxP +FDO
301.apsi: -fast +FDO
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>