



CFP2000 Result

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Advanced Micro Devices
Tyan Thunder K7 Motherboard, 1.2GHz Athlon MP Processor

SPECfp2000 = **481**
SPECfp_base2000 = **433**

SPEC license #: 49 Tested by: AMD Test date: May-2001 Hardware Avail: Jun-2001 Software Avail: Jan-2001

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	200 400 600 800			
168.wupwise	1600	239	669	239	670	[Bar chart showing ratio 670]			
171.swim	3100	395	785	395	785	[Bar chart showing ratio 785]			
172.mgrid	1800	388	464	385	467	[Bar chart showing ratio 467]			
173.applu	2100	481	437	481	437	[Bar chart showing ratio 437]			
177.mesa	1400	211	662	207	677	[Bar chart showing ratio 677]			
178.galgel	2900	925	314	503	577	[Bar chart showing ratio 577]			
179.art	2600	754	345	719	362	[Bar chart showing ratio 362]			
183.quake	1300	366	355	259	502	[Bar chart showing ratio 502]			
187.facerec	1900	456	417	350	542	[Bar chart showing ratio 542]			
188.amp	2200	635	347	624	353	[Bar chart showing ratio 353]			
189.lucas	2000	417	480	415	482	[Bar chart showing ratio 482]			
191.fma3d	2100	470	446	424	496	[Bar chart showing ratio 496]			
200.sixtrack	1100	362	304	360	305	[Bar chart showing ratio 305]			
301.apsi	2600	826	315	778	334	[Bar chart showing ratio 334]			

Hardware

CPU: 1.2GHz AMD Athlon MP AHD1200AMS3C
CPU MHz: 1200
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1, 2
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 256KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 256MB PC2100 DDR SDRAM CL2 Registered
Disk Subsystem: IDE IBM Deskstar DTLA-307020
Other Hardware: None

Software

Operating System: Windows 2000 SP1
Compiler: Compaq Visual Fortran 6.5A
Intel C/C++ 5.0 build 001120
Intel Fortran 5.0 build 001120
Microsoft Visual Studio 6.0 (libraries)
MicroQuill Smartheap Library 5.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
icl and ifl are the Intel C/C++ and Fortran compilers
f90 is the Compaq Fortran compiler
shlw32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
Portability:
178.galgel: -FI -Fe$@ -link -stack:32000000
Baseline: C icl -QxK -Qipo +FDO
Baseline: Fortran ifl -O3 -QxK -Qipo +FDO
Peak tuning:
168.wupwise: ifl -O3 -QxK -Qipo +FDO
171.swim: ifl -O3 -QxK -Qipo +FDO
172.mgrid: ifl -O3 -QaxK -Qipo +FDO
173.applu: ifl -O3 -QxK -Qipo +FDO
177.mesa: icl -QxK -Qipo -Oa +FDO shlw32M.lib
178.galgel: f90 -Optimize:5 -fast
179.art: icl -QxK -Qipo -Oa +FDO shlw32M.lib
183.quake: icl -QxK -Qipo -Qrcd -Oa +FDO shlw32M.lib
```



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Notes/Tuning Information (Continued)

```
187.facerec: f90 -Optimize:5 -fast
188.ampp:    icl -QxK -Qipo -Oa +FDO
189.lucas:   ifl -O3 -QxK -Qipo +FDO shlW32M.lib
191.fma3d:   f90 -Optimize:5 -fast
200.sixtrack: ifl -QxK -Qipo +FDO
301.apsi:    f90 -Optimize:5 -fast
```

Library ordering for 189.lucas (to include SmartHeap correctly with default libs):

```
LIBS=libIEPCF90.lib libintrins.lib libF90.lib
libqwind.lib libm.lib shlW32M.lib LIBC.lib libirc.lib OLDNAMES.lib
ONESTEP is used for all base and peak runs
```

The tested system can be assembled using a case such as the Chenming 601 AE-F-O,
a 460W power supply such as the Delta Electronics Model DPS-465 AB A
The System bus runs at 266MHz