



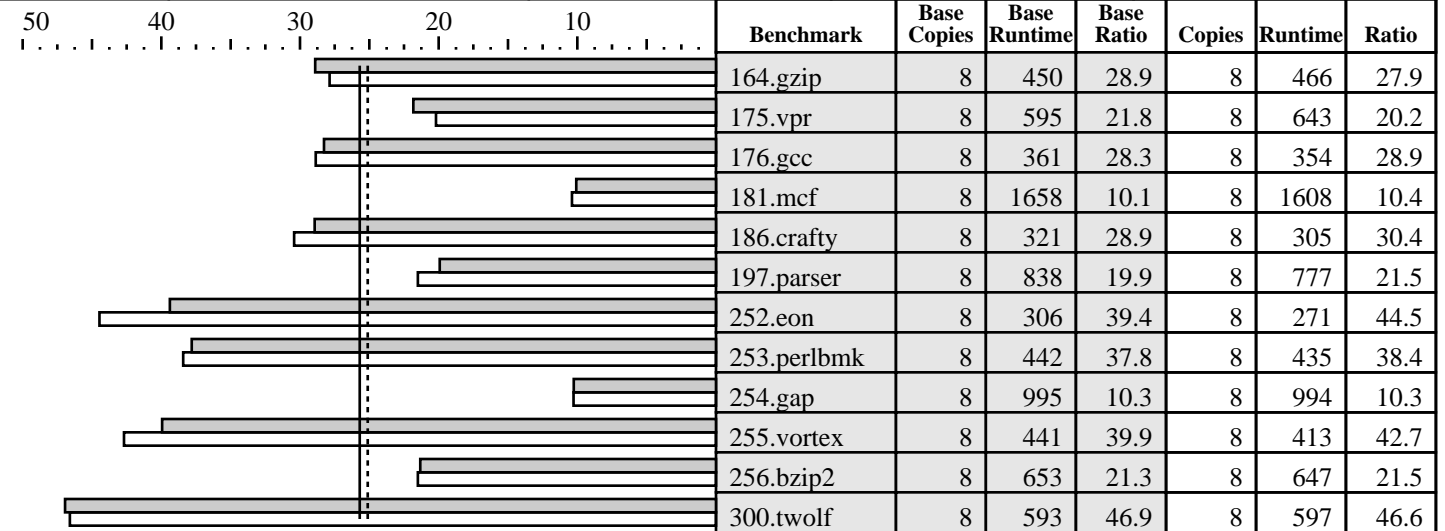
# CINT2000 Result

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## Dell PowerEdge 8450

SPECint\_rate2000 = 25.7  
SPECint\_rate\_base2000 = 25.1

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Apr-2001 | Hardware Avail: Mar-2000 | Software Avail: Oct-2000



### Hardware

CPU: Pentium III Xeon  
 CPU MHz: 700  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 8 chips, 1 core/chip  
 CPU(s) orderable: 1 to 8  
 Parallel: No  
 Primary Cache: 16KB(I) + 16KB(D) on chip  
 Secondary Cache: 2MB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 3GB SDRAM  
 Disk Subsystem: 1 x 9GB SEAGATE Ultra-2  
 Other Hardware:

### Software

Operating System: Microsoft Windows 2000 Advanced Server  
 Compiler: Intel C/C++ Compiler 5.0 Build 001004 for Windows NT  
 Microsoft Visual C++ 6.0 (for libraries)  
 MicroQuill SmartHeap Library 5.0  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

### FEEDBACK-DIRECTED OPTIMIZATION

+FDO: PASS1= -Qprof\_gen PASS2= -Qprof\_use

### BASE TUNING

C: -QxK -Qipo shlW32M.lib +FDO

C++: -QxK -Qipo -GX -GR

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### PEAK TUNING

164.gzip: -QxK -Qipo +FDO shlW32M.lib

175.vpr: -QxK -Qipo +FDO shlW32M.lib

176.gcc: -QxK -Qipo +FDO shlW32M.lib

181.mcf: -QxK -Qipo +FDO shlW32M.lib

186.crafty: -QxK -Qipo +FDO shlW32M.lib

197.parser: -QxK -Qipo +FDO shlW32M.lib

252.eon: -QxK -Qipo +FDO

253.perlbnk: -QxK -Qipo +FDO shlW32M.lib

254.gap: -QxK -Qipo +FDO shlW32M.lib



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## Notes/Tuning Information (Continued)

```
255.vortex: -QxK -Qipo +FDO -Oa shlw32M.lib
256.bzip2:  -QxK -Qipo +FDO -Oa shlw32M.lib
300.twolf:  -QxK -Qipo +FDO -Oa shlw32M.lib
LIBRARIES
shlw32M.lib: MicroQuill SmartHeap Library 5.0
              www.microquill.com
```