SPEC ACCEL™ OMP Result

Supermicro
(Test Sponsor: NVIDIA Corporation)

EPYC 7451
A+ Server 1023US-TR4

$SPECaccel\_omp\_peak = 3.18$

$SPECaccel\_omp\_base = 3.18$

ACCEL license:	019	**Test date:**	Aug-2018
Test sponsor:	NVIDIA Corporation	**Hardware Availability:**	Jul-2017
Tested by:	NVIDIA Corporation	**Software Availability:**	Aug-2018

Benchmark results (scale 0 to 19.0):

- **503.postencil**: 1.70
- **504.polbm**: 2.03
- **514.pomriq**: 1.23
- **550.pmd**: 1.84
- **551.ppalm**: 2.73
- **552.pep**: 1.38
- **553.pclvrleaf**: 4.09
- **554.pcg**: 4.55
- **555.pseismic**: 1.78
- **556.psp**: 9.47
- **557.pcsp**: 10.7
- **559.pmniGhost**: 3.35
- **560.pilbdc**: 3.57
- **563.pswim**: 1.60
- **570.pbt**: 18.6

SPECaccel_omp_base = 3.18
SPECaccel_omp_peak = 3.18

Hardware

CPU Name:	AMD EPYC 7451
CPU Characteristics:	
CPU MHz:	2300
CPU MHz Maximum:	3200
FPU:	Integrated
CPU(s) enabled:	48 cores, 2 chips, 24 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 32 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	64 MB I+D on chip per chip, 8 MB shared / 3 cores
Other Cache:	None

Accelerator

Accel Model Name:	EPYC 7451
Accel Vendor:	AMD
Accel Name:	EPYC 7451
Type of Accel:	CPU
Accel Connection:	Not Applicable
Does Accel Use ECC:	Yes
Accel Description:	AMD EPYC 48-core CPU
Accel Driver:	Not Applicable

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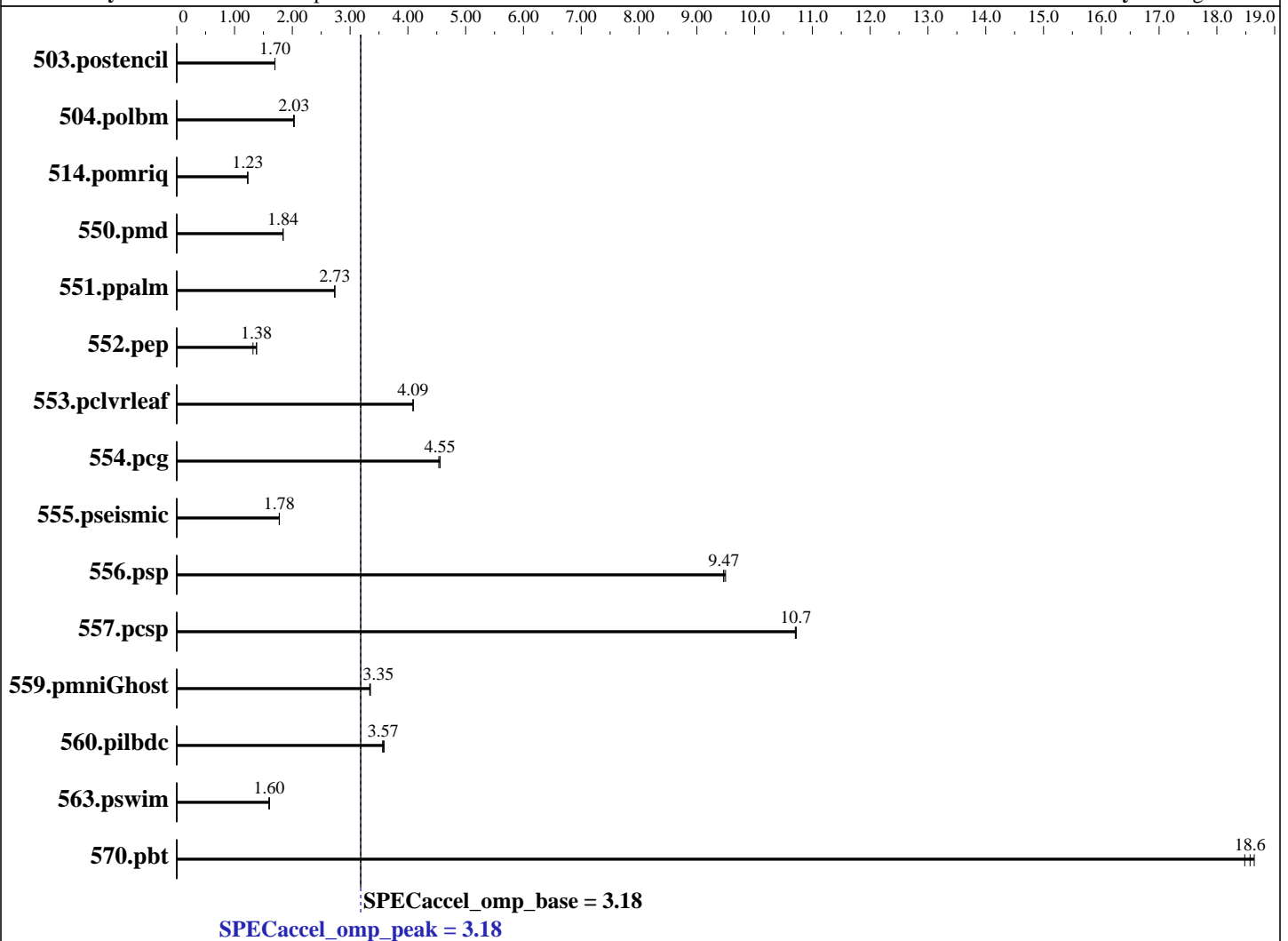
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Hardware (Continued)

Memory:	256 GB (16 x 16GB 4DRx4 PC4-2666V-L, running at 2667)
Disk Subsystem:	1 x 480 GB Intel SATA SSD (SSDSC2BB48)
Other Hardware:	None

Software

Operating System:	CentOS Linux release 7.4.1708 (Core) 4.15.0-1.el7.elrepo.x86_64
Compiler:	PGI Professional Edition, Release 18.7 LLVM
File System:	xfs
System State:	Run level 3 (multi-user)
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.postencil	**64.2**	**1.70**	64.1	1.70	64.2	1.70	**64.2**	**1.70**	64.1	1.70	64.2	1.70
504.polbm	60.3	2.02	60.0	2.03	**60.2**	**2.03**	60.3	2.02	60.0	2.03	**60.2**	**2.03**
514.pomriq	506	1.23	506	1.23	**506**	**1.23**	506	1.23	506	1.23	**506**	**1.23**
550.pmd	**131**	**1.84**	131	1.84	131	1.84	**131**	**1.84**	131	1.84	131	1.84
551.ppalm	199	2.73	**199**	**2.73**	199	2.74	199	2.73	**199**	**2.73**	199	2.74
552.pep	167	1.38	**168**	**1.38**	175	1.32	167	1.38	**168**	**1.38**	175	1.32
553.pclvrleaf	280	4.09	280	4.09	**280**	**4.09**	280	4.09	280	4.09	**280**	**4.09**
554.pcg	73.1	4.56	**73.1**	**4.55**	73.4	4.53	73.1	4.56	**73.1**	**4.55**	73.4	4.53
555.pseismic	**159**	**1.78**	159	1.78	159	1.77	**159**	**1.78**	159	1.78	159	1.77
556.psp	86.1	9.50	86.4	9.46	**86.4**	**9.47**	86.1	9.50	86.4	9.46	**86.4**	**9.47**
557.pcsp	**80.2**	**10.7**	80.1	10.7	80.2	10.7	**80.2**	**10.7**	80.1	10.7	80.2	10.7
559.pmniGhost	119	3.35	**119**	**3.35**	119	3.34	119	3.35	**119**	**3.35**	119	3.34
560.pilbdc	182	3.59	**183**	**3.57**	183	3.56	182	3.59	**183**	**3.57**	183	3.56
563.pswim	**99.4**	**1.60**	99.7	1.60	99.1	1.60	**99.4**	**1.60**	99.7	1.60	99.1	1.60
570.pbt	41.8	18.6	42.2	18.5	**42.0**	**18.6**	41.8	18.6	42.2	18.5	**42.0**	**18.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The config file option 'submit' was used.
   Submit command: numactl --interleave all $command
```

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Test date: Aug-2018
Hardware Availability: Jul-2017
Software Availability: Aug-2018

Platform Notes

```
Sysinfo program /local/home/colgrove/SPECACCEL/Docs/sysinfo
$Rev: 6965 $ $Date:: 2015-04-21 #$ c05a7f14b1b1765e3fe1df68447e8a35
running on epyc.pgi.net Mon Aug 13 11:10:06 2018

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/accel/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7451 24-Core Processor
      2 "physical id"s (chips)
      96 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 24
      siblings  : 48
      physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26
      28 29 30
      physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26
      28 29 30
   cache size : 512 KB

From /proc/meminfo
   MemTotal:       264029300 kB
   HugePages_Total:       20
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   CentOS Linux release 7.4.1708 (Core)

From /etc/*release* /etc/*version*
   centos-release: CentOS Linux release 7.4.1708 (Core)
   centos-release-upstream: Derived from Red Hat Enterprise Linux 7.4 (Source)
   hpe-mpi-release: HPE MPI 1.1, Build 717r22.rhel74-1711292100
   os-release:
      NAME="CentOS Linux"
      VERSION="7 (Core)"
      ID="centos"
      ID_LIKE="rhel fedora"
      VERSION_ID="7"
      PRETTY_NAME="CentOS Linux 7 (Core)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:centos:centos:7"
   redhat-release: CentOS Linux release 7.4.1708 (Core)
   sgi-release: SGI Performance Suite 1.15
   system-release: CentOS Linux release 7.4.1708 (Core)
   system-release-cpe: cpe:/o:centos:centos:7
```

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Platform Notes (Continued)

```
uname -a:
    Linux epyc.pgi.net 4.15.0-1.el7.elrepo.x86_64 #1 SMP Sun Jan 28 20:45:20 EST
    2018 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 1 13:00

SPEC is set to: /local/home/colgrove/SPECACCEL
    Filesystem                  Type  Size  Used Avail Use% Mounted on
    /dev/mapper/centos-root xfs   443G  161G  282G  37% /
Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.


(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
ACC_NUM_CORES = "48"
HUGETLB_PATH = "/mnt/hugetlb"
KMP_ALL_THREADS = "48"
OMP_PLACES = "{0},{1},{2},{3},{4},{5},{6},{7},{8},{9},{10},{11},{12},{13},{14},{15},{16},
    {17},{18},{19},{20},{21},{22},{23},{24},{25},{26},{27},{28},{29},{30},{31},{32},{33},
    {34},{35},{36},{37},{38},{39},{40},{41},{42},{43},{44},{45},{46},{47}"
OMP_PROC_BIND = "true"
OMP_THREAD_LIMIT = "48"

551.ppalm (base): "advec_ws_private" src.alt was used.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Base Compiler Invocation

C benchmarks:
    pgcc

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
pgfortran
```

Benchmarks using both Fortran and C:
```
pgcc pgfortran
```

Base Portability Flags

```
503.postencil: -DSPEC_USE_INNER_SIMD
   504.polbm: -DSPEC_USE_INNER_SIMD
  514.pomriq: -DSPEC_USE_INNER_SIMD
     550.pmd: -DSPEC_USE_INNER_SIMD
   551.ppalm: -DSPEC_USE_INNER_SIMD
     552.pep: -DSPEC_USE_INNER_SIMD
 553.pclvrleaf: -DSPEC_USE_INNER_SIMD
     554.pcg: -DSPEC_USE_INNER_SIMD
 555.pseismic: -DSPEC_USE_INNER_SIMD
     556.psp: -DSPEC_USE_INNER_SIMD
    557.pcsp: -DSPEC_USE_INNER_SIMD
 559.pmniGhost: -DSPEC_USE_INNER_SIMD
   560.pilbdc: -DSPEC_USE_INNER_SIMD
   563.pswim: -DSPEC_USE_INNER_SIMD
     570.pbt: -DSPEC_USE_INNER_SIMD
```

Base Optimization Flags

C benchmarks:
```
-fast -Mnouniform -Mhugetlb -mp -V18.7 -Mllvm
```

Fortran benchmarks:
```
-fast -Mnouniform -Mhugetlb -mp -V18.7 -Mllvm
```

Benchmarks using both Fortran and C:

```
553.pclvrleaf: -fast -Mnouniform -Mhugetlb -mp -V18.7 -Mllvm
```

```
559.pmniGhost: -fast -Mnouniform -Mhugetlb -mp -V18.7 -Mllvm -Mnomain
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

```
503.postencil: basepeak = yes

504.polbm: basepeak = yes

514.pomriq: basepeak = yes

552.pep: basepeak = yes

554.pcg: basepeak = yes

557.pcsp: basepeak = yes

570.pbt: basepeak = yes
```

Fortran benchmarks:

```
550.pmd: basepeak = yes

551.ppalm: basepeak = yes

555.pseismic: basepeak = yes

556.psp: basepeak = yes

560.pilbdc: basepeak = yes

563.pswim: basepeak = yes
```

Benchmarks using both Fortran and C:

```
553.pclvrleaf: basepeak = yes

559.pmniGhost: basepeak = yes
```

The flags files that were used to format this result can be browsed at
https://www.spec.org/accel/flags/PGI-Platform-Multicore-OMP.html
https://www.spec.org/accel/flags/pgi2018_flags.html

You can also download the XML flags sources by saving the following links:
https://www.spec.org/accel/flags/PGI-Platform-Multicore-OMP.xml
https://www.spec.org/accel/flags/pgi2018_flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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