



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_int\_base = 826

## PowerEdge C6620 (Intel Xeon Max 9470)

SPECrate®2017\_int\_peak = 849

CPU2017 License: 6573

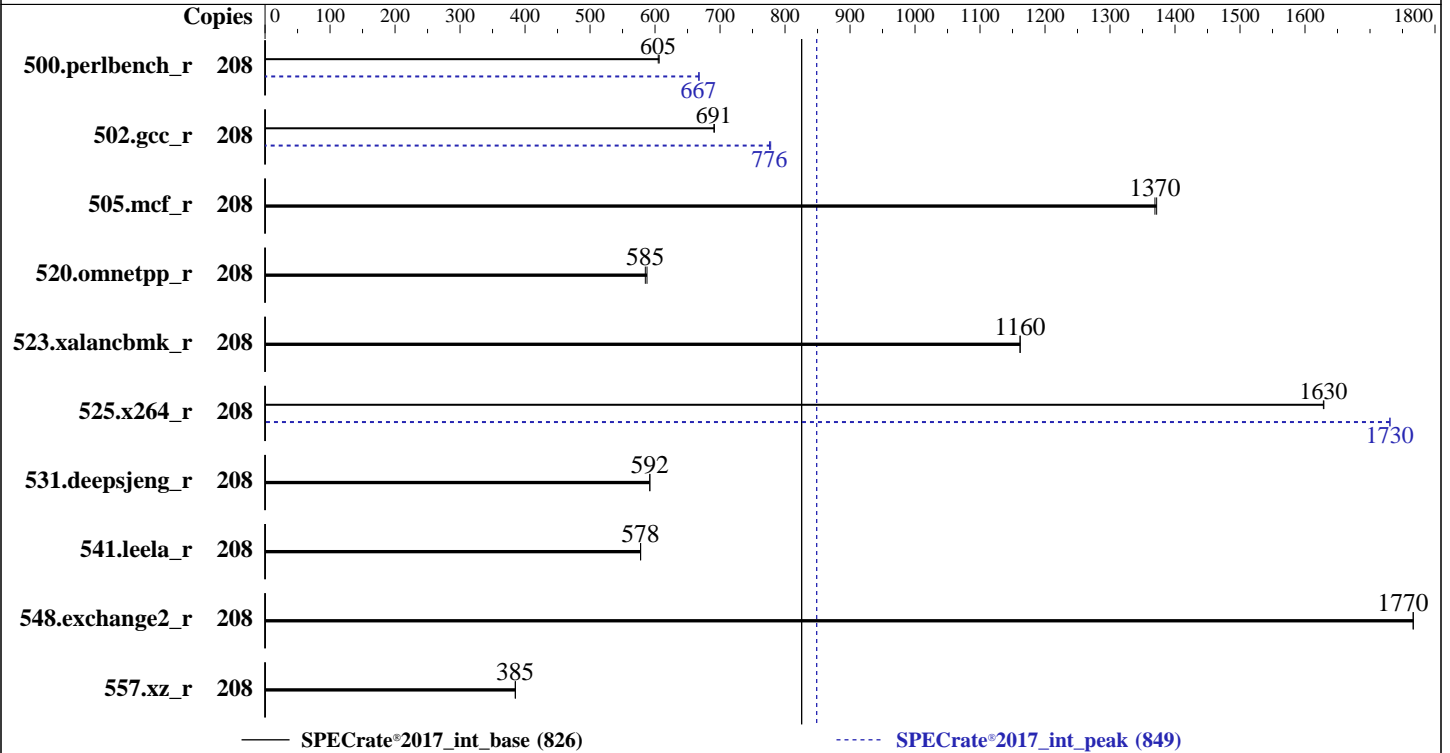
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: May-2023

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Max 9470  
 Max MHz: 3500  
 Nominal: 2000  
 Enabled: 104 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 105 MB I+D on chip per chip  
 Other: None  
 Memory: 1152 GB (16 x 64 GB 2Rx4 PC5-4800B-R + 8 x 16 GB HBM)  
 Storage: 120 GB on tmpfs  
 Other: CPU Cooling: DLC

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 2.1.3 released Jan-2024  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.  
 OS set to prefer power efficiency.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	208	546	606	<b><u>547</u></b>	<b><u>605</u></b>			208	496	668	<b><u>496</u></b>	<b><u>667</u></b>		
502.gcc_r	208	<b><u>427</u></b>	<b><u>691</u></b>	426	691			208	379	778	<b><u>379</u></b>	<b><u>776</u></b>		
505.mcf_r	208	245	1370	<b><u>246</u></b>	<b><u>1370</u></b>			208	245	1370	<b><u>246</u></b>	<b><u>1370</u></b>		
520.omnetpp_r	208	<b><u>466</u></b>	<b><u>585</u></b>	464	588			208	<b><u>466</u></b>	<b><u>585</u></b>	464	588		
523.xalancbmk_r	208	189	1160	<b><u>189</u></b>	<b><u>1160</u></b>			208	189	1160	<b><u>189</u></b>	<b><u>1160</u></b>		
525.x264_r	208	224	1630	<b><u>224</u></b>	<b><u>1630</u></b>			208	210	1730	<b><u>210</u></b>	<b><u>1730</u></b>		
531.deepsjeng_r	208	403	592	<b><u>403</u></b>	<b><u>592</u></b>			208	403	592	<b><u>403</u></b>	<b><u>592</u></b>		
541.leela_r	208	<b><u>596</u></b>	<b><u>578</u></b>	596	578			208	<b><u>596</u></b>	<b><u>578</u></b>	596	578		
548.exchange2_r	208	308	1770	<b><u>309</u></b>	<b><u>1770</u></b>			208	308	1770	<b><u>309</u></b>	<b><u>1770</u></b>		
557.xz_r	208	583	385	<b><u>584</u></b>	<b><u>385</u></b>			208	583	385	<b><u>584</u></b>	<b><u>385</u></b>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/lib/ia32:/mnt
/ramdisk/cpu2017-1.1.9-ic2024.0.2/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

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## General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 120 GB ramdisk created with the cmd: "mount -t tmpfs -o size=120G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

ADDDC Setting : Disabled  
 DIMM Self Healing on  
 Uncorrectable Memory Error : Disabled  
 HBM Mode : Cache

Virtualization Technology : Disabled  
 DCU Streamer Prefetcher : Disabled  
 Sub NUMA Cluster : 4-way Clustering  
 LLC Prefetch : Disabled  
 Dead Line LLC Alloc : Disabled

System Profile : Custom  
 CPU Power Management : Maximum Performance  
 C1E : Disabled  
 C States : Autonomous  
 Memory Patrol Scrub : Disabled  
 Energy Efficiency Policy : Performance  
 PCI ASPM L1 Link  
 Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo  
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
 running on localhost Thu Apr 4 20:35:35 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged

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## Platform Notes (Continued)

- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
20:35:35 up 23 min, 1 user, load average: 0.26, 0.06, 0.06
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root     tty1    -             20:35   23.00s  1.52s  0.00s  /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.1 --output_format html,pdf,txt
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4125949
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4125949
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.1 --output_format html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.1 --output_format html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 -c ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=104 --define physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak -o all --define DL-BIOS-SNC=4 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define DL-VERS=5.1 --output_format html,pdf,txt intrate
```

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## Platform Notes (Continued)

```
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 --configfile
ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=104 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --define
DL-BIOS-SNC=4 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-4.inc --define DL-BIOS-LogProc=1 --define
DL-BIOS-adddcD=1 --define DL-VERS=5.1 --output_format html,pdf,txt --nopower --runmode rate --tune
base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2
```

### 6. /proc/cpuinfo

```
model name      : Intel (R) Xeon (R) CPU Max 9470
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2c000351
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 52
siblings       : 104
2 physical ids (chips)
208 processors (hardware threads)
physical id 0: core ids 0-51
physical id 1: core ids 0-51
physical id 0: apicids 0-103
physical id 1: apicids 128-231
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      46 bits physical, 57 bits virtual
Byte Order:        Little Endian
CPU(s):            208
On-line CPU(s) list: 0-207
Vendor ID:         GenuineIntel
Model name:        Intel (R) Xeon (R) CPU Max 9470
CPU family:        6
Model:             143
Thread(s) per core: 2
Core(s) per socket: 52
Socket(s):         2
Stepping:          8
CPU max MHz:       3500.0000
CPU min MHz:       800.0000
BogoMIPS:          4000.00
Flags:             fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                  clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                  lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                  nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                  ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                  x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                  abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                  cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                  avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
```

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### Platform Notes (Continued)

```
avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
```

Lld cache:	4.9 MiB (104 instances)
Lli cache:	3.3 MiB (104 instances)
L2 cache:	208 MiB (104 instances)
L3 cache:	210 MiB (2 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120,128,136,144,152,160,168,176,184,192,200
NUMA node1 CPU(s):	4,12,20,28,36,44,52,60,68,76,84,92,100,108,116,124,132,140,148,156,164,172,180,188,196,204
NUMA node2 CPU(s):	2,10,18,26,34,42,50,58,66,74,82,90,98,106,114,122,130,138,146,154,162,170,178,186,194,202
NUMA node3 CPU(s):	6,14,22,30,38,46,54,62,70,78,86,94,102,110,118,126,134,142,150,158,166,174,182,190,198,206
NUMA node4 CPU(s):	1,9,17,25,33,41,49,57,65,73,81,89,97,105,113,121,129,137,145,153,161,169,177,185,193,201
NUMA node5 CPU(s):	5,13,21,29,37,45,53,61,69,77,85,93,101,109,117,125,133,141,149,157,165,173,181,189,197,205
NUMA node6 CPU(s):	3,11,19,27,35,43,51,59,67,75,83,91,99,107,115,123,131,139,147,155,163,171,179,187,195,203
NUMA node7 CPU(s):	7,15,23,31,39,47,55,63,71,79,87,95,103,111,119,127,135,143,151,159,167,175,183,191,199,207
Vulnerability Itlb multihit:	Not affected
Vulnerability Lltf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
Lld	48K	4.9M	12	Data	1	64	1	64
Lli	32K	3.3M	8	Instruction	1	64	1	64
L2	2M	208M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120,128,136,144,152,160,168,176,184,192,200
node 0 size: 128434 MB
node 0 free: 128085 MB
node 1 cpus: 4,12,20,28,36,44,52,60,68,76,84,92,100,108,116,124,132,140,148,156,164,172,180,188,196,204
node 1 size: 129017 MB
node 1 free: 128755 MB
node 2 cpus: 2,10,18,26,34,42,50,58,66,74,82,90,98,106,114,122,130,138,146,154,162,170,178,186,194,202
node 2 size: 129017 MB
```

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## Platform Notes (Continued)

```

node 2 free: 128634 MB
node 3 cpus: 6,14,22,30,38,46,54,62,70,78,86,94,102,110,118,126,134,142,150,158,166,174,182,190,198,206
node 3 size: 129017 MB
node 3 free: 128725 MB
node 4 cpus: 1,9,17,25,33,41,49,57,65,73,81,89,97,105,113,121,129,137,145,153,161,169,177,185,193,201
node 4 size: 129017 MB
node 4 free: 128666 MB
node 5 cpus: 5,13,21,29,37,45,53,61,69,77,85,93,101,109,117,125,133,141,149,157,165,173,181,189,197,205
node 5 size: 129017 MB
node 5 free: 128660 MB
node 6 cpus: 3,11,19,27,35,43,51,59,67,75,83,91,99,107,115,123,131,139,147,155,163,171,179,187,195,203
node 6 size: 129017 MB
node 6 free: 128619 MB
node 7 cpus: 7,15,23,31,39,47,55,63,71,79,87,95,103,111,119,127,135,143,151,159,167,175,183,191,199,207
node 7 size: 128978 MB
node 7 free: 117108 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 17 17 17 26 26 26 26
  1: 17 10 17 17 26 26 26 26
  2: 17 17 10 17 26 26 26 26
  3: 17 17 17 10 26 26 26 26
  4: 26 26 26 26 10 17 17 17
  5: 26 26 26 26 17 10 17 17
  6: 26 26 26 26 17 17 10 17
  7: 26 26 26 26 17 17 17 10

```

```

-----
9. /proc/meminfo
   MemTotal:      1056273768 kB

```

```

-----
10. who -r
    run-level 3 Apr 4 20:13

```

```

-----
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
    Default Target   Status
    multi-user       running

```

```

-----
12. Services, from systemctl list-unit-files
STATE                               UNIT FILES
enabled                             YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager firewalld getty@
                                     irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections
                                     postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4
                                     wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime                     systemd-remount-fs
disabled                             autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                                     chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info gpm
                                     grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys
                                     kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvme-autoconnect rpcbind
                                     rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                                     systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                                     systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
indirect                             wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default

```

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## Platform Notes (Continued)

```
root=UUID=a980d06c-c41f-4c9d-8603-80e11879418a
splash=silent
mitigations=auto
quiet
security=apparmor
```

### 14. cpupower frequency-info

```
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.50 GHz.
                  The governor "powersave" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
```

### 15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

### 16. /sys/kernel/mm/transparent\_hugepage

```
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
```

### 18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
```

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017\_int\_base = 826

PowerEdge C6620 (Intel Xeon Max 9470)

SPECrate®2017\_int\_peak = 849

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: May-2023  
Software Availability: Dec-2023

## Platform Notes (Continued)

### 19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 120G 5.7G 115G 5% /mnt/ramdisk

### 20. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge C6620  
Product Family: PowerEdge  
Serial: SC662L1

### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

14x 00AD063200AD HMCG94AEBRA109N 64 GB 2 rank 4800  
2x 00AD063200AD HMCG94MEBRA109N 64 GB 2 rank 4800  
8x 00CE 43 16 GB 3200

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 2.1.3  
BIOS Date: 01/10/2024  
BIOS Revision: 2.1

## Compiler Version Notes

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.0.2 Build 20231213  
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C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)

(Continued on next page)



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Hardware Availability: May-2023  
Software Availability: Dec-2023

## Compiler Version Notes (Continued)

| 557.xz\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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-----

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
541.leela\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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-----

=====  
Fortran | 548.exchange2\_r(base, peak)  
-----

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
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-----

## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64



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Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: May-2023  
Software Availability: Dec-2023

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```



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Test Date: Apr-2024

Hardware Availability: May-2023

Software Availability: Dec-2023

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

```
502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.0/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.9.html>



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**Tested by:** Dell Inc.

**Test Date:** Apr-2024

**Hardware Availability:** May-2023

**Software Availability:** Dec-2023

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.9.xml>

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