



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_fp\_base = 304

ThinkSystem ST650 V3  
(3.90 GHz, Intel Xeon Gold 6534)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9017

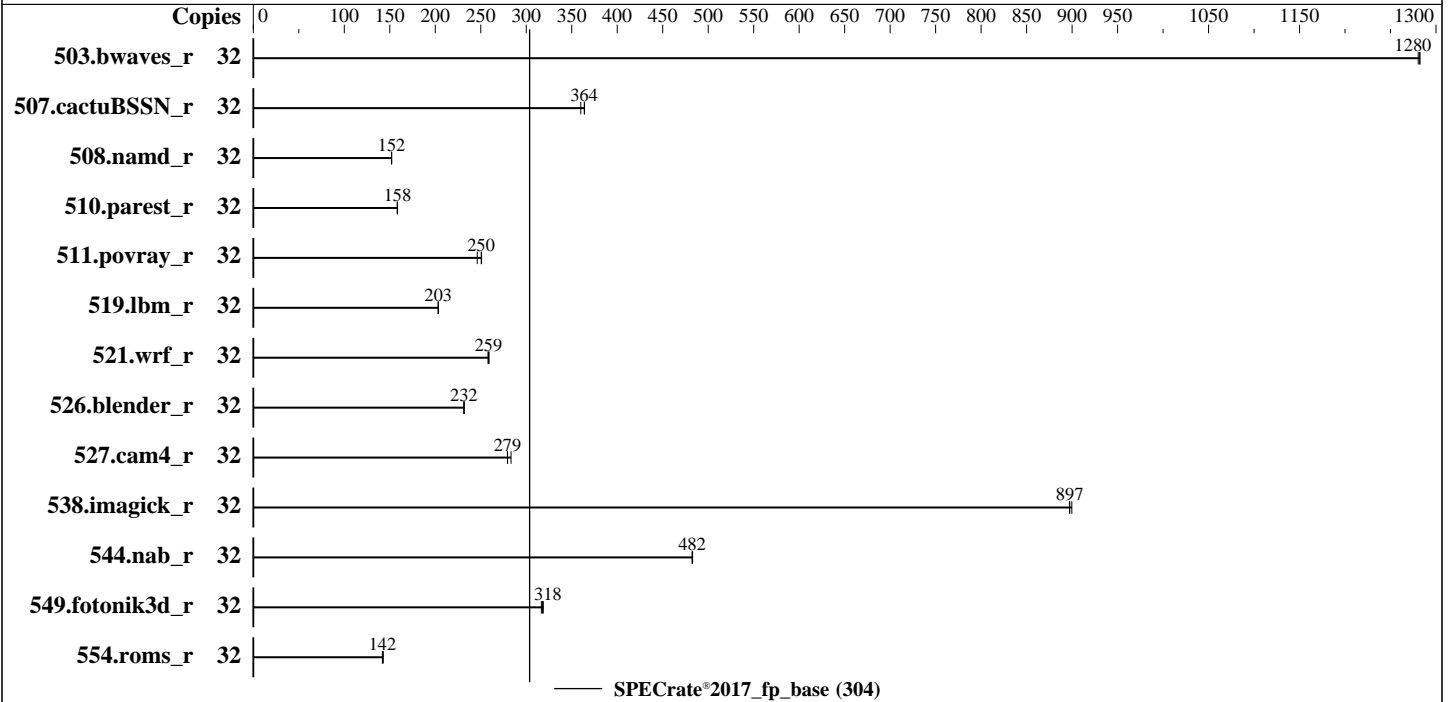
Test Date: Jun-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jun-2024

Tested by: Lenovo Global Technology

Software Availability: Mar-2024



### Hardware

CPU Name: Intel Xeon Gold 6534  
 Max MHz: 4200  
 Nominal: 3900  
 Enabled: 16 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 22.5 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
 Storage: 1 x 480 GB SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 Kernel 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Lenovo BIOS Version USE127C 4.20 released Apr-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	<u>250</u>	<u>1280</u>	250	1280	251	1280							
507.cactuBSSN_r	32	<u>111</u>	<u>364</u>	113	360	111	364							
508.namd_r	32	200	152	200	152	<u>200</u>	<u>152</u>							
510.parest_r	32	529	158	<u>528</u>	<u>158</u>	528	159							
511.povray_r	32	<u>298</u>	<u>250</u>	298	251	304	246							
519.lbm_r	32	166	203	166	203	<u>166</u>	<u>203</u>							
521.wrf_r	32	278	258	277	259	<u>277</u>	<u>259</u>							
526.blender_r	32	210	232	<u>210</u>	<u>232</u>	211	231							
527.cam4_r	32	198	283	200	279	<u>200</u>	<u>279</u>							
538.imagick_r	32	88.7	897	88.5	900	<u>88.7</u>	<u>897</u>							
544.nab_r	32	112	482	112	483	<u>112</u>	<u>482</u>							
549.fotonik3d_r	32	391	319	394	317	<u>392</u>	<u>318</u>							
554.roms_r	32	<u>358</u>	<u>142</u>	356	143	358	142							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017-1.1.9-ic2024.1/lib/intel64:/home/cpu2017-1.1.9-ic2024.1/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



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### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
C-States set to Legacy  
SNC set to SNC2  
LLC Prefetch set to Disabled  
AMP Prefetch set to Enable

Sysinfo program /home/cpu2017-1.1.9-ic2024.1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Mon Jun 3 15:04:04 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

2. w  
15:04:04 up 2 min, 1 user, load average: 0.00, 0.00, 0.00  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

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### Platform Notes (Continued)

```
root      tty1      -                15:03  36.00s  0.77s  0.01s  sh
Run503-compliant-ic2024.1-lin-sapphirerapids-ratefp-base-smt-on-20240308.sh
```

-----  
3. Username

From environment variable \$USER: root

-----  
4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4127088
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes      (-u) 4127088
virtual memory          (kbytes, -v) unlimited
file locks               (-x) unlimited
```

-----  
5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./run_ST650V3_EMR_new.sh
sh Run503-compliant-ic2024.1-lin-sapphirerapids-ratefp-base-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.023/templogs/preenv.fprate.023.0.log --lognum 023.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2024.1
```

-----  
6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) GOLD 6534
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping        : 2
microcode       : 0x21000230
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores       : 8
siblings        : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
```

(Continued on next page)



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### Platform Notes (Continued)

physical id 1: apicids 128-143

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:   0-31
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) GOLD 6534
CPU family:            6
Model:                 207
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             2
Stepping:              2
BogoMIPS:              7800.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                        vmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                        bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities

Virtualization:        VT-x
L1d cache:             768 KiB (16 instances)
L1i cache:             512 KiB (16 instances)
L2 cache:              32 MiB (16 instances)
L3 cache:              45 MiB (2 instances)
NUMA node(s):          4
NUMA node0 CPU(s):    0-3,16-19
NUMA node1 CPU(s):    4-7,20-23
NUMA node2 CPU(s):    8-11,24-27
NUMA node3 CPU(s):    12-15,28-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
                        sequence

```

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### Platform Notes (Continued)

Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-3,16-19
node 0 size: 257676 MB
node 0 free: 257180 MB
node 1 cpus: 4-7,20-23
node 1 size: 258045 MB
node 1 free: 257557 MB
node 2 cpus: 8-11,24-27
node 2 size: 258045 MB
node 2 free: 257640 MB
node 3 cpus: 12-15,28-31
node 3 size: 258033 MB
node 3 free: 257647 MB
node distances:
node  0  1  2  3
0:  10 12 21 21
1:  12 10 21 21
2:  21 21 10 12
3:  21 21 12 10
```

9. /proc/meminfo

MemTotal: 1056565384 kB

10. who -r

run-level 3 Jun 3 15:01

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```
Default Target Status
multi-user      running
```

12. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofsd autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievd
issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind
rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd
```

(Continued on next page)



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### Platform Notes (Continued)

indirect            wickedd

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=d1343b23-a61e-463a-96fe-4b32a83e08cb
splash=silent
mitigations=auto
quiet
security=apparmor

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing            1
kernel.randomize_va_space       2
vm.compaction_proactiveness     20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                   0
vm.dirty_expire_centisecs       3000
vm.dirty_ratio                   20
vm.dirty_writeback_centisecs     500
vm.dirtytime_expire_seconds     43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio           1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages      0
vm.swappiness                    60
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag                    always defer defer+madvice [madvice] never
enabled                   [always] madvice never
hpage_pmd_size           2097152
shmem_enabled            always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs    60000
defrag                    1
max_ptes_none            511
max_ptes_shared          256
max_ptes_swap            64
pages_to_scan            4096
scan_sleep_millisecs     10000

```

18. OS release

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### Platform Notes (Continued)

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP5

-----  
19. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2024.1  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda3 xfs 445G 43G 403G 10% /

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem ST650 V3  
Product Family: ThinkSystem  
Serial: MDSN00110D

-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
12x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 4800  
4x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4800

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: USE127C-4.20  
BIOS Date: 04/10/2024  
BIOS Revision: 4.20  
Firmware Revision: 5.10

### Compiler Version Notes

-----  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

-----  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

-----  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
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### Compiler Version Notes (Continued)

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using both C and C++:  
icpx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

SPECrate®2017\_fp\_base = 304

ThinkSystem ST650 V3  
(3.90 GHz, Intel Xeon Gold 6534)

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jun-2024

Hardware Availability: Jun-2024

Software Availability: Mar-2024

## Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

C++ benchmarks:

```

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

Fortran benchmarks:

```

-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

Benchmarks using both Fortran and C:

```

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

Benchmarks using both C and C++:

```

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512

```

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## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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