



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_fp\_base = 134

### PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573

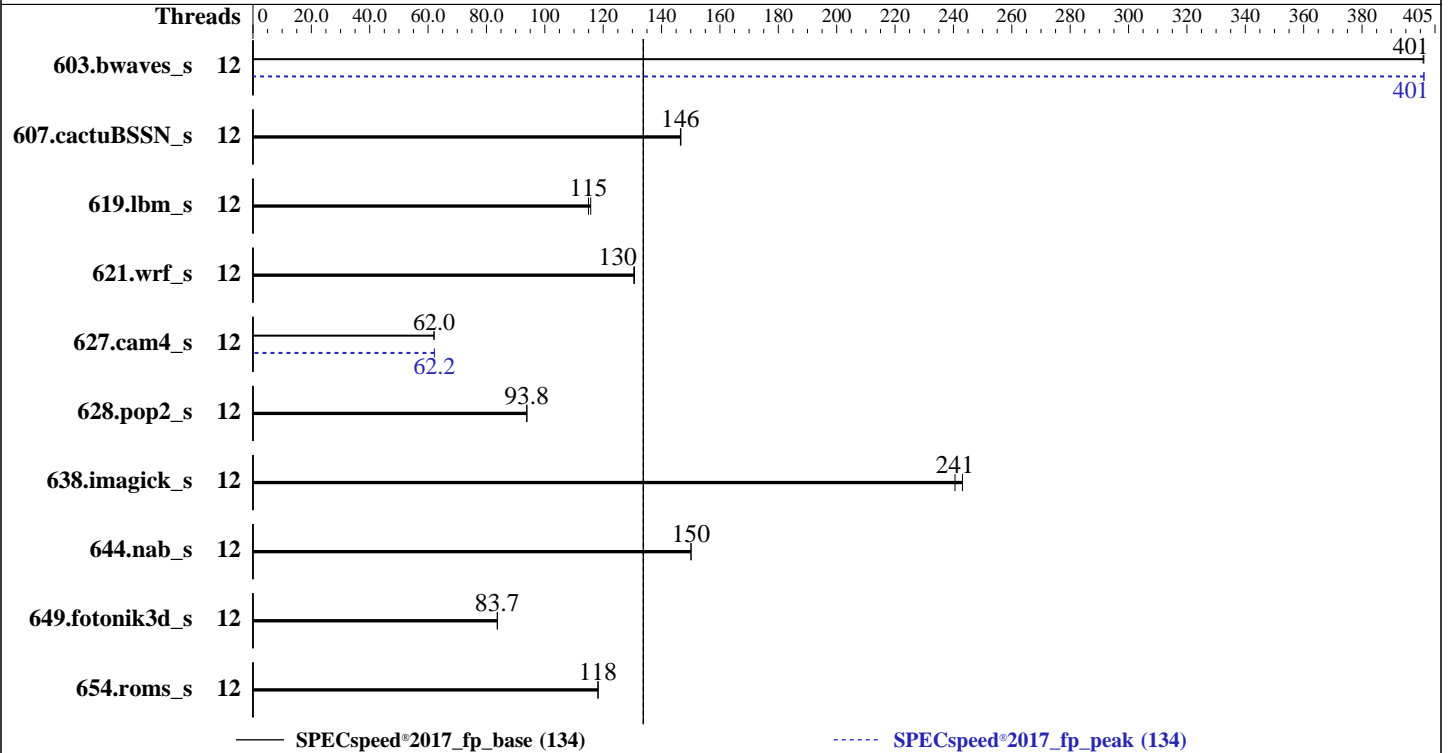
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Silver 4510  
 Max MHz: 4100  
 Nominal: 2400  
 Enabled: 12 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 30 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (8 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)  
 Storage: 40 GB on tmpfs  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
 Parallel: Yes  
 Firmware: Version 1.9.3 released Mar-2024  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECSpeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	12	<b><u>147</u></b>	<b><u>401</u></b>	147	401			12	<b><u>147</u></b>	<b><u>401</u></b>	147	401		
607.cactuBSSN_s	12	<b><u>114</u></b>	<b><u>146</u></b>	114	147			12	<b><u>114</u></b>	<b><u>146</u></b>	114	147		
619.lbm_s	12	45.3	116	<b><u>45.6</u></b>	<b><u>115</u></b>			12	45.3	116	<b><u>45.6</u></b>	<b><u>115</u></b>		
621.wrf_s	12	101	131	<b><u>101</u></b>	<b><u>130</u></b>			12	101	131	<b><u>101</u></b>	<b><u>130</u></b>		
627.cam4_s	12	<b><u>143</u></b>	<b><u>62.0</u></b>	143	62.1			12	143	62.2	<b><u>143</u></b>	<b><u>62.2</u></b>		
628.pop2_s	12	<b><u>127</u></b>	<b><u>93.8</u></b>	126	93.9			12	<b><u>127</u></b>	<b><u>93.8</u></b>	126	93.9		
638.imagick_s	12	<b><u>60.0</u></b>	<b><u>241</u></b>	59.3	243			12	<b><u>60.0</u></b>	<b><u>241</u></b>	59.3	243		
644.nab_s	12	<b><u>116</u></b>	<b><u>150</u></b>	116	150			12	<b><u>116</u></b>	<b><u>150</u></b>	116	150		
649.fotonik3d_s	12	109	83.8	<b><u>109</u></b>	<b><u>83.7</u></b>			12	109	83.8	<b><u>109</u></b>	<b><u>83.7</u></b>		
654.roms_s	12	133	118	<b><u>133</u></b>	<b><u>118</u></b>			12	133	118	<b><u>133</u></b>	<b><u>118</u></b>		

SPECSpeed®2017\_fp\_base = **134**

SPECSpeed®2017\_fp\_peak = **134**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH =  
"/mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECSpeed®2017\_fp\_peak = 134

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023

## Platform Notes

### BIOS settings:

ADDC Setting : Disabled  
DIMM Self Healing on  
Uncorrectable Memory Error : Disabled

Logical Processor : Disabled  
Virtualization Technology : Disabled  
DCU Streamer Prefetcher : Disabled

System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
Memory Patrol Scrub : Disabled  
Energy Efficiency Policy : Performance  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Apr 23 08:02:48 2024

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

```

```

2. w
08:02:48 up 5:50, 3 users, load average: 2.78, 4.37, 3.00

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

```

USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1    -             02:15   2:19m  1.10s  0.00s /bin/bash
/home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt

```

```

-----
3. Username
   From environment variable $USER:  root

```

```

-----
4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size           (kbytes, -d) unlimited
   scheduling priority     (-e) 0
   file size                (blocks, -f) unlimited
   pending signals         (-i) 2061122
   max locked memory       (kbytes, -l) 64
   max memory size         (kbytes, -m) unlimited
   open files              (-n) 1024
   pipe size                (512 bytes, -p) 8
   POSIX message queues    (bytes, -q) 819200
   real-time priority      (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time                (seconds, -t) unlimited
   max user processes      (-u) 2061122
   virtual memory          (kbytes, -v) unlimited
   file locks              (-x) unlimited

```

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc
--define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
/bin/bash /home/DellFiles/bin/Intel/dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc
--define DL-BIOS-adddcD=1 --define DL-VERS=5.2 --output_format html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.0.2-lin-sapphirerapids-speed-20231213.cfg --define cores=12 --tune base,peak -o all --define
drop_caches --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-adddcD=1 --define
DL-VERS=5.2 --output_format html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.0.2-lin-sapphirerapids-speed-20231213.cfg --define cores=12 --tune base,peak --output_format all
--define drop_caches --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-adddcD=1
--define DL-VERS=5.2 --output_format html,pdf,txt --nopower --runmode speed --tune base:peak --size
refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/temlogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2

```

```

-----
6. /proc/cpuinfo
   model name      : INTEL(R) XEON(R) SILVER 4510
   vendor_id      : GenuineIntel
   cpu family     : 6
   model          : 143
   stepping       : 8

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

microcode : 0x2b0005c0  
bugs : spectre\_v1 spectre\_v2 spec\_store\_bypass swapgs eibrs\_pbrsb  
cpu cores : 12  
siblings : 12  
1 physical ids (chips)  
12 processors (hardware threads)  
physical id 0: core ids 0-11  
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) SILVER 4510
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 12
Socket(s): 1
Stepping: 8
CPU max MHz: 4100.0000
CPU min MHz: 800.0000
BogoMIPS: 4800.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsavec cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities

L1d cache: 576 KiB (12 instances)
L1i cache: 384 KiB (12 instances)
L2 cache: 24 MiB (12 instances)
L3 cache: 30 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-11
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRBSB-eIBRS SW sequence  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	576K	12	Data	1	64	1	64
L1i	32K	384K	8	Instruction	1	64	1	64
L2	2M	24M	16	Unified	2	2048	1	64
L3	30M	30M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)  
node 0 cpus: 0-11  
node 0 size: 515311 MB  
node 0 free: 496502 MB  
node distances:  
node 0  
0: 10

9. /proc/meminfo

MemTotal: 527679208 kB

10. who -r

run-level 3 Apr 23 02:15 last=5

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target Status  
graphical running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager firewalld getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nsd nvme-fc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info gpm grub2-once haveged haveged-switch-root ipmi ipmievdev issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb nvmmf-autoconnect ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=9e20eb74-d7e2-4c8f-a983-577420e73ea7

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 4.10 GHz.
                  The governor "powersave" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Platform Notes (Continued)

### 19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2024.0.2  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 40G 12G 29G 29% /mnt/ramdisk

### 20. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge XR8620t  
Product Family: PowerEdge  
Serial: SLX8621

### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
8x 002C0632002C MTC40F2046S1RC56BG1 64 GB 2 rank 5600, configured at 4400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.9.3  
BIOS Date: 03/20/2024  
BIOS Revision: 1.9

## Compiler Version Notes

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

(Continued on next page)





# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Jun-2024  
Software Availability: Dec-2023

## Compiler Version Notes (Continued)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:  
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC\_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 134

PowerEdge XR8620t (Intel Xeon Silver 4510)

SPECspeed®2017\_fp\_peak = 134

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Jun-2024

Software Availability: Dec-2023

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-04-23 08:02:47-0400.  
Report generated on 2024-07-01 10:34:52 by CPU2017 PDF formatter v6716.  
Originally published on 2024-06-29.