



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

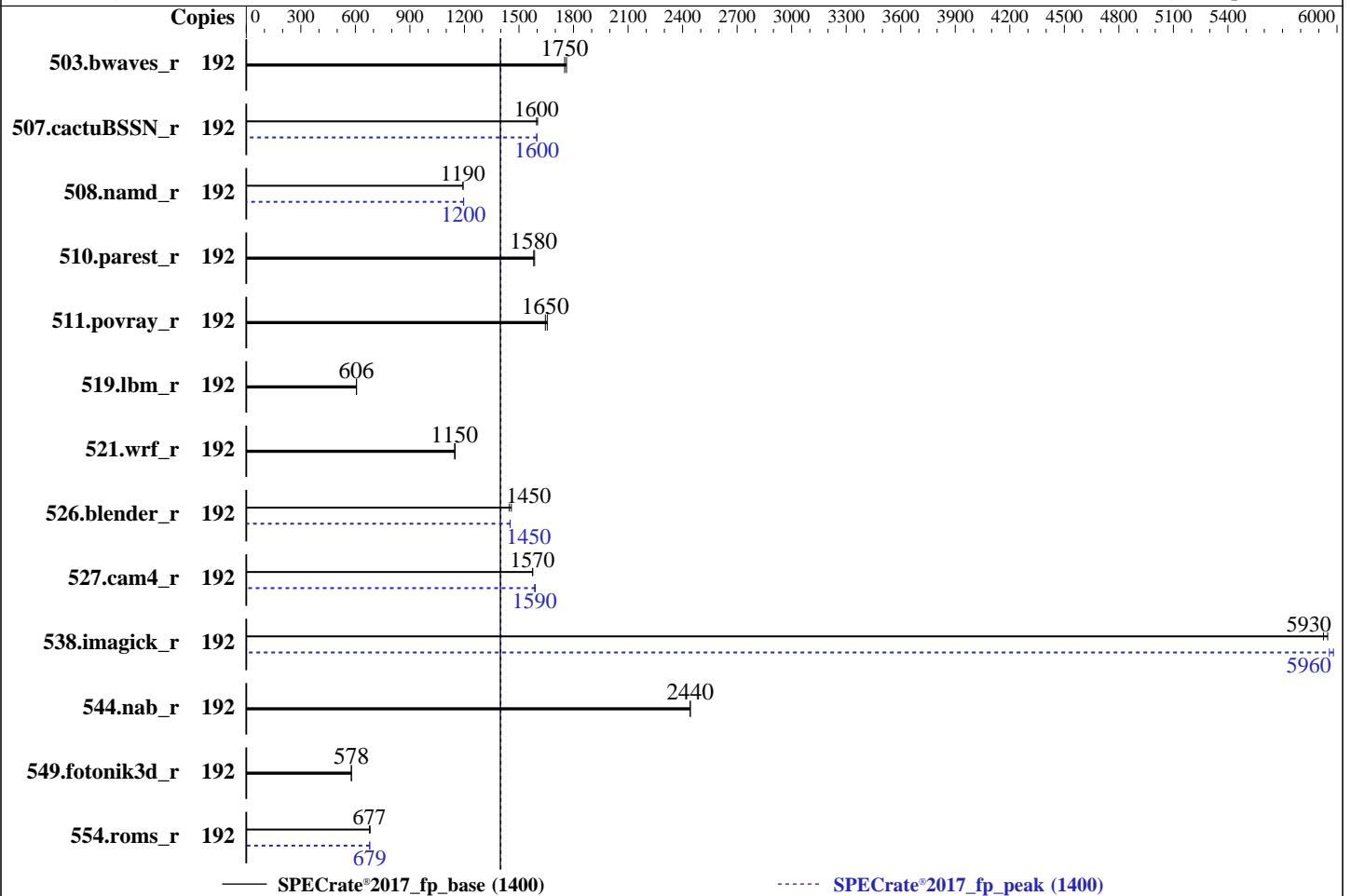
Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Sep-2023



### Hardware

CPU Name: AMD EPYC 9654  
 Max MHz: 3700  
 Nominal: 2400  
 Enabled: 192 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 110 GB on tmpfs  
 Other: CPU Cooling: DLC

### Software

OS: Ubuntu 22.04.3 LTS  
 5.15.0-84-generic  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 1.4.6 released Jul-2023  
 File System: tmpfs  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2024  
Hardware Availability: Feb-2023  
Software Availability: Sep-2023

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	<u>1099</u>	<u>1750</u>	1093	1760			192	<u>1099</u>	<u>1750</u>	1093	1760		
507.cactuBSSN_r	192	<u>152</u>	<u>1600</u>	152	1600			192	<u>152</u>	<u>1600</u>	152	1600		
508.namd_r	192	<u>153</u>	<u>1190</u>	153	1190			192	153	1200	<u>153</u>	<u>1200</u>		
510.parest_r	192	317	1590	<u>318</u>	<u>1580</u>			192	317	1590	<u>318</u>	<u>1580</u>		
511.povray_r	192	<u>273</u>	<u>1650</u>	271	1660			192	<u>273</u>	<u>1650</u>	271	1660		
519.lbm_r	192	<u>334</u>	<u>606</u>	334	606			192	<u>334</u>	<u>606</u>	334	606		
521.wrf_r	192	<u>375</u>	<u>1150</u>	374	1150			192	<u>375</u>	<u>1150</u>	374	1150		
526.blender_r	192	201	1460	<u>202</u>	<u>1450</u>			192	<u>201</u>	<u>1450</u>	201	1450		
527.cam4_r	192	<u>213</u>	<u>1570</u>	213	1580			192	211	1590	<u>212</u>	<u>1590</u>		
538.imagick_r	192	<u>80.6</u>	<u>5930</u>	80.3	5950			192	<u>80.1</u>	<u>5960</u>	79.8	5980		
544.nab_r	192	<u>132</u>	<u>2440</u>	132	2440			192	<u>132</u>	<u>2440</u>	132	2440		
549.fotonik3d_r	192	<u>1294</u>	<u>578</u>	1294	578			192	<u>1294</u>	<u>578</u>	1294	578		
554.roms_r	192	448	681	<u>450</u>	<u>677</u>			192	449	679	<u>450</u>	<u>679</u>		

SPECrate®2017\_fp\_base = 1400

SPECrate®2017\_fp\_peak = 1400

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Sep-2023

## Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
  "/mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-Al.1/amd_rate_aocc400_znver4_A_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-Al.1/amd_rate_aocc400_znver4_A_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 110 GB ramdisk created with the cmd: "mount -t tmpfs -o size=110G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

DRAM Refresh Delay : Performance  
DIMM Self Healing on  
Uncorrectable Memory Error : Disabled

Logical Processor : Disabled  
Virtualization Technology : Disabled  
NUMA Nodes per Socket : 4

System Profile : Custom  
C-States : Disabled  
Memory Patrol Scrub : Disabled  
PCI ASPM L1 Link  
Power Management : Disabled  
Determinism Slider : Power Determinism

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-Al.1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on amd-spa Wed Apr 10 18:40:09 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Apr-2024

**Hardware Availability:** Feb-2023

**Software Availability:** Sep-2023

## Platform Notes (Continued)

2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.10)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
-----
1. uname -a
Linux amd-spa 5.15.0-84-generic #93-Ubuntu SMP Tue Sep 5 17:16:10 UTC 2023 x86_64 x86_64 x86_64 GNU/Linux
```

```
-----
2. w
18:40:09 up 2:38, 1 user, load average: 102.85, 167.71, 181.31
USER  TTY  FROM          LOGIN@  IDLE   JCPU   PCPU WHAT
root  tty1  -             16:02   2:37m  2.10s  0.50s /bin/bash ./amd_rate_aocc400_znver4_A1.sh
```

```
-----
3. Username
From environment variable $USER: root
```

```
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            6190282
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio            0
```

```
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Date: Apr-2024

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Sep-2023

## Platform Notes (Continued)

```

/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define
DL-BIOS-adddcD=1 --define DL-VERS=5.0 --output_format html,pdf,txt --define DL-LQC=1
python3 ./run_amd_rate_aocc400_znver4_A1.py
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define DL-BIOS-NPS=4
--define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-BIOS-adddcD=1 --define DL-VERS=5.0 --output_format
html,pdf,txt --define DL-LQC=1 fprate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-BIOS-adddcD=1 --define DL-VERS=5.0
--output_format html,pdf,txt --define DL-LQC=1 --nopower --runmode rate --tune base:peak --size
test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
speperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1

```

```

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9654 96-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa10113e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 3584 4K pages
cpu cores      : 96
siblings       : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 1: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 1: apicids
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

```

-----
7. lscpu

From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 192
On-line CPU(s) list:   0-191
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 9654 96-Core Processor
CPU family:             25
Model:                  17
Thread(s) per core:    1
Core(s) per socket:    96
Socket(s):              2
Stepping:               1
BogoMIPS:               4801.51
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                        lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpperf

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Apr-2024  
**Hardware Availability:** Feb-2023  
**Software Availability:** Sep-2023

### Platform Notes (Continued)

```
rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic
movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce
topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
fsgsbase bmlil avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerpr rdpru
wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
v_omsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57
rdpid overflow_recov succor smca fsrm flush_lld
```

```
Virtualization: AMD-V
L1d cache: 6 MiB (192 instances)
L1i cache: 6 MiB (192 instances)
L2 cache: 192 MiB (192 instances)
L3 cache: 768 MiB (24 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-23
NUMA node1 CPU(s): 24-47
NUMA node2 CPU(s): 48-71
NUMA node3 CPU(s): 72-95
NUMA node4 CPU(s): 96-119
NUMA node5 CPU(s): 120-143
NUMA node6 CPU(s): 144-167
NUMA node7 CPU(s): 168-191
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB
filling, PBRBS-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	6M	8	Data	1	64	1	64
L1i	32K	6M	8	Instruction	1	64	1	64
L2	1M	192M	8	Unified	2	2048	1	64
L3	32M	768M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-23
node 0 size: 193074 MB
node 0 free: 191907 MB
node 1 cpus: 24-47
node 1 size: 193528 MB
node 1 free: 192469 MB
node 2 cpus: 48-71
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Apr-2024  
**Hardware Availability:** Feb-2023  
**Software Availability:** Sep-2023

### Platform Notes (Continued)

```

node 2 size: 193528 MB
node 2 free: 192465 MB
node 3 cpus: 72-95
node 3 size: 193512 MB
node 3 free: 192470 MB
node 4 cpus: 96-119
node 4 size: 193481 MB
node 4 free: 192429 MB
node 5 cpus: 120-143
node 5 size: 193528 MB
node 5 free: 188963 MB
node 6 cpus: 144-167
node 6 size: 193528 MB
node 6 free: 192448 MB
node 7 cpus: 168-191
node 7 size: 193501 MB
node 7 free: 192454 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

-----
9. /proc/meminfo
   MemTotal:      1584828376 kB

```

```

-----
10. who -r
    run-level 5 Apr 10 16:02

```

```

-----
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.10)
    Default Target Status
    graphical          running

```

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor blk-availability console-setup cron dmesg e2scrub_reap finalrd
getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors
lvm2-monitor lxd-agent multipathd networkd-dispatcher open-vm-tools pollinate rsyslog
secureboot-db setvtrgb ssh systemd-networkd systemd-pstore systemd-resolved
systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables open-iscsi rsync serial-getty@
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync upower
generated apport
indirect uuuid
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
systemd-networkd-wait-online x11-common

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Platform Notes (Continued)

```
BOOT_IMAGE=/vmlinuz-5.15.0-84-generic
root=/dev/mapper/ubuntuvg-ubuntu--lv
ro
```

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2400MHz
-----
```

```
-----
15. tuned-adm active
  Current active profile: latency-performance
-----
```

```
-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      3
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                  8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
-----
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
-----
```

```
-----
19. OS release
-----
```

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Platform Notes (Continued)

From /etc/\*-release /etc/\*-version  
os-release Ubuntu 22.04.3 LTS

### 20. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 110G 3.5G 107G 4% /mnt/ramdisk

### 21. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge R6625  
Product Family: PowerEdge  
Serial: BGP4016

### 22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

16x 80AD000080AD HMC94AEBRA109N 64 GB 2 rank 4800  
8x 80AD000080AD HMC94AEBRA123N 64 GB 2 rank 4800

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.4.6  
BIOS Date: 07/06/2023  
BIOS Revision: 1.4

## Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
```

```
=====  
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
```

```
=====  
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
```

```
=====  
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
```

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
 526.blender\_r: -funsigned-char -DSPEC\_LP64  
 527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3  
 -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
 -march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
 -mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
 -fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
 -zopt -lamdlibm -lamdalloc -lflang

C++ benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

```
538.imagick_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

544.nab\_r: basepeak = yes

C++ benchmarks:

```
508.namd_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fininline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc
```

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -lflang
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

```
527.cam4_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-lflang
```

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

```
526.blender_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-finline-aggressive -mllvm -unroll-threshold=100 -lamdlibm
-lamdalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-finline-aggressive -faggressive-loop-transform -fvectors-transform
-fscalar-transform -Mrecursive -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang
```



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 1400

PowerEdge R6625 (AMD EPYC 9654 96-Core Processor)

SPECrate®2017\_fp\_peak = 1400

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2024

Hardware Availability: Feb-2023

Software Availability: Sep-2023

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/aocc400-flags\\_A1.1.html](http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.html)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/aocc400-flags\\_A1.1.xml](http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.xml)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.2.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-04-10 14:40:09-0400.

Report generated on 2024-05-07 22:14:20 by CPU2017 PDF formatter v6716.

Originally published on 2024-05-07.