



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

### SPECrate®2017\_fp\_base = 341

### PowerEdge HS5610 (Intel Xeon Silver 4510)

### SPECrate®2017\_fp\_peak = 349

CPU2017 License: 6573

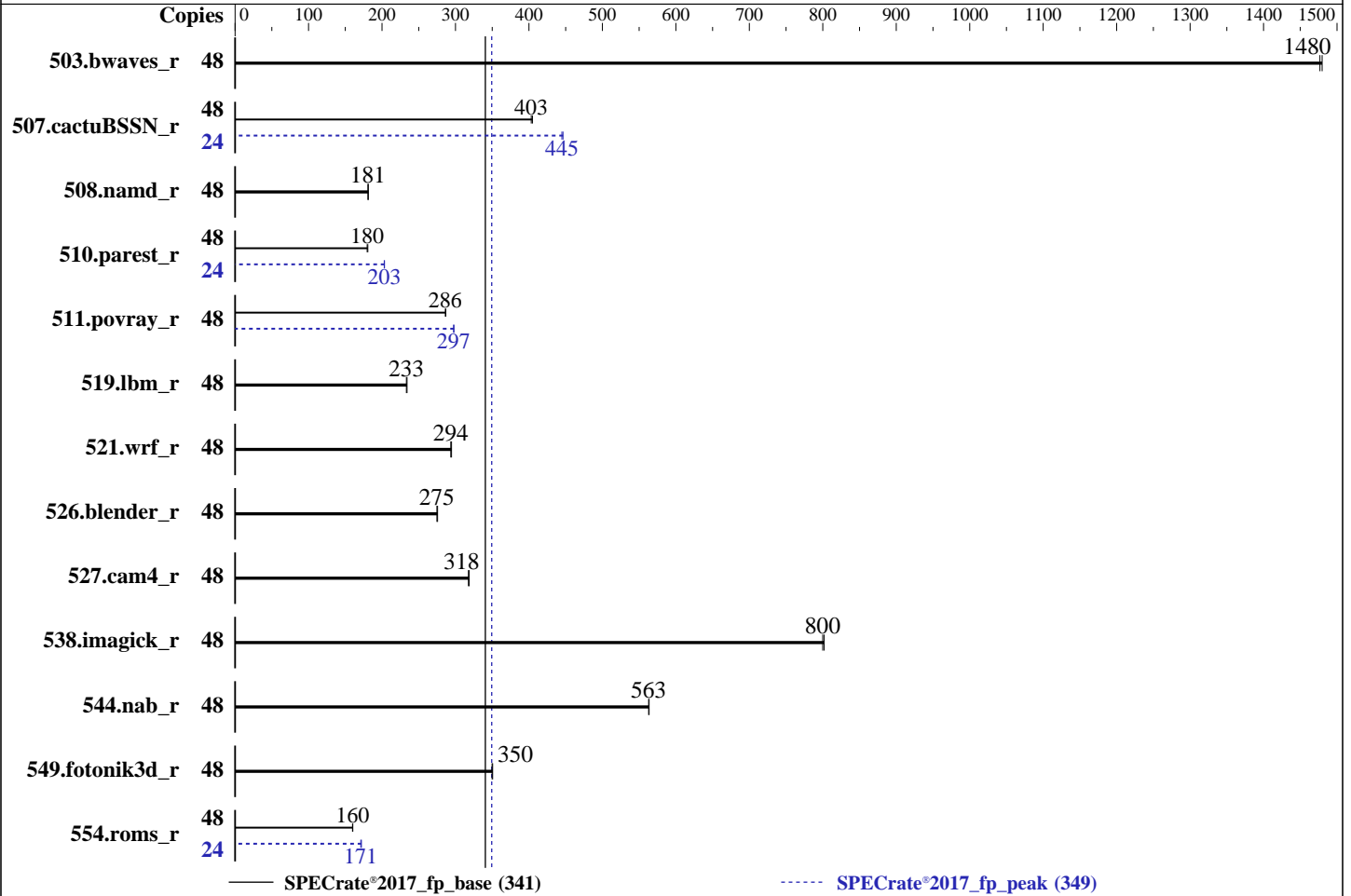
Test Date: Jan-2024

Test Sponsor: Dell Inc.

Hardware Availability: Nov-2023

Tested by: Dell Inc.

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Silver 4510  
 Max MHz: 4100  
 Nominal: 2400  
 Enabled: 24 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 30 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)  
 Storage: 125 GB on tmpfs  
 Other: Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++  
 Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran  
 Compiler for Linux;  
 Parallel: No  
 Firmware: Version 2.0.0 released Nov-2023  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance  
 at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	325	1480	<u>326</u>	<u>1480</u>			48	325	1480	<u>326</u>	<u>1480</u>		
507.cactuBSSN_r	48	<u>151</u>	<u>403</u>	150	405			24	68.1	446	<u>68.3</u>	<u>445</u>		
508.namd_r	48	<u>252</u>	<u>181</u>	251	181			48	<u>252</u>	<u>181</u>	251	181		
510.parest_r	48	695	181	<u>699</u>	<u>180</u>			24	<u>309</u>	<u>203</u>	309	203		
511.povray_r	48	391	287	<u>392</u>	<u>286</u>			48	<u>377</u>	<u>297</u>	376	298		
519.lbm_r	48	<u>217</u>	<u>233</u>	216	234			48	<u>217</u>	<u>233</u>	216	234		
521.wrf_r	48	<u>366</u>	<u>294</u>	365	295			48	<u>366</u>	<u>294</u>	365	295		
526.blender_r	48	265	276	<u>266</u>	<u>275</u>			48	265	276	<u>266</u>	<u>275</u>		
527.cam4_r	48	263	319	<u>264</u>	<u>318</u>			48	263	319	<u>264</u>	<u>318</u>		
538.imagick_r	48	149	802	<u>149</u>	<u>800</u>			48	149	802	<u>149</u>	<u>800</u>		
544.nab_r	48	<u>144</u>	<u>563</u>	143	564			48	<u>144</u>	<u>563</u>	143	564		
549.fotonik3d_r	48	<u>535</u>	<u>350</u>	534	351			48	<u>535</u>	<u>350</u>	534	351		
554.roms_r	48	476	160	<u>477</u>	<u>160</u>			24	222	172	<u>223</u>	<u>171</u>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOCCONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation

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## General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

ADDC Setting : Disabled  
DIMM Self Healing on  
Uncorrectable Memory Error : Disabled

Virtualization Technology : Disabled  
DCU Streamer Prefetcher : Disabled  
Sub NUMA Cluster : 2-way Clustering  
LLC Prefetch : Disabled  
Dead Line LLC Alloc : Disabled

System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
Memory Patrol Scrub : Disabled  
Energy Efficiency Policy : Performance  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Fri Jan 5 23:20:34 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl

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## Platform Notes (Continued)

- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
23:20:34 up 3:53, 1 user, load average: 29.64, 43.18, 46.00
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 19:27 3:53m 1.27s 0.01s /bin/bash ./dell-run-speccpu.sh rate
--define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
DL-VERS=v4.8.6 --output_format html,pdf,txt
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 4124470
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 4124470
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 34
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
--define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
--define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=24 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --define DL-BIOS-SNC=2
```

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## Platform Notes (Continued)

```
--iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=24 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --define
DL-BIOS-SNC=2 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --nopower --runmode rate --tune
base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
```

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) SILVER 4510
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000571
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 12
siblings       : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-11
physical id 1: core ids 0-11
physical id 0: apicids 0-23
physical id 1: apicids 64-87
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu

From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) SILVER 4510
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
Stepping: 8
CPU max MHz: 4100.0000
CPU min MHz: 800.0000
BogoMIPS: 4800.00
Flags: fpu_vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
```

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## Platform Notes (Continued)

cdp\_l2 ssbd mba ibrs ibpb stibp ibrs\_enhanced fsgsbase tsc\_adjust bml hle  
avx2 smep bmi2 erms invpcid rtm cqm rdt\_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl  
xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total  
cqm\_mbm\_local split\_lock\_detect avx\_vnni avx512\_bf16 wbnoinvd dtherm ida  
arat pln pts avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni vaes  
vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid  
bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize  
tsxldtrk pconfig arch\_lbr avx512\_fp16 amx\_tile flush\_lld arch\_capabilities

L1d cache: 1.1 MiB (24 instances)  
L1i cache: 768 KiB (24 instances)  
L2 cache: 48 MiB (24 instances)  
L3 cache: 60 MiB (2 instances)  
NUMA node(s): 4  
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44  
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46  
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45  
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:  
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE  
L1d 48K 1.1M 12 Data 1 64 1 64  
L1i 32K 768K 8 Instruction 1 64 1 64  
L2 2M 48M 16 Unified 2 2048 1 64  
L3 30M 60M 15 Unified 3 32768 1 64

8. numactl --hardware  
NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 4 nodes (0-3)  
node 0 cpus: 0,4,8,12,16,20,24,28,32,36,40,44  
node 0 size: 257425 MB  
node 0 free: 248313 MB  
node 1 cpus: 2,6,10,14,18,22,26,30,34,38,42,46  
node 1 size: 258010 MB  
node 1 free: 257307 MB  
node 2 cpus: 1,5,9,13,17,21,25,29,33,37,41,45  
node 2 size: 258044 MB  
node 2 free: 257406 MB  
node 3 cpus: 3,7,11,15,19,23,27,31,35,39,43,47  
node 3 size: 257666 MB  
node 3 free: 256800 MB  
node distances:  
node 0 1 2 3  
0: 10 12 21 21  
1: 12 10 21 21  
2: 21 21 10 12  
3: 21 21 12 10

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## Platform Notes (Continued)

9. /proc/meminfo  
MemTotal: 1055894788 kB

10. who -r  
run-level 3 Jan 5 19:27

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager firewalld getty@  
irqbalance issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd  
nvme-fc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore  
wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info gpm  
grub2-once haveged haveged-switch-root instsvcdrv ipmi ipmievd issue-add-ssh-keys  
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvme-autoconnect rpcbind  
rpmconfigcheck rsyncd serial-getty@ smartd\_generate\_opts snmpd snmptrapd  
systemd-boot-check-no-failures systemd-network-generator systemd-sysext  
systemd-time-wait-sync systemd-timesyncd vncserver@  
indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=a2071361-7500-4d59-ac2e-a3222a0598a9  
splash=silent  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=366M,high  
crashkernel=72M,low

14. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 4.10 GHz.  
The governor "powersave" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0

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## Platform Notes (Continued)

```

vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

```

```

-----
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs            tmpfs 125G  4.1G 121G   4% /mnt/ramdisk

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:          Dell Inc.
Product:         PowerEdge HS5610
Product Family:  PowerEdge
Serial:          DULAC01

```

```

-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x 00CE069D00CE M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 4400

```

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## Platform Notes (Continued)

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.  
BIOS Version: 2.0.0  
BIOS Date: 11/23/2023  
BIOS Revision: 2.0

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

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=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

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# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 341

PowerEdge HS5610 (Intel Xeon Silver 4510)

SPECrate®2017\_fp\_peak = 349

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2024

Hardware Availability: Nov-2023

Software Availability: Dec-2023

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

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CPU2017 License: 6573

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Tested by: Dell Inc.

Test Date: Jan-2024

Hardware Availability: Nov-2023

Software Availability: Dec-2023

## Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>

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