SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge HS5610 (Intel Xeon Gold 6548Y+)

$SPECrate®2017\_fp\_base = 720$

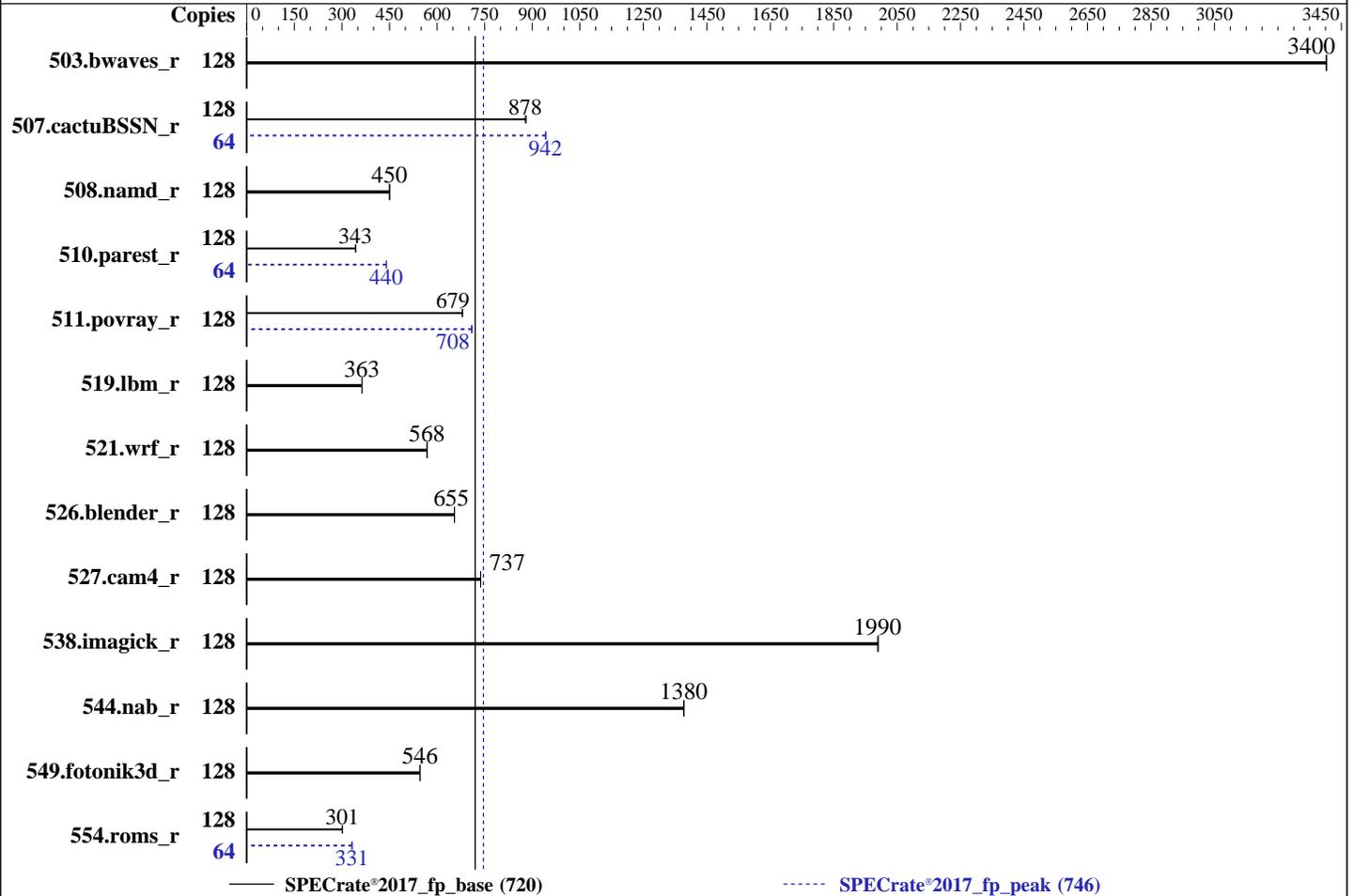$SPECrate®2017\_fp\_peak = 746$

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2023
Hardware Availability: Nov-2023
Software Availability: Dec-2023

Benchmark	Copies	base	peak
503.bwaves_r	128	3400	
507.cactuBSSN_r	128	878	
507.cactuBSSN_r	64		942
508.namd_r	128	450	
510.parest_r	128	343	
510.parest_r	64		440
511.povray_r	128	679	
511.povray_r	128		708
519.lbm_r	128	363	
521.wrf_r	128	568	
526.blender_r	128	655	
527.cam4_r	128	737	
538.imagick_r	128	1990	
544.nab_r	128	1380	
549.fotonik3d_r	128	546	
554.roms_r	128	301	
554.roms_r	64		331

SPECrate®2017_fp_base (720) SPECrate®2017_fp_peak (746)

Hardware

CPU Name: Intel Xeon Gold 6548Y+
 Max MHz: 4100
 Nominal: 2500
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 60 MB I+D on chip per chip
 Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200)
Storage: 125 GB on tmpfs
Other: Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
5.14.21-150500.53-default
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 2.0.0 released Nov-2023
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	377	3400	**377**	**3400**			128	377	3400	**377**	**3400**		
507.cactuBSSN_r	128	184	880	**185**	**878**			64	86.0	942	**86.0**	**942**		
508.namd_r	128	270	450	**270**	**450**			128	270	450	**270**	**450**		
510.parest_r	128	**977**	**343**	974	344			64	380	440	**381**	**440**		
511.povray_r	128	**440**	**679**	439	680			128	421	710	**422**	**708**		
519.lbm_r	128	**372**	**363**	371	363			128	**372**	**363**	371	363		
521.wrf_r	128	**505**	**568**	504	569			128	**505**	**568**	504	569		
526.blender_r	128	298	655	**298**	**655**			128	298	655	**298**	**655**		
527.cam4_r	128	**304**	**737**	304	738			128	**304**	**737**	304	738		
538.imagick_r	128	160	1990	**160**	**1990**			128	160	1990	**160**	**1990**		
544.nab_r	128	**156**	**1380**	156	1380			128	**156**	**1380**	156	1380		
549.fotonik3d_r	128	**913**	**546**	913	546			128	**913**	**546**	913	546		
554.roms_r	128	674	302	**676**	**301**			64	306	332	**307**	**331**		

SPECrate®2017_fp_base =	**720**
SPECrate®2017_fp_peak =	**746**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation

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General Notes (Continued)

```
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
                ADDDC Setting : Disabled
        DIMM Self Healing on
  Uncorrectable Memory Error : Disabled

     Virtualization Technology : Disabled
        DCU Streamer Prefetcher : Disabled
              Sub NUMA Cluster : 2-way Clustering
                   LLC Prefetch : Disabled
            Dead Line LLC Alloc : Disabled

                 System Profile : Custom
        CPU Power Management : Maximum Performance
                           C1E : Disabled
                      C States : Autonomous
            Memory Patrol Scrub : Disabled
      Energy Efficiency Policy : Performance
             PCI ASPM L1 Link
               Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Dec 27 22:06:41 2023

SUT (System Under Test) info as seen by some common utilities.

---------------------------------------------------------------
Table of contents
---------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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Platform Notes (Continued)

```
  16. /sys/kernel/mm/transparent_hugepage
  17. /sys/kernel/mm/transparent_hugepage/khugepaged
  18. OS release
  19. Disk information
  20. /sys/devices/virtual/dmi/id
  21. dmidecode
  22. BIOS
  ------------------------------------------------------------


  ------------------------------------------------------------
  1. uname -a
    Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
    x86_64 x86_64 x86_64 GNU/Linux


  ------------------------------------------------------------
  2. w
     22:06:41 up  4:09,  1 user,  load average: 77.31, 114.42, 121.70
    USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
    root     tty1     -                17:57    4:09m  1.15s  0.00s /bin/bash ./dell-run-speccpu.sh rate
    --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
    DL-VERS=v4.8.6 --output_format html,pdf,txt


  ------------------------------------------------------------
  3. Username
    From environment variable $USER:  root


  ------------------------------------------------------------
  4. ulimit -a
    core file size          (blocks, -c) unlimited
    data seg size           (kbytes, -d) unlimited
    scheduling priority             (-e) 0
    file size               (blocks, -f) unlimited
    pending signals                 (-i) 4124283
    max locked memory       (kbytes, -l) 64
    max memory size         (kbytes, -m) unlimited
    open files                      (-n) 1024
    pipe size            (512 bytes, -p) 8
    POSIX message queues     (bytes, -q) 819200
    real-time priority              (-r) 0
    stack size              (kbytes, -s) unlimited
    cpu time               (seconds, -t) unlimited
    max user processes              (-u) 4124283
    virtual memory          (kbytes, -v) unlimited
    file locks                      (-x) unlimited


  ------------------------------------------------------------
  5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 34
   login -- root
   -bash
   /bin/bash ./DELL_rate.sh
   /bin/bash ./dell-run-main.sh rate
   /bin/bash ./dell-run-main.sh rate
   /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
     --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt
   /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
     --define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
     ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
     --define invoke_with_interleave --define drop_caches --tune base,peak -o all --define DL-BIOS-SNC=2
```

(Continued on next page)

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Platform Notes (Continued)

```
   --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
   DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --define
   DL-BIOS-SNC=2 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
   DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --nopower --runmode rate --tune
   base:peak --size refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
 $SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3


 --------------------------------------------------------------
6. /proc/cpuinfo
    model name      : INTEL(R) XEON(R) GOLD 6548Y+
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 207
    stepping        : 2
    microcode       : 0x21000200
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 32
    siblings        : 64
    2 physical ids (chips)
    128 processors (hardware threads)
    physical id 0: core ids 0-31
    physical id 1: core ids 0-31
    physical id 0: apicids 0-63
    physical id 1: apicids 128-191
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.


 --------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:            x86_64
  CPU op-mode(s):          32-bit, 64-bit
  Address sizes:           46 bits physical, 57 bits virtual
  Byte Order:              Little Endian
  CPU(s):                  128
  On-line CPU(s) list:     0-127
  Vendor ID:               GenuineIntel
  Model name:              INTEL(R) XEON(R) GOLD 6548Y+
  CPU family:              6
  Model:                   207
  Thread(s) per core:      2
  Core(s) per socket:      32
  Socket(s):               2
  Stepping:                2
  BogoMIPS:                5000.00
  Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                           clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                           lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                           nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                           ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                           x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                           abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                           cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                           avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
```

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Platform Notes (Continued)

```
                               avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                               xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                               cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi
                               avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                               avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                               cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                               arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
  L1d cache:                   3 MiB (64 instances)
  L1i cache:                   2 MiB (64 instances)
  L2 cache:                    128 MiB (64 instances)
  L3 cache:                    120 MiB (2 instances)
  NUMA node(s):                4
  NUMA node0 CPU(s):           0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,10
                               0,104,108,112,116,120,124
  NUMA node1 CPU(s):           2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,1
                               02,106,110,114,118,122,126
  NUMA node2 CPU(s):           1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93,97,10
                               1,105,109,113,117,121,125
  NUMA node3 CPU(s):           3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79,83,87,91,95,99,1
                               03,107,111,115,119,123,127
  Vulnerability Itlb multihit:     Not affected
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
  Vulnerability Mmio stale data:   Not affected
  Vulnerability Retbleed:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                   sequence
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
    NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL  SETS PHY-LINE COHERENCY-SIZE
    L1d       48K       3M   12 Data              1    64        1             64
    L1i       32K       2M    8 Instruction       1    64        1             64
    L2         2M     128M   16 Unified           2  2048        1             64
    L3        60M     120M   15 Unified           3 65536        1             64

    ------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus:
  0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,100,104,108,112,116,120,124
  node 0 size: 257420 MB
  node 0 free: 247840 MB
  node 1 cpus:
  2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,102,106,110,114,118,122,126
  node 1 size: 258039 MB
  node 1 free: 256769 MB
  node 2 cpus:
  1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93,97,101,105,109,113,117,121,125
  node 2 size: 258005 MB
  node 2 free: 256942 MB
  node 3 cpus:
  3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79,83,87,91,95,99,103,107,111,115,119,123,127
  node 3 size: 257634 MB
  node 3 free: 256559 MB
```

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Platform Notes (Continued)

```
   node distances:
   node   0   1   2   3
     0:  10  12  21  21
     1:  12  10  21  21
     2:  21  21  10  12
     3:  21  21  12  10

  ------------------------------------------------------------
  9. /proc/meminfo
     MemTotal:       1055846948 kB

  ------------------------------------------------------------
  10. who -r
    run-level 3 Dec 27 17:57

  ------------------------------------------------------------
  11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
    Default Target  Status
    multi-user      running

  ------------------------------------------------------------
  12. Services, from systemctl list-unit-files
    STATE           UNIT FILES
    enabled         YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager firewalld getty@
                    irqbalance issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nscd
                    nvmefc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                    wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled        autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                    chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info gpm
                    grub2-once haveged haveged-switch-root instsvcdrv ipmi ipmievd issue-add-ssh-keys
                    kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvmf-autoconnect rpcbind
                    rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                    systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                    systemd-time-wait-sync systemd-timesyncd vncserver@
    indirect        wickedd

  ------------------------------------------------------------
  13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
    root=UUID=a2071361-7500-4d59-ac2e-a3222a0598a9
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
    crashkernel=366M,high
    crashkernel=72M,low

  ------------------------------------------------------------
  14. cpupower frequency-info
    analyzing CPU 0:
      Unable to determine current policy
      boost state support:
        Supported: yes
        Active: yes

  ------------------------------------------------------------
  15. sysctl
    kernel.numa_balancing            1
    kernel.randomize_va_space        2
```

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Platform Notes (Continued)

```
   vm.compaction_proactiveness        20
   vm.dirty_background_bytes           0
   vm.dirty_background_ratio          10
   vm.dirty_bytes                      0
   vm.dirty_expire_centisecs        3000
   vm.dirty_ratio                     20
   vm.dirty_writeback_centisecs      500
   vm.dirtytime_expire_seconds     43200
   vm.extfrag_threshold              500
   vm.min_unmapped_ratio               1
   vm.nr_hugepages                     0
   vm.nr_hugepages_mempolicy           0
   vm.nr_overcommit_hugepages          0
   vm.swappiness                      60
   vm.watermark_boost_factor       15000
   vm.watermark_scale_factor          10
   vm.zone_reclaim_mode                0


   ---------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
   defrag          always defer defer+madvise [madvise] never
   enabled         [always] madvise never
   hpage_pmd_size  2097152
   shmem_enabled   always within_size advise [never] deny force


   ---------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs   60000
   defrag                      1
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000


   ---------------------------------------------------------------
18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP5


   ---------------------------------------------------------------
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
   Filesystem      Type   Size  Used Avail Use% Mounted on
   tmpfs          tmpfs  125G  4.1G  121G   4% /mnt/ramdisk


   ---------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
     Vendor:        Dell Inc.
     Product:       PowerEdge HS5610
     Product Family: PowerEdge
     Serial:        DULAC01


   ---------------------------------------------------------------
21. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
```

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

Dell Inc.

PowerEdge HS5610 (Intel Xeon Gold 6548Y+)

SPECrate®2017_fp_base = 720

SPECrate®2017_fp_peak = 746

CPU2017 License: 6573		**Test Date:**	Dec-2023
Test Sponsor: Dell Inc.		**Hardware Availability:** Nov-2023
Tested by: Dell Inc.		**Software Availability:** Dec-2023

Platform Notes (Continued)

```
     16x 00CE069D00CE M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 5200


    --------------------------------------------------------------
    22. BIOS
    (This section combines info from /sys/devices and dmidecode.)
       BIOS Vendor:      Dell Inc.
       BIOS Version:     2.0.0
       BIOS Date:        11/23/2023
       BIOS Revision:    2.0
```

Compiler Version Notes

```
==============================================================================================
C              | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++            | 508.namd_r(base, peak) 510.parest_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++, C         | 511.povray_r(base, peak) 526.blender_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
Fortran        | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
Fortran, C     | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

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SPEC CPU®2017 Floating Point Rate Result

Dell Inc.

PowerEdge HS5610 (Intel Xeon Gold 6548Y+)

$SPECrate^{®}2017\_fp\_base = 720$

$SPECrate^{®}2017\_fp\_peak = 746$

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2023
Hardware Availability: Nov-2023
Software Availability: Dec-2023

Compiler Version Notes (Continued)

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

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CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2023
Hardware Availability: Nov-2023
Software Availability: Dec-2023

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:
```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

(Continued on next page)

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Dell Inc.

PowerEdge HS5610 (Intel Xeon Gold 6548Y+)

SPECrate®2017_fp_base = 720

SPECrate®2017_fp_peak = 746

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2023
Hardware Availability: Nov-2023
Software Availability: Dec-2023

Peak Compiler Invocation (Continued)

Fortran benchmarks:
```
ifx
```

Benchmarks using both Fortran and C:
```
ifx icx
```

Benchmarks using both C and C++:
```
icpx icx
```

Benchmarks using Fortran, C, and C++:
```
icpx icx ifx
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

519.lbm_r: `basepeak = yes`

538.imagick_r: `basepeak = yes`

544.nab_r: `basepeak = yes`

C++ benchmarks:

508.namd_r: `basepeak = yes`

510.parest_r: `-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

Fortran benchmarks:

503.bwaves_r: `basepeak = yes`

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge HS5610 (Intel Xeon Gold 6548Y+)

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CPU2017 License: 6573
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Test Date: Dec-2023
Hardware Availability: Nov-2023
Software Availability: Dec-2023

Peak Optimization Flags (Continued)

549.fotonik3d_r: basepeak = yes

554.roms_r: `-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -nostandard-realloc-lhs`
`-align array32byte -auto -ljemalloc`
`-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: `-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs`
`-fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1)
`-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse`
`-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int`
`-mprefer-vector-width=512 -ljemalloc`
`-L/usr/local/jemalloc64-5.0.1/lib`

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
`-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512`
`-nostandard-realloc-lhs -align array32byte -auto -ljemalloc`
`-L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml`

SPEC CPU®2017 Floating Point Rate Result

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Test Sponsor:	Dell Inc.	**Hardware Availability:**	Nov-2023
Tested by:	Dell Inc.	**Software Availability:**	Dec-2023

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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