



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Fujitsu**

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

**SPECrate®2017\_fp\_base = 1440**

**SPECrate®2017\_fp\_peak = Not Run**

CPU2017 License: 19

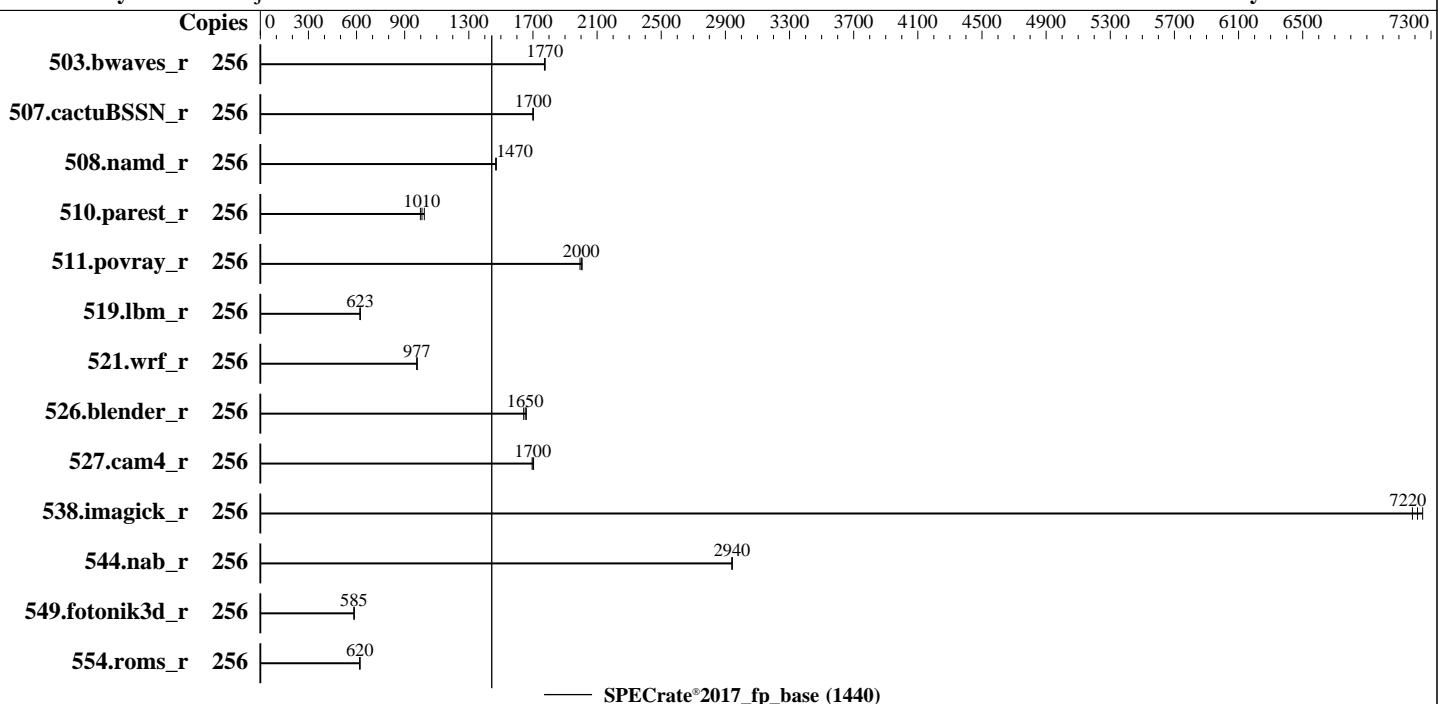
Test Sponsor: Fujitsu

Tested by: Fujitsu

**Test Date:** Jan-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Nov-2022



## Hardware

CPU Name: AMD EPYC 9754  
 Max MHz: 3100  
 Nominal: 2250  
 Enabled: 256 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 16 MB shared / 8 cores  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x PCIE NVME SSD, 6.4 TB  
 Other: None

## Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 5.14.0-70.13.1.el9\_0.x86\_64  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: No  
 Firmware: Fujitsu BIOS Version Version V5.0.0.27 R1.5.0 for D4129-A1x. Released May-2024 tested as V5.0.0.27 R0.90.0 for D4129-A1x  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	256	1446	1770	<b>1446</b>	<b>1770</b>	1449	1770									
507.cactubSSN_r	256	190	1700	<b>190</b>	<b>1700</b>	191	1700									
508.namd_r	256	166	1470	<b>166</b>	<b>1470</b>	165	1470									
510.parest_r	256	<b>664</b>	<b>1010</b>	671	998	655	1020									
511.povray_r	256	298	2010	300	1990	<b>299</b>	<b>2000</b>									
519.lbm_r	256	434	622	433	623	<b>433</b>	<b>623</b>									
521.wrf_r	256	587	977	<b>587</b>	<b>977</b>	587	977									
526.blender_r	256	235	1660	237	1640	<b>236</b>	<b>1650</b>									
527.cam4_r	256	264	1690	263	1700	<b>263</b>	<b>1700</b>									
538.imagick_r	256	87.8	7250	88.6	7180	<b>88.2</b>	<b>7220</b>									
544.nab_r	256	146	2940	146	2940	<b>146</b>	<b>2940</b>									
549.fotonik3d_r	256	1705	585	1707	584	<b>1707</b>	<b>585</b>									
554.roms_r	256	655	621	656	620	<b>656</b>	<b>620</b>									

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
 To free node-local memory and avoid remote memory usage,  
 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
 To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
 To disable address space layout randomization (ASLR) to reduce run-to-run  
 variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/home/Benchmark/speccpu2017r/amd_rate_aocc400_znver4_A_lib/lib:/home/Benchmark/speccpu2017r/amd_rate_  
    aocc400_znver4_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
SMT Control = Disabled  
L2 Stream HW Prefetcher = Disabled  
Determinism Slider = Power  
TDP Control = Manual  
TDP Limit = 400  
Package Power Limit Control = Manual  
Package Power Limit = 400  
Power Profile Selection = High Performance  
NUMA nodes per socket = NPS4  
Periodic Directory Rinse (PDR) Tuning = Cache-Bound  
FAN Control = Full

```
Sysinfo program /home/Benchmark/speccpu2017r/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Thu Apr 7 01:52:41 2022
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Platform Notes (Continued)

```
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
-----  
1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64  
x86_64 x86_64 GNU/Linux
```

```
-----  
2. w  
01:52:41 up 5:55, 1 user, load average: 51.65, 185.02, 229.19  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root ttym1 20:01 5:50m 1.51s 0.21s /bin/bash ./amd_rate_aocc400_znver4_A1.sh
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 3091701  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 3091701  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31  
login -- root  
-bash  
-bash  
python3 ./run_amd_fprate_31_aocc400_znver4_A1.py
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Platform Notes (Continued)

```
/bin/bash ./amd_rate_aocc400_znver4_A1.sh
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune base --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode rate --tune base --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/Benchmark/speccpu2017r

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9754 128-Core Processor
vendor_id        : AuthenticAMD
cpu family      : 25
model           : 160
stepping         : 2
microcode        : 0xaa00213
bugs             : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size         : 3584 4K pages
cpu cores        : 128
siblings          : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 1: core ids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 0: apicids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 1: apicids
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439,448-455,4
64-471,480-487,496-503
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----
7. lscpu
```

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                256
On-line CPU(s) list:   0-255
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9754 128-Core Processor
BIOS Model name:       AMD EPYC 9754 128-Core Processor
CPU family:             25
Model:                 160
Thread(s) per core:    1
Core(s) per socket:    128
Socket(s):              2
Stepping:               2
Frequency boost:       enabled
CPU max MHz:            3100.3411
CPU min MHz:            1500.0000
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Fujitsu**

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

**SPECrate®2017\_fp\_base = 1440**

**SPECrate®2017\_fp\_peak = Not Run**

**CPU2017 License:** 19

**Test Date:** Jan-2024

**Test Sponsor:** Fujitsu

**Hardware Availability:** Feb-2024

**Tested by:** Fujitsu

**Software Availability:** Nov-2022

## Platform Notes (Continued)

BogoMIPS:

4492.82

Flags:

```
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
abm sse4a misalignsse 3dnopprefetch osvw ibs skinit wdt tce topoext
perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13
invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbmb_total cqmq_mbmb_local
avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
umip pku ospk avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
```

Virtualization:

AMD-V

L1d cache:

8 MiB (256 instances)

L1i cache:

8 MiB (256 instances)

L2 cache:

256 MiB (256 instances)

L3 cache:

512 MiB (32 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-31

NUMA node1 CPU(s):

32-63

NUMA node2 CPU(s):

64-95

NUMA node3 CPU(s):

96-127

NUMA node4 CPU(s):

128-159

NUMA node5 CPU(s):

160-191

NUMA node6 CPU(s):

192-223

NUMA node7 CPU(s):

224-255

Vulnerability Itlb multihit:

Not affected

Vulnerability Lltf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP disabled, RSB filling

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	8M	8	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	8	Unified	2	2048	1	64
L3	16M	512M	16	Unified	3	16384	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-31

node 0 size: 95810 MB

node 0 free: 94463 MB

node 1 cpus: 32-63

node 1 size: 96761 MB

node 1 free: 95491 MB

node 2 cpus: 64-95

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Platform Notes (Continued)

```
node 2 size: 96761 MB
node 2 free: 95595 MB
node 3 cpus: 96-127
node 3 size: 96761 MB
node 3 free: 95599 MB
node 4 cpus: 128-159
node 4 size: 96761 MB
node 4 free: 95600 MB
node 5 cpus: 160-191
node 5 size: 96724 MB
node 5 free: 95589 MB
node 6 cpus: 192-223
node 6 size: 96761 MB
node 6 free: 95620 MB
node 7 cpus: 224-255
node 7 size: 96688 MB
node 7 free: 95441 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10  12  12  12  32  32  32  32
  1: 12  10  12  12  32  32  32  32
  2: 12  12  10  12  32  32  32  32
  3: 12  12  12  10  32  32  32  32
  4: 32  32  32  32  10  12  12  12
  5: 32  32  32  32  12  10  12  12
  6: 32  32  32  32  12  12  10  12
  7: 32  32  32  32  12  12  12  10
```

```
-----  
9. /proc/meminfo  
MemTotal: 791582920 kB
```

```
-----  
10. who -r  
run-level 3 Apr 6 20:00
```

```
-----  
11. Systemd service manager version: systemd 250 (250-6.el9_0)  
Default Target Status  
multi-user running
```

```
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES
enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
                chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi
                iscsi-onboot kdump libstoragemgmt lm_sensors lvm2-monitor mcelog mdmonitor microcode
                multipathd nis-domainname nvmefc-boot-connections pmcd pmie pmlogger rhsmcertd rsyslog
                selinux-autorelabel-mark smartd sshd sssd sysstat systemd-network-generator udisks2 upower
enabled-runtime rc-local systemd-remount-fs
disabled       arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
                canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower debug-shell
                fancontrol grafana-server iprdump iprinit iprupdate iscsid iscsiui0 kpatch kvm_stat ledmon
                man-db-restart-cache-update nftables nvme-fc-autoconnect pmfind pmie_farm pmlogger_farm
                pmproxy podman podman-auto-update podman-restart postfix powertop psacct ras-mc-ctl
                rasdaemon rdisc rhcd rhsm-facts rpmbuild rrdcached saslauthd serial-getty@
                sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext trace-cmd
indirect        sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Platform Notes (Continued)

```
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=(hd1,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
    root=/dev/mapper/rhel-root
    ro
    crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
    resume=/dev/mapper/rhel-swap
    rd.lvm.lv=rhel/root
    rd.lvm.lv=rhel/swap
    rhgb
    quiet
```

```
-----  
14. cpupower frequency-info
analyzing CPU 0:
    current policy: frequency should be within 1.50 GHz and 2.25 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
        Supported: yes
        Active: yes
        Boost States: 0
        Total States: 3
        Pstate-P0: 2250MHz
```

```
-----  
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compression_proactiveness   20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage
    defrag          [always] defer defer+madvise madvice never
    enabled         [always] madvice never
    hpage_pmd_size 2097152
    shmem_enabled   always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                  1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Platform Notes (Continued)

```
pages_to_scan      4096
scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release      Red Hat Enterprise Linux 9.0 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)  
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

```
-----  
19. Disk information  
SPEC is set to: /home/Benchmark/speccpu2017r  
Filesystem          Type   Size  Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs    5.8T  60G  5.7T   2% /home
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor:          FUJITSU  
Product:         PRIMERGY RX2450 M2  
Product Family: SERVER  
Serial:          XXXXXXXXXXXX
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
24x Samsung M321R4GA3BB6-CQKEG 32 GB 2 rank 4800
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor:      FUJITSU // American Megatrends Inc.  
BIOS Version:     V5.0.0.27 R0.90.0 for D4129-A1x  
BIOS Date:        12/21/2023  
BIOS Revision:    0.90  
Firmware Revision: 2.40
```

## Compiler Version Notes

```
=====  
C           | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)  
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin  
=====
```

```
=====  
C++          | 508.namd_r(base) 510.parest_r(base)  
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++, C | 511.povray\_r(base) 526.blender\_r(base)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++, C, Fortran | 507.cactuBSSN\_r(base)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#434 2022\_10\_28) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactusBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.llbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -lamdlibm -lamdalloc -lflang
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIB -ffast-math -mllvm -unroll-threshold=100  
-finline-aggressive -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIB -ffast-math -Kieee -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIB -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIB -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX2450 M2,  
AMD EPYC 9754, 2.25 GHz

SPECrate®2017\_fp\_base = 1440

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Nov-2022

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdaloc -lflang
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>  
<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-Genoa-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>  
<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-Genoa-RevC.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2022-04-07 01:52:40-0400.

Report generated on 2024-02-14 12:25:54 by CPU2017 PDF formatter v6716.

Originally published on 2024-02-14.