



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 90.1

## PowerEdge R360 (Intel Xeon E-2456)

SPECrate®2017\_fp\_peak = 90.3

CPU2017 License: 6573

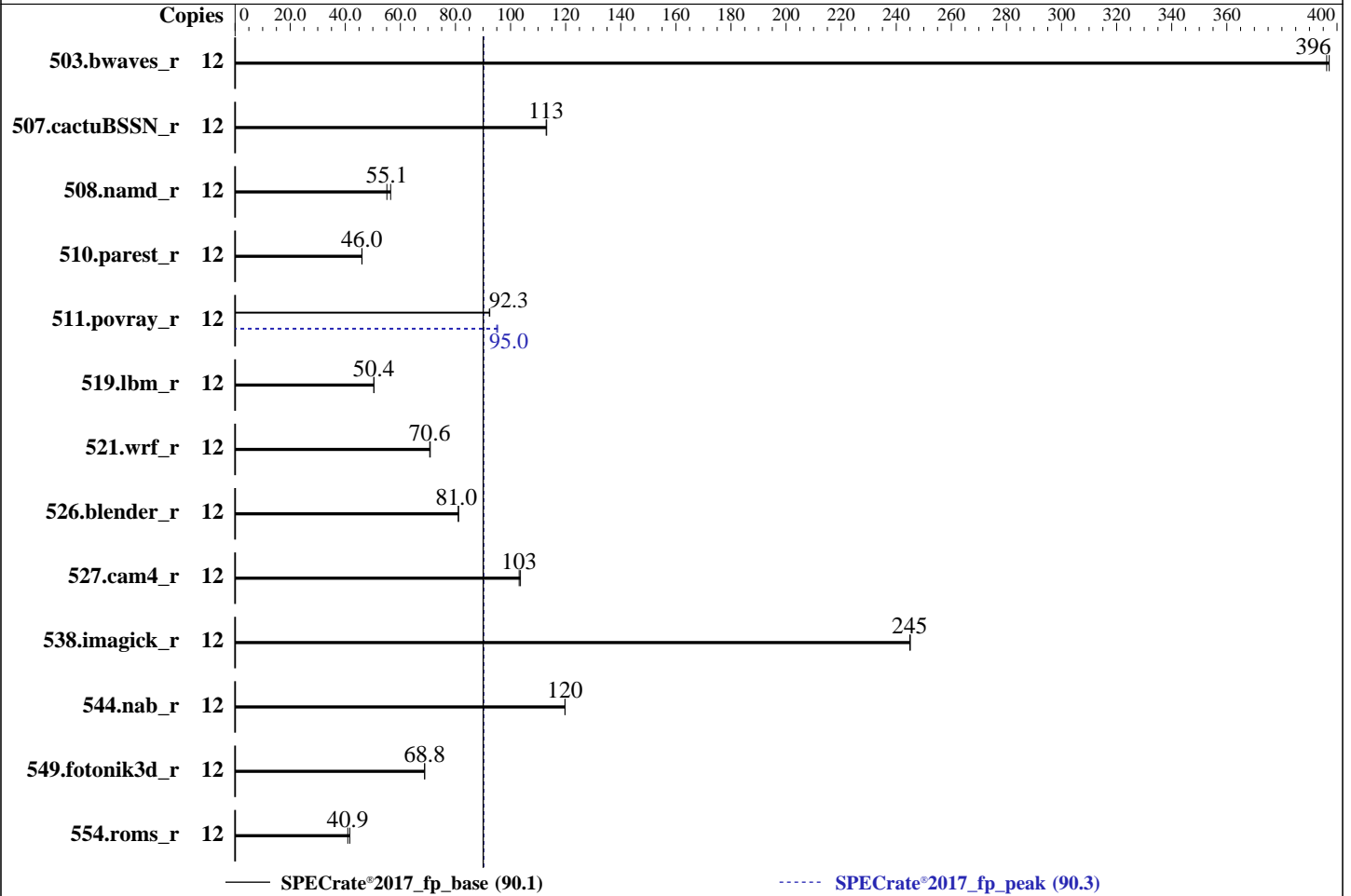
Test Date: Nov-2023

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2023

Tested by: Dell Inc.

Software Availability: Jul-2023



### Hardware

CPU Name: Intel Xeon E-2456  
 Max MHz: 5100  
 Nominal: 3300  
 Enabled: 6 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 18 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)  
 Storage: 40 GB on tmpfs  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.2 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Version 1.0.0 released Oct-2023  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	12	<b>304</b>	<b>396</b>	303	397			12	<b>304</b>	<b>396</b>	303	397		
507.cactuBSSN_r	12	<b>135</b>	<b>113</b>	134	113			12	<b>135</b>	<b>113</b>	134	113		
508.namd_r	12	<b>207</b>	<b>55.1</b>	202	56.5			12	<b>207</b>	<b>55.1</b>	202	56.5		
510.parest_r	12	<b>682</b>	<b>46.0</b>	681	46.1			12	<b>682</b>	<b>46.0</b>	681	46.1		
511.povray_r	12	303	92.4	<b>304</b>	<b>92.3</b>			12	<b>295</b>	<b>95.0</b>	294	95.3		
519.lbm_r	12	<b>251</b>	<b>50.4</b>	251	50.4			12	<b>251</b>	<b>50.4</b>	251	50.4		
521.wrf_r	12	<b>380</b>	<b>70.6</b>	379	70.9			12	<b>380</b>	<b>70.6</b>	379	70.9		
526.blender_r	12	225	81.1	<b>226</b>	<b>81.0</b>			12	225	81.1	<b>226</b>	<b>81.0</b>		
527.cam4_r	12	<b>204</b>	<b>103</b>	203	104			12	<b>204</b>	<b>103</b>	203	104		
538.imagick_r	12	122	245	<b>122</b>	<b>245</b>			12	122	245	<b>122</b>	<b>245</b>		
544.nab_r	12	169	120	<b>169</b>	<b>120</b>			12	169	120	<b>169</b>	<b>120</b>		
549.fotonik3d_r	12	680	68.8	<b>680</b>	<b>68.8</b>			12	680	68.8	<b>680</b>	<b>68.8</b>		
554.roms_r	12	<b>466</b>	<b>40.9</b>	458	41.6			12	<b>466</b>	<b>40.9</b>	458	41.6		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
"/mnt/ramdisk/cpu2017-1.1.9-ic2023.x/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.x/je5.0.1-64"  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

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## General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

DIMM Self Healing on  
Uncorrectable Memory Error : Disabled

Virtualization Technology : Disabled  
LLC Prefetch : Disabled  
Dead Line LLC Alloc : Disabled

System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.x/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Mon Nov 13 20:15:09 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

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## Platform Notes (Continued)

1. `uname -a`  
Linux localhost.localdomain 5.14.21-150400.22-default #1 SMP PREEMPT\_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86\_64 x86\_64 x86\_64 GNU/Linux

2. `w`  
20:15:09 up 3:14, 1 user, load average: 9.74, 11.52, 11.83  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root tty1 - 17:02 3:12m 1.30s 0.00s /bin/bash ./dell-run-speccpu.sh rate  
--define DL-BIOSinc=Dell-BIOS\_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.3  
--output\_format html,pdf,txt

3. Username  
From environment variable \$USER: root

4. `ulimit -a`  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 256444  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 256444  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. `sysinfo process ancestry`  
/usr/lib/systemd/systemd linux --switched-root --system --deserialize 30  
login -- root  
-bash  
/bin/bash ./DELL\_rate.sh  
/bin/bash ./dell-run-main.sh rate  
/bin/bash ./dell-run-main.sh rate  
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS\_Xeon-4E.inc --define DL-BIOS-LogProc=1  
--define DL-VERS=v4.8.3 --output\_format html,pdf,txt  
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS\_Xeon-4E.inc --define DL-BIOS-LogProc=1  
--define DL-VERS=v4.8.3 --output\_format html,pdf,txt  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 -c  
ic2023.2-lin-core-avx2-rate-20230622.cfg --define smt-on --define cores=6 --define physicallogical  
--define no-numa --tune base,peak -o all --define drop\_caches --iterations 2 --define  
DL-BIOSinc=Dell-BIOS\_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.3 --output\_format  
html,pdf,txt fprate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=12 --configfile  
ic2023.2-lin-core-avx2-rate-20230622.cfg --define smt-on --define cores=6 --define physicallogical  
--define no-numa --tune base,peak --output\_format all --define drop\_caches --iterations 2 --define  
DL-BIOSinc=Dell-BIOS\_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=v4.8.3 --output\_format  
html,pdf,txt --nopower --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv

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## Platform Notes (Continued)

```
--logfile $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.x
```

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) E E-2456
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x11f
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 6
siblings       : 12
1 physical ids (chips)
12 processors (hardware threads)
physical id 0: core ids 0-5
physical id 0: apicids 0-11
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         42 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                12
On-line CPU(s) list:   0-11
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) E E-2456
CPU family:            6
Model:                 183
Thread(s) per core:    2
Core(s) per socket:    6
Socket(s):             1
Stepping:              1
BogoMIPS:              6604.80
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic
                        movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                        3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp
                        ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed
                        adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                        avx_vnni dtherm ida arat pln pts umip pku ospke waitpkg gfni vaes
                        vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig
                        arch_lbr flush_lld arch_capabilities
L1d cache:             288 KiB (6 instances)
L1i cache:             192 KiB (6 instances)
L2 cache:              12 MiB (6 instances)
L3 cache:              18 MiB (1 instance)
NUMA node(s):         1
NUMA node0 CPU(s):    0-11
Vulnerability Itlb multihit: Not affected
```

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### Platform Notes (Continued)

Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	288K	12	Data	1	64	1	64
L1i	32K	192K	8	Instruction	1	64	1	64
L2	2M	12M	16	Unified	2	2048	1	64
L3	18M	18M	9	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 1 nodes (0)
node 0 cpus: 0-11
node 0 size: 64135 MB
node 0 free: 52555 MB
node distances:
node 0
0: 10

```

9. /proc/meminfo

MemTotal: 65674928 kB

10. who -r

run-level 3 Nov 13 17:01

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager firewalld getty@ haveged irqbalance issue-generator kbdsettings klog lvm2-monitor nscd purge-kernels rollback rsyslog smartd sshd wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	ModemManager accounts-daemon appstream-sync-cache autofs autoyast-initscripts blk-availability bluetooth bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievd iscsi iscsi-init iscsid iscsiui issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nm-cloud-setup nmb ostree-remount postfix rdisc rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd udisks2 upower wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant@
indirect	wickedd

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## Platform Notes (Continued)

### 13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=ef117b4a-6c89-42a3-b0a0-2cc3fe661a68
linux
splash=silent
quiet
security=
mitigations=auto
```

### 14. cpupower frequency-info

```
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

### 15. sysctl

```
kernel.numa_balancing          0
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs    500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

### 16. /sys/kernel/mm/transparent\_hugepage

```
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

### 18. OS release

```
From /etc/*-release /etc/*-version
```

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## Platform Notes (Continued)

os-release SUSE Linux Enterprise Server 15 SP4

### 19. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.x  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 40G 5.4G 35G 14% /mnt/ramdisk

### 20. /sys/devices/virtual/dmi/id

Vendor: Dell Inc.  
Product: PowerEdge R360  
Product Family: PowerEdge  
Serial: R360SH8

### 21. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
2x 002C00001201 MTC20C2085S1EC48BA1 32 GB 2 rank 4800, configured at 4400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.0.0  
BIOS Date: 10/13/2023  
BIOS Revision: 1.0

## Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
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## Compiler Version Notes (Continued)

C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

-----  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
-----

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

-----  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.0 Build 20230622  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 90.1

PowerEdge R360 (Intel Xeon E-2456)

SPECrate®2017\_fp\_peak = 90.3

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Nov-2023

Hardware Availability: Dec-2023

Software Availability: Jul-2023

## Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

### C++ benchmarks:

```

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

### Fortran benchmarks:

```

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

### Benchmarks using both Fortran and C:

```

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

```

### Benchmarks using both C and C++:

```

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

```

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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>



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