



# SPEC CPU®2017 Integer Rate Result

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## Nettrix

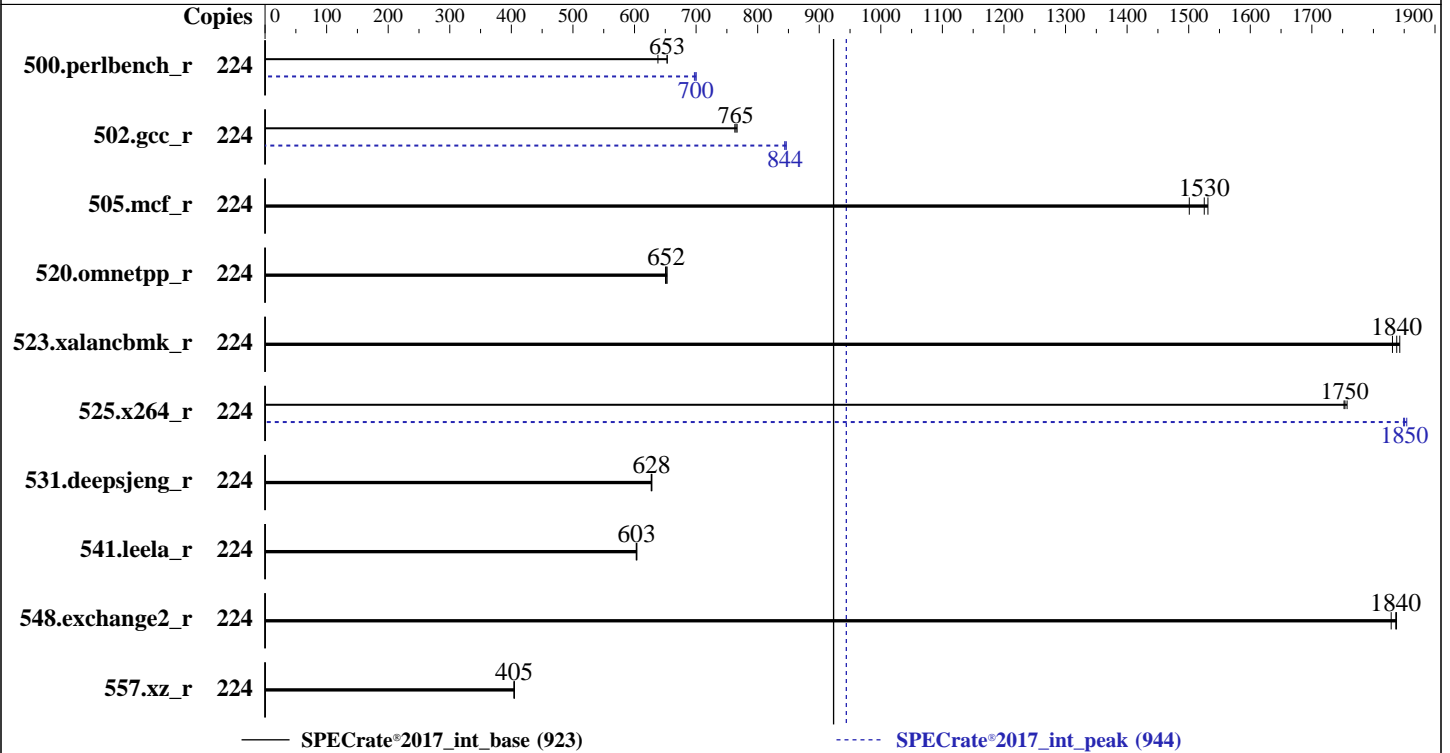
SPECrate®2017\_int\_base = 923

R620 G50 (Intel Xeon Max 9480, 1.9 GHz)

SPECrate®2017\_int\_peak = 944

CPU2017 License: 6138  
Test Sponsor: Nettrix  
Tested by: Nettrix

Test Date: Jul-2023  
Hardware Availability: Feb-2023  
Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Max 9480  
 Max MHz: 3500  
 Nominal: 1900  
 Enabled: 112 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 112.5 MB I+D on chip per chip  
 Other: None  
 Memory: 1152 GB (16 x 64 GB 2Rx4 PC5-4800B-R + 2 x 64 GB HBM)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Nettrix BIOS Version NNH1041020 released Jan-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	224	559	638	<b>546</b>	<b>653</b>	546	653	224	511	697	509	700	<b>510</b>	<b>700</b>
502.gcc_r	224	416	763	<b>415</b>	<b>765</b>	414	767	224	<b>376</b>	<b>844</b>	375	847	376	844
505.mcf_r	224	241	1500	<b>237</b>	<b>1530</b>	236	1530	224	241	1500	<b>237</b>	<b>1530</b>	236	1530
520.omnetpp_r	224	<b>451</b>	<b>652</b>	452	650	450	653	224	<b>451</b>	<b>652</b>	452	650	450	653
523.xalancbmk_r	224	<b>129</b>	<b>1840</b>	129	1830	128	1840	224	<b>129</b>	<b>1840</b>	129	1830	128	1840
525.x264_r	224	223	1760	<b>224</b>	<b>1750</b>	224	1750	224	212	1850	<b>212</b>	<b>1850</b>	212	1850
531.deepsjeng_r	224	409	627	<b>409</b>	<b>628</b>	409	628	224	409	627	<b>409</b>	<b>628</b>	409	628
541.leela_r	224	614	604	<b>615</b>	<b>603</b>	615	603	224	614	604	<b>615</b>	<b>603</b>	615	603
548.exchange2_r	224	319	1840	321	1830	<b>320</b>	<b>1840</b>	224	319	1840	321	1830	<b>320</b>	<b>1840</b>
557.xz_r	224	599	404	<b>598</b>	<b>405</b>	597	405	224	599	404	<b>598</b>	<b>405</b>	597	405

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =

"/home/SPECcpu2017\_ic2023/lib/intel64:/home/SPECcpu2017\_ic2023/lib/ia32:/home/SPECcpu2017\_ic2023/je5.0  
.1-32"

MALLOC\_CONF = "retain:true"



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## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Configuration:

- SNC (Sub NUMA) set to Enable SNC4 (4-clusters)
- Patrol Scrub set to Disabled
- LLC dead line alloc set to Disabled
- DCU Streamer Prefetcher set to Disabled
- Hardware P-States set to Native Mode

Sysinfo program /home/SPECcpu2017\_ic2023/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Jul 16 00:14:09 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

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### Platform Notes (Continued)

1. `uname -a`  
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT\_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)  
x86\_64 x86\_64 x86\_64 GNU/Linux

2. `w`  
00:14:09 up 1 day, 13:16, 0 users, load average: 0.15, 9.95, 81.60  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username  
From environment variable \$USER: root

4. `ulimit -a`  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 4126496  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 4126496  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. `sysinfo process ancestry`  
/usr/lib/systemd/systemd --switched-root --system --deserialize 29  
sh run\_rate.sh  
runcpu --nobuild --action validate --reportable --define default-platform-flags --define numcopies=224 -c  
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=112 --define physicalfirst  
--define invoke\_with\_interleave --define drop\_caches --tune base,peak -o all intrate  
runcpu --nobuild --action validate --reportable --define default-platform-flags --define numcopies=224  
--configfile ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=112 --define  
physicalfirst --define invoke\_with\_interleave --define drop\_caches --tune base,peak --output\_format all  
--nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.017/templogs/preenv.intrate.017.0.log --lognum 017.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/SPECcpu2017\_ic2023

6. `/proc/cpuinfo`  
model name : Intel (R) Xeon (R) CPU Max 9480  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 143  
stepping : 8  
microcode : 0x2c000120  
bugs : spectre\_v1 spectre\_v2 spec\_store\_bypass swapgs  
cpu cores : 56

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### Platform Notes (Continued)

```
siblings          : 112
2 physical ids (chips)
224 processors (hardware threads)
physical id 0: core ids 0-55
physical id 1: core ids 0-55
physical id 0: apicids 0-111
physical id 1: apicids 128-239
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                224
On-line CPU(s) list:  0-223
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon (R) CPU Max 9480
CPU family:            6
Model:                 143
Thread(s) per core:    2
Core(s) per socket:   56
Socket(s):             2
Stepping:              8
CPU max MHz:           3500.0000
CPU min MHz:           800.0000
BogoMIPS:              3800.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                        vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                        x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                        abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
                        intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
                        flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
                        erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                        clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                        hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke waitpkg
                        avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                        avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                        enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                        amx_tile flush_l1d arch_capabilities
Virtualization:        VT-x
L1d cache:             5.3 MiB (112 instances)
L1i cache:             3.5 MiB (112 instances)
L2 cache:              224 MiB (112 instances)
L3 cache:              225 MiB (2 instances)
NUMA node(s):         8
NUMA node0 CPU(s):    0-13,112-125
NUMA node1 CPU(s):    14-27,126-139
NUMA node2 CPU(s):    28-41,140-153
NUMA node3 CPU(s):    42-55,154-167
NUMA node4 CPU(s):    56-69,168-181
```

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### Platform Notes (Continued)

```

NUMA node5 CPU(s):          70-83,182-195
NUMA node6 CPU(s):          84-97,196-209
NUMA node7 CPU(s):          98-111,210-223
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:    Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:    Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:        Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	5.3M	12	Data	1	64	1	64
L1i	32K	3.5M	8	Instruction	1	64	1	64
L2	2M	224M	16	Unified	2	2048	1	64
L3	112.5M	225M	15	Unified	3	122880	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-13,112-125
node 0 size: 128610 MB
node 0 free: 126905 MB
node 1 cpus: 14-27,126-139
node 1 size: 129016 MB
node 1 free: 128126 MB
node 2 cpus: 28-41,140-153
node 2 size: 129016 MB
node 2 free: 128120 MB
node 3 cpus: 42-55,154-167
node 3 size: 129016 MB
node 3 free: 128116 MB
node 4 cpus: 56-69,168-181
node 4 size: 129016 MB
node 4 free: 128094 MB
node 5 cpus: 70-83,182-195
node 5 size: 129016 MB
node 5 free: 128034 MB
node 6 cpus: 84-97,196-209
node 6 size: 129016 MB
node 6 free: 128121 MB
node 7 cpus: 98-111,210-223
node 7 size: 128935 MB
node 7 free: 127940 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  17  17  17  26  26  26  26
1:  17  10  17  17  26  26  26  26
2:  17  17  10  17  26  26  26  26
3:  17  17  17  10  26  26  26  26
4:  26  26  26  26  10  17  17  17
5:  26  26  26  26  17  10  17  17
6:  26  26  26  26  17  17  10  17
7:  26  26  26  26  17  17  17  10

```

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### Platform Notes (Continued)

9. /proc/meminfo  
MemTotal: 1056406708 kB

10. who -r  
run-level 3 Jul 14 10:59

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
Default Target Status  
multi-user degraded

12. Failed units, from systemctl list-units --state=failed  
UNIT LOAD ACTIVE SUB DESCRIPTION  
\* sep5.service loaded failed failed systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled apparmor auditd chronyd cron getty@ haveged irqbalance issue-generator iuvolt kbdsettings postfix purge-kernels rollback sep5 sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled boot-sysctl ca-certificates chrony-wait console-getty debug-shell exchange-bmc-os-info grub2-once haveged-switch-root ipmievd issue-add-ssh-keys kexec-load lunmask munge nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ slurmd smartd smartd\_generate\_opts srp\_daemon srp\_daemon\_port@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd ypbind  
indirect wickedd

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default  
root=UUID=3e42e6f0-0a08-4b20-8347-f1472220dfdb  
splash=silent  
mitigations=auto  
quiet

15. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 3.50 GHz.  
The governor "performance" may decide which speed to use within this range.  
boost state support:  
Supported: yes  
Active: yes

16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 20  
vm.dirty\_writeback\_centisecs 500

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### Platform Notes (Continued)

```

vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio       1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness                60
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

```

```

-----
20. Disk information
SPEC is set to: /home/SPECcpu2017_ic2023
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda3   xfs   413G 172G 241G  42% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:      Nettrix
Product:     N/A
Product Family: Rack
Serial:      R620G50S22

```

```

-----
22. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
8x Hynix HMC94MEBQA121N 64 GB 2 rank 4800
8x Hynix HMC94MEBQA123N 64 GB 2 rank 4800
8x Intel 16 GB 1 rank 3200

```

23. BIOS

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### Platform Notes (Continued)

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: NNH1041020  
BIOS Date: 01/09/2023  
BIOS Revision: 5.29

Each Intel Xeon CPU Max processor is configured with 64 GB of High Bandwidth Memory (HBM) in-package. dmidecode is additionally reporting the capacity of the CPU in-package HBM stack as: '8x Intel 16 GB 1 rank 3200'

### Compiler Version Notes

=====  
C | 502.gcc\_r(peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C | 502.gcc\_r(peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbnk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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=====

=====  
Fortran | 548.exchange2\_r(base, peak)  
=====

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=====



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Nettrix

SPECrate®2017\_int\_base = 923

R620 G50 (Intel Xeon Max 9480, 1.9 GHz)

SPECrate®2017\_int\_peak = 944

CPU2017 License: 6138  
Test Sponsor: Nettrix  
Tested by: Nettrix

Test Date: Jul-2023  
Hardware Availability: Feb-2023  
Software Availability: Dec-2022

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```



# SPEC CPU®2017 Integer Rate Result

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Test Date: Jul-2023  
Hardware Availability: Feb-2023  
Software Availability: Dec-2022

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

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Test Date: Jul-2023  
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Software Availability: Dec-2022

## Peak Optimization Flags (Continued)

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.xml>

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