



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380a Gen11

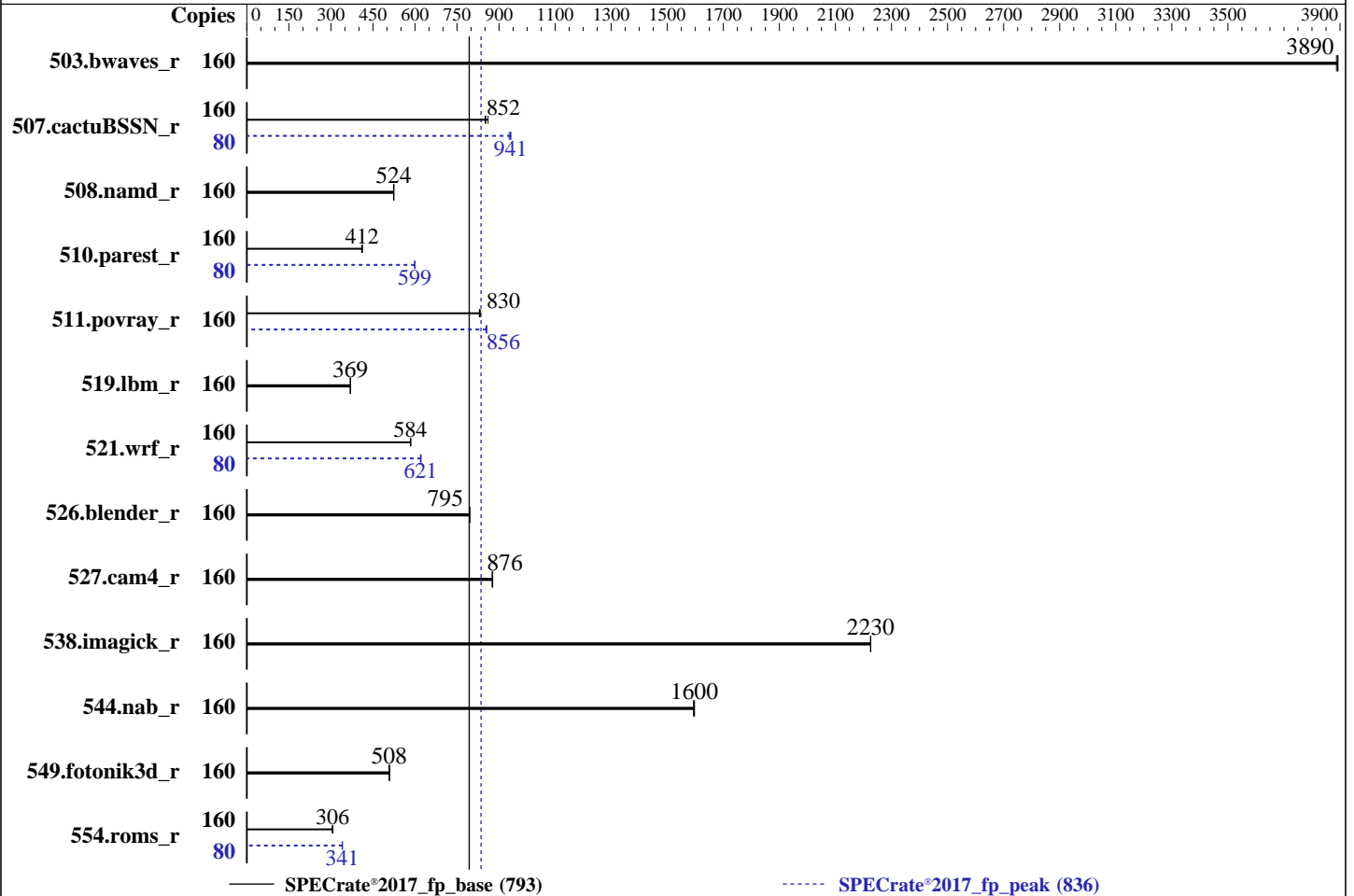
(2.00 GHz, Intel Xeon Platinum 8460Y+)

## SPECrate®2017\_fp\_base = 793

## SPECrate®2017\_fp\_peak = 836

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** May-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022



### Hardware

CPU Name: Intel Xeon Platinum 8460Y+  
 Max MHz: 3700  
 Nominal: 2000  
 Enabled: 80 cores, 2 chips, 2 threads/core  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 105 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 1.6 TB NVMe SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.0 (Plow)  
 Kernel 5.14.0-70.13.1.el9\_0.x86\_64  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: HPE BIOS Version v1.22 01/18/2023 released Jan-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: May-2023  
Hardware Availability: Mar-2023  
Software Availability: Dec-2022

## Results Table

| Benchmark       | Base   |             |             |            |            |             |             | Peak   |            |             |            |            |             |             |
|-----------------|--------|-------------|-------------|------------|------------|-------------|-------------|--------|------------|-------------|------------|------------|-------------|-------------|
|                 | Copies | Seconds     | Ratio       | Seconds    | Ratio      | Seconds     | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio      | Seconds     | Ratio       |
| 503.bwaves_r    | 160    | <b>412</b>  | <b>3890</b> | 412        | 3890       | 413         | 3890        | 160    | <b>412</b> | <b>3890</b> | 412        | 3890       | 413         | 3890        |
| 507.cactuBSSN_r | 160    | 238         | 850         | 235        | 861        | <b>238</b>  | <b>852</b>  | 80     | 108        | 942         | 108        | 936        | <b>108</b>  | <b>941</b>  |
| 508.namd_r      | 160    | 290         | 524         | 290        | 525        | <b>290</b>  | <b>524</b>  | 160    | 290        | 524         | 290        | 525        | <b>290</b>  | <b>524</b>  |
| 510.parest_r    | 160    | <b>1017</b> | <b>412</b>  | 1022       | 410        | 1015        | 412         | 80     | 349        | 599         | <b>349</b> | <b>599</b> | 349         | 599         |
| 511.povray_r    | 160    | 449         | 833         | <b>450</b> | <b>830</b> | 450         | 830         | 160    | 438        | 854         | 437        | 856        | <b>437</b>  | <b>856</b>  |
| 519.lbm_r       | 160    | 456         | 370         | 457        | 369        | <b>456</b>  | <b>369</b>  | 160    | 456        | 370         | 457        | 369        | <b>456</b>  | <b>369</b>  |
| 521.wrf_r       | 160    | <b>614</b>  | <b>584</b>  | 614        | 584        | 612         | 585         | 80     | 289        | 620         | 288        | 622        | <b>289</b>  | <b>621</b>  |
| 526.blender_r   | 160    | 306         | 795         | 307        | 793        | <b>306</b>  | <b>795</b>  | 160    | 306        | 795         | 307        | 793        | <b>306</b>  | <b>795</b>  |
| 527.cam4_r      | 160    | <b>320</b>  | <b>876</b>  | 319        | 877        | 320         | 876         | 160    | <b>320</b> | <b>876</b>  | 319        | 877        | 320         | 876         |
| 538.imagick_r   | 160    | <b>179</b>  | <b>2230</b> | 179        | 2230       | 179         | 2220        | 160    | <b>179</b> | <b>2230</b> | 179        | 2230       | 179         | 2220        |
| 544.nab_r       | 160    | 169         | 1590        | 169        | 1600       | <b>169</b>  | <b>1600</b> | 160    | 169        | 1590        | 169        | 1600       | <b>169</b>  | <b>1600</b> |
| 549.fotonik3d_r | 160    | 1225        | 509         | 1227       | 508        | <b>1227</b> | <b>508</b>  | 160    | 1225       | 509         | 1227       | 508        | <b>1227</b> | <b>508</b>  |
| 554.roms_r      | 160    | <b>830</b>  | <b>306</b>  | 830        | 306        | 833         | 305         | 80     | <b>373</b> | <b>341</b>  | 374        | 340        | 373         | 341         |

SPECrate®2017\_fp\_base = **793**

SPECrate®2017\_fp\_peak = **836**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
IRQ balance service was stopped using "systemctl stop irqbalance.service"
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
perf-bias for all the CPUs is set using "cpupower set -b 0"
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380a Gen11**

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

The system ROM used for this result contains Intel microcode version 0x2b000161 for the Intel Xeon Platinum 8460Y+ Processor

BIOS Configuration:

Workload Profile set to General Throughput Compute

Thermal Configuration set to Maximum Cooling

Enhanced Processor Performance Profile set to Aggressive

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Memory Patrol Scrubbing set to Disabled

Workload Profile set to Custom

DCU Stream Prefetcher set to Disabled

Adjacent Sector Prefetch set to Disabled

Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost.localdomain Wed May 17 17:22:45 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9\_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380a Gen11**

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

1. uname -a
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux

```

```

2. w
17:22:45 up 0 min,  0 users,  load average: 4.92, 1.72, 0.61
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT

```

```

3. Username
From environment variable $USER:  root

```

```

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size               (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                   (blocks, -f) unlimited
pending signals             (-i) 2062712
max locked memory           (kbytes, -l) 64
max memory size             (kbytes, -m) unlimited
open files                  (-n) 1024
pipe size                   (512 bytes, -p) 8
POSIX message queues        (bytes, -q) 819200
real-time priority          (-r) 0
stack size                  (kbytes, -s) unlimited
cpu time                    (seconds, -t) unlimited
max user processes          (-u) 2062712
virtual memory              (kbytes, -v) unlimited
file locks                  (-x) unlimited

```

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 18
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fprate.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 --configfile
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8460Y+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

stepping      : 6
microcode     : 0x2b000161
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores     : 40
siblings      : 80
2 physical ids (chips)
160 processors (hardware threads)
physical id 0: core ids 0-39
physical id 1: core ids 0-39
physical id 0: apicids 0-79
physical id 1: apicids 128-207

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 160
On-line CPU(s) list:   0-159
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:              Intel(R) Xeon(R) Platinum 8460Y+
BIOS Model name:        Intel(R) Xeon(R) Platinum 8460Y+
CPU family:              6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    40
Socket(s):               2
Stepping:               6
BogoMIPS:                4000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                        vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                        erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                        clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:         VT-x
L1d cache:              3.8 MiB (80 instances)
L1i cache:              2.5 MiB (80 instances)
L2 cache:               160 MiB (80 instances)
L3 cache:               210 MiB (2 instances)
NUMA node(s):           8
NUMA node0 CPU(s):      0-9,80-89
NUMA node1 CPU(s):      10-19,90-99

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380a Gen11**

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** May-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

NUMA node2 CPU(s):          20-29,100-109
NUMA node3 CPU(s):          30-39,110-119
NUMA node4 CPU(s):          40-49,120-129
NUMA node5 CPU(s):          50-59,130-139
NUMA node6 CPU(s):          60-69,140-149
NUMA node7 CPU(s):          70-79,150-159
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:         Not affected
Vulnerability Meltdown:    Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:   Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:   Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:       Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS   | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|--------|----------|----------------|
| L1d  | 48K      | 3.8M     | 12   | Data        | 1     | 64     | 1        | 64             |
| L1i  | 32K      | 2.5M     | 8    | Instruction | 1     | 64     | 1        | 64             |
| L2   | 2M       | 160M     | 16   | Unified     | 2     | 2048   | 1        | 64             |
| L3   | 105M     | 210M     | 15   | Unified     | 3     | 114688 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-9,80-89
node 0 size: 64220 MB
node 0 free: 63391 MB
node 1 cpus: 10-19,90-99
node 1 size: 64507 MB
node 1 free: 64136 MB
node 2 cpus: 20-29,100-109
node 2 size: 64507 MB
node 2 free: 64111 MB
node 3 cpus: 30-39,110-119
node 3 size: 64507 MB
node 3 free: 64045 MB
node 4 cpus: 40-49,120-129
node 4 size: 64507 MB
node 4 free: 64153 MB
node 5 cpus: 50-59,130-139
node 5 size: 64507 MB
node 5 free: 64147 MB
node 6 cpus: 60-69,140-149
node 6 size: 64507 MB
node 6 free: 64147 MB
node 7 cpus: 70-79,150-159
node 7 size: 64451 MB
node 7 free: 64064 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  20  30  30  30  30  30  30
1:  20  10  30  30  30  30  30  30
2:  30  30  10  20  30  30  30  30
3:  30  30  20  10  30  30  30  30
4:  30  30  30  30  10  20  30  30
5:  30  30  30  30  20  10  30  30
6:  30  30  30  30  30  30  10  20

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** May-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

7: 30 30 30 30 30 30 30 20 10

9. /proc/meminfo

MemTotal: 528095280 kB

10. who -r

run-level 3 May 17 17:22

11. Systemd service manager version: systemd 250 (250-6.el9\_0)

Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond  
dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode  
nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd  
systemd-network-generator tuned udisks2

enabled-runtime systemd-remount-fs

disabled blk-availability chrony-wait chronyd console-getty cpupower debug-shell kvm\_stat  
man-db-restart-cache-update nftables powertop rdisc rhsm rhsm-facts rpmdb-rebuild  
serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex  
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9\_0.x86\_64  
root=/dev/mapper/rhel-root  
ro  
resume=/dev/mapper/rhel-swap  
rd.lvm.lv=rhel/root  
rd.lvm.lv=rhel/swap

14. cpupower frequency-info

analyzing CPU 0:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

15. tuned-adm active

Current active profile: throughput-performance

16. sysctl

kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 40  
vm.dirty\_writeback\_centisecs 500

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380a Gen11**

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** May-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022

## Platform Notes (Continued)

```

vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold        500
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy  0
vm.nr_overcommit_hugepages 0
vm.swappiness               10
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled        [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)

```

```

-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      1.4T      154G 1.3T  11% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:         HPE
Product:        ProLiant DL380a Gen11
Product Family: ProLiant
Serial:         CNX22602MZ

```

```

-----
22. dmidecode
Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  7x Hynix HMC88AEBRA168N 32 GB 2 rank 4800
  6x Hynix HMC88MEBRA113N 32 GB 2 rank 4800
  3x Hynix HMC88MEBRA115N 32 GB 2 rank 4800

```

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL380a Gen11**

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## Platform Notes (Continued)

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 1.22  
BIOS Date: 01/18/2023  
BIOS Revision: 1.22  
Firmware Revision: 1.30

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** May-2023  
**Hardware Availability:** Mar-2023  
**Software Availability:** Dec-2022

## Compiler Version Notes (Continued)

### Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

### Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

### Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: May-2023

Hardware Availability: Mar-2023

Software Availability: Dec-2022

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017\_fp\_base = 793

SPECrate®2017\_fp\_peak = 836

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

## Peak Optimization Flags (Continued)

```
554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL380a Gen11

(2.00 GHz, Intel Xeon Platinum 8460Y+)

**SPECrate®2017\_fp\_base = 793**

**SPECrate®2017\_fp\_peak = 836**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** May-2023

**Hardware Availability:** Mar-2023

**Software Availability:** Dec-2022

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-05-17 07:52:45-0400.

Report generated on 2023-07-05 11:04:22 by CPU2017 PDF formatter v6716.

Originally published on 2023-07-04.