



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

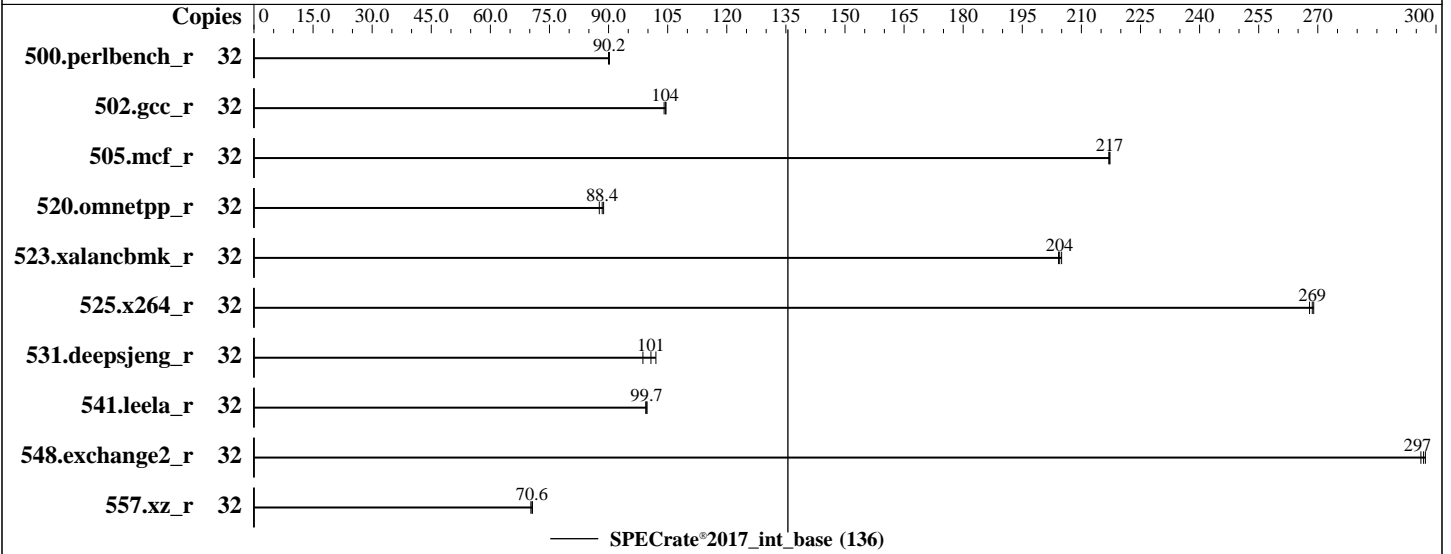
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Apr-2023

Hardware Availability: Jul-2021

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Silver 4309Y  
 Max MHz: 3600  
 Nominal: 2800  
 Enabled: 16 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 12 MB I+D on chip per chip  
 Other: None  
 Memory: 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86\_64)  
 Kernel 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;  
 Parallel: No  
 Firmware: Lenovo BIOS Version U8E121H 1.50 released Feb-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 136

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Apr-2023  
Hardware Availability: Jul-2021  
Software Availability: Dec-2022

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	<b>565</b>	<b>90.2</b>	565	90.2	566	90.0							
502.gcc_r	32	433	105	435	104	<b>434</b>	<b>104</b>							
505.mcf_r	32	238	217	238	217	<b>238</b>	<b>217</b>							
520.omnetpp_r	32	479	87.6	<b>475</b>	<b>88.4</b>	473	88.7							
523.xalancbmk_r	32	165	205	165	204	<b>165</b>	<b>204</b>							
525.x264_r	32	209	268	<b>209</b>	<b>269</b>	208	269							
531.deepsjeng_r	32	359	102	<b>364</b>	<b>101</b>	371	98.7							
541.leela_r	32	531	99.8	533	99.4	<b>532</b>	<b>99.7</b>							
548.exchange2_r	32	282	297	<b>282</b>	<b>297</b>	283	296							
557.xz_r	32	492	70.3	<b>490</b>	<b>70.6</b>	489	70.7							

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-ic2023.0/lib/intel64:/home/cpu2017-1.1.9-ic2023.0/lib/ia32:/home/cpu2017-1.1.9-ic
2023.0/je5.0.1-32"
MALLOC_CONF = "retain:true"
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 136

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Date:** Apr-2023

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Jul-2021

**Tested by:** Lenovo Global Technology

**Software Availability:** Dec-2022

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
DCU Streamer Prefetcher set to Disabled  
UPI Link Disable set to Disabled 1 Link

Sysinfo program /home/cpu2017-1.1.9-ic2023.0/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Apr 16 20:52:23 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT\_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)  
x86\_64 x86\_64 x86\_64 GNU/Linux  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

```
-----
2. w
 20:52:23 up 27 min,  1 user,  load average: 0.16, 0.10, 0.05
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root      ttyl      -              20:38    7.00s  1.34s  0.01s  -bash
```

```
-----
3. Username
  From environment variable $USER:  root
```

```
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size          (kbytes, -d) unlimited
scheduling priority    (-e) 0
file size              (blocks, -f) unlimited
pending signals        (-i) 8255548
max locked memory      (kbytes, -l) 64
max memory size        (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority     (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes     (-u) 8255548
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runccpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
  ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=16 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base -o all intrate
runccpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
  ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=16 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
  rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.001/temlogs/preenv.intrate.001.0.log --lognum 001.0 --from_runccpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.0
```

```
-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
vendor_id      : GenuineIntel
cpu family     : 6
model          : 106
stepping       : 6
microcode      : 0xd000389
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 8
siblings       : 16
2 physical ids (chips)
32 processors (hardware threads)
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 136

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

physical id 0: core ids 0-7  
physical id 1: core ids 0-7  
physical id 0: apicids 0-15  
physical id 1: apicids 64-79

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                32
On-line CPU(s) list:   0-31
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) Silver 4309Y CPU @ 2.80GHz
CPU family:            6
Model:                 106
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             2
Stepping:              6
CPU max MHz:           3600.0000
CPU min MHz:           800.0000
BogoMIPS:              5600.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2
                        ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch
                        cpuid_fault epb cat_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb stibp
                        ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase
                        tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
                        avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd
                        sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
                        cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd
                        dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm
                        md_clear pconfig flush_l1d arch_capabilities

Virtualization:        VT-x
L1d cache:             768 KiB (16 instances)
L1i cache:             512 KiB (16 instances)
L2 cache:              20 MiB (16 instances)
L3 cache:              24 MiB (2 instances)
NUMA node(s):         2
NUMA node0 CPU(s):    0-7,16-23
NUMA node1 CPU(s):    8-15,24-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:   Not affected
Vulnerability Tsx async abort: Not affected

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	1.3M	20M	20	Unified	2	1024	1	64
L3	12M	24M	12	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-7,16-23
node 0 size: 1031778 MB
node 0 free: 1008100 MB
node 1 cpus: 8-15,24-31
node 1 size: 1032133 MB
node 1 free: 1031747 MB
node distances:
node  0  1
  0: 10 20
  1: 20 10

```

9. /proc/meminfo

MemTotal: 2113444968 kB

10. who -r

run-level 3 Apr 16 20:25

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi
issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld gpm grub2-once haveged-switch-root ipmi ipmievd iscsi-init iscsid iscsiuiio
issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb rdisc
rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync systemd-timesyncd
indirect wickedd

```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=6cc1fa87-8ca7-4ee9-8302-06728562cf7b
splash=silent
mitigations=auto
quiet
security=apparmor

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 136

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Apr-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2021

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

### Platform Notes (Continued)

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.60 GHz.
                  The governor "powersave" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes
-----
```

```
-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness   20
vm.dirty_background_bytes     0
vm.dirty_background_ratio     10
vm.dirty_bytes                0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages               0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  60
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0
-----
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2023.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   889G  25G  864G   3% /
-----
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023  
**Hardware Availability:** Jul-2021  
**Software Availability:** Dec-2022

### Platform Notes (Continued)

-----  
20. /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem ST650V2  
Product Family: ThinkSystem  
Serial: 1234567890  
-----

-----  
21. dmidecode  
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
32x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666  
-----

-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: U8E121H-1.50  
BIOS Date: 02/09/2023  
BIOS Revision: 1.50  
Firmware Revision: 2.50  
-----

### Compiler Version Notes

-----  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557.xz\_r(base)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base) 541.leela\_r(base)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

-----  
Fortran | 548.exchange2\_r(base)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
-----

### Base Compiler Invocation

C benchmarks:  
icx

(Continued on next page)





# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

SPECrate®2017\_int\_base = 136

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Date:** Apr-2023

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Jul-2021

**Tested by:** Lenovo Global Technology

**Software Availability:** Dec-2022

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2023.0.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem ST650 V2  
(2.80 GHz, Intel Xeon Silver 4309Y)

SPECrate®2017\_int\_base = 136

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023

**Hardware Availability:** Jul-2021

**Software Availability:** Dec-2022

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-J.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-J.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-04-16 08:52:22-0400.

Report generated on 2024-01-29 17:41:40 by CPU2017 PDF formatter v6716.

Originally published on 2023-05-09.