



# SPEC CPU®2017 Integer Rate Result

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## Lenovo Global Technology

ThinkSystem SR655 V3  
(4.05 GHz,AMD EPYC 9274F)

SPECrate®2017\_int\_base = 315

SPECrate®2017\_int\_peak = 323

CPU2017 License: 9017

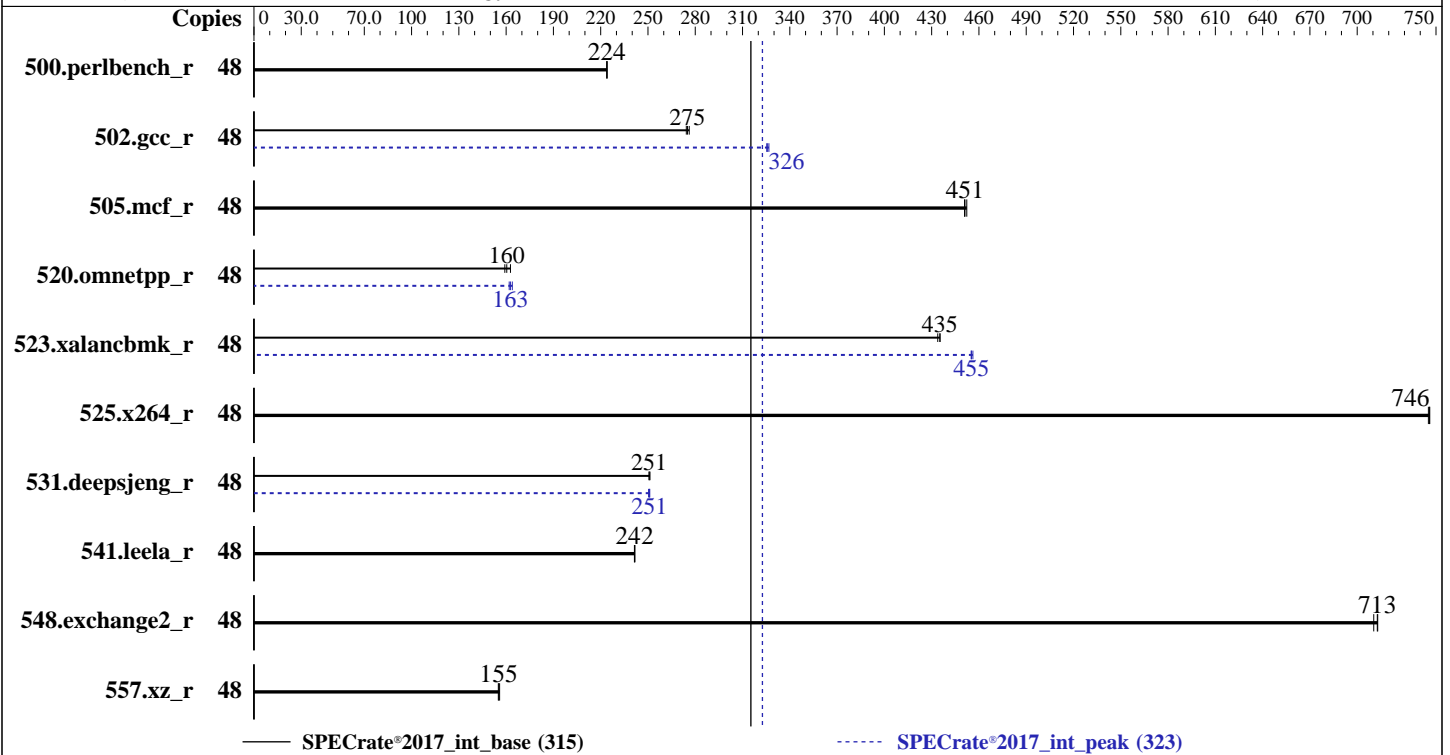
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Apr-2023

Hardware Availability: Apr-2023

Software Availability: Nov-2022



### Hardware

CPU Name: AMD EPYC 9274F  
 Max MHz: 4300  
 Nominal: 4050  
 Enabled: 24 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 32 MB shared / 3 cores  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
 Kernel 5.14.21-150400.22-default  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: No  
 Firmware: Lenovo BIOS Version KAE109F 1.40 released Feb-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	341	224	<b><u>341</u></b>	<b><u>224</u></b>	341	224	48	341	224	<b><u>341</u></b>	<b><u>224</u></b>	341	224
502.gcc_r	48	<b><u>247</u></b>	<b><u>275</u></b>	248	274	246	276	48	209	325	<b><u>209</u></b>	<b><u>326</u></b>	208	327
505.mcf_r	48	<b><u>172</u></b>	<b><u>451</u></b>	172	451	172	452	48	<b><u>172</u></b>	<b><u>451</u></b>	172	451	172	452
520.omnetpp_r	48	396	159	387	163	<b><u>393</u></b>	<b><u>160</u></b>	48	389	162	384	164	<b><u>387</u></b>	<b><u>163</u></b>
523.xalancbmk_r	48	116	435	<b><u>116</u></b>	<b><u>435</u></b>	117	434	48	<b><u>111</u></b>	<b><u>455</u></b>	111	456	111	455
525.x264_r	48	113	745	<b><u>113</u></b>	<b><u>746</u></b>	113	746	48	113	745	<b><u>113</u></b>	<b><u>746</u></b>	113	746
531.deepsjeng_r	48	219	251	<b><u>219</u></b>	<b><u>251</u></b>	219	251	48	220	250	<b><u>219</u></b>	<b><u>251</u></b>	219	251
541.leela_r	48	<b><u>329</u></b>	<b><u>242</u></b>	329	242	329	241	48	<b><u>329</u></b>	<b><u>242</u></b>	329	242	329	241
548.exchange2_r	48	176	713	177	710	<b><u>176</u></b>	<b><u>713</u></b>	48	176	713	177	710	<b><u>176</u></b>	<b><u>713</u></b>
557.xz_r	48	333	156	<b><u>334</u></b>	<b><u>155</u></b>	334	155	48	333	156	<b><u>334</u></b>	<b><u>155</u></b>	334	155

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To enable THP for all allocations for peak runs,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-amd-aocc400-genoa-Ble/amd_rate_aocc400_genoa_B_lib/lib:/home/cpu2017-1.1.9-amd-ao
cc400-genoa-Ble/amd_rate_aocc400_genoa_B_lib/lib32:"
MALLOC_CONF = "retain:true"
```

Environment variables set by runcpu during the 523.xalancbmk\_r peak run:

```
MALLOC_CONF = "thp:never"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance and then set it to Custom Mode  
NUMA Nodes per Socket set to NPS4

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc400-genoa-Ble/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Apr 5 15:29:44 2023
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode

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## Platform Notes (Continued)

22. BIOS

1. `uname -a`

```
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

2. `w`

```
15:29:45 up 5 min, 1 user, load average: 0.04, 0.07, 0.03
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU WHAT
root      tty1    -             15:27   17.00s 0.92s  0.04s /bin/bash ./amd_rate_aocc400_genoa_B1.sh
```

3. Username

```
From environment variable $USER: root
```

4. `ulimit -a`

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 1546786
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes     (-u) 1546786
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. `sysinfo process ancestry`

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./speccpu_genoa.sh
/bin/bash ./Run025-compliant-amd-rateint.sh
python3 ./run_amd_rate_aocc400_genoa_B1.py
/bin/bash ./amd_rate_aocc400_genoa_B1.sh
runcpu --config amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower --runmode
rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.285/templogs/preenv.intrate.285.0.log --lognum 285.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc400-genoa-B1e
```

6. `/proc/cpuinfo`

```
model name      : AMD EPYC 9274F 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
```

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### Platform Notes (Continued)

```
microcode      : 0xa101111
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 24
siblings      : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-2,8-10,16-18,24-26,32-34,40-42,48-50,56-58
physical id 0: apicids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

#### 7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                48
On-line CPU(s) list:   0-47
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 9274F 24-Core Processor
CPU family:            25
Model:                 17
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              1
Stepping:              1
Frequency boost:       enabled
CPU max MHz:           4303.1250
CPU min MHz:           1500.0000
BogoMIPS:              8087.74
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
                        pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                        abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
                        perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
                        invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
                        avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
                        svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                        pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                        avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
Virtualization:        AMD-V
L1d cache:             768 KiB (24 instances)
L1i cache:             768 KiB (24 instances)
L2 cache:              24 MiB (24 instances)
L3 cache:              256 MiB (8 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0-5,24-29
NUMA node1 CPU(s):    6-11,30-35
NUMA node2 CPU(s):    12-17,36-41
NUMA node3 CPU(s):    18-23,42-47
```

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### Platform Notes (Continued)

Vulnerability Itlb multihit:	Not affected
Vulnerability Lltf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	768K	8	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	8	Unified	2	2048	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-5,24-29
node 0 size: 96518 MB
node 0 free: 96035 MB
node 1 cpus: 6-11,30-35
node 1 size: 96764 MB
node 1 free: 96514 MB
node 2 cpus: 12-17,36-41
node 2 size: 96730 MB
node 2 free: 96506 MB
node 3 cpus: 18-23,42-47
node 3 size: 96706 MB
node 3 free: 96194 MB
node distances:
node  0  1  2  3
 0:  10  12  12  12
 1:  12  10  12  12
 2:  12  12  10  12
 3:  12  12  12  10

```

9. /proc/meminfo

MemTotal: 396001936 kB

10. who -r

run-level 3 Apr 5 15:24

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

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### Platform Notes (Continued)

```

enabled-runtime  systemd-remount-fs
disabled         autofs  autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                 chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                 firewallld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievd iscsi-init
                 iscsid iscsiuiio issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs
                 nfs-blkmap nmb rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb
                 snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                 systemd-time-wait-sync systemd-timesyncd
generated       ntp_sync
indirect        wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=4fcd643d-e392-48aa-b6f6-80f024d1c633
splash=silent
mitigations=auto
quiet
security=apparmor

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 1.50 GHz and 4.05 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     0
vm.compaction_proactiveness    20
vm.dirty_background_bytes     0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  1
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          1

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled    [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

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### Platform Notes (Continued)

```

17. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs  60000
   defrag                  1
   max_ptes_none          511
   max_ptes_shared        256
   max_ptes_swap           64
   pages_to_scan          4096
   scan_sleep_millisecs   10000

```

```

18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP4

```

```

19. Disk information
SPECC is set to: /home/cpu2017-1.1.9-amd-aocc400-genoa-Ble
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   446G  24G  422G   6% /

```

```

20. /sys/devices/virtual/dmi/id
   Vendor:      Lenovo
   Product:     ThinkSystem SR655V3
   Product Family: ThinkSystem
   Serial:      1234567890

```

```

21. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  12x Samsung M321R4GA3BB0-CQKMG 32 GB 2 rank 4800

```

```

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:      Lenovo
   BIOS Version:     KAE109F-1.40
   BIOS Date:        02/27/2023
   BIOS Revision:    1.40
   Firmware Revision: 1.40

```

### Compiler Version Notes

C | 502.gcc\_r(peak)

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

```

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)

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### Compiler Version Notes (Continued)

| 557.xz\_r(base, peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 502.gcc\_r(peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C++ | 523.xalancbmk\_r(peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base,  
| peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C++ | 523.xalancbmk\_r(peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: i386-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

=====  
C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base, peak) 541.leela\_r(base,  
| peak)

-----  
AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)

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# SPEC CPU®2017 Integer Rate Result

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## Lenovo Global Technology

ThinkSystem SR655 V3  
(4.05 GHz,AMD EPYC 9274F)

SPECrate®2017\_int\_base = 315

SPECrate®2017\_int\_peak = 323

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2023  
**Hardware Availability:** Apr-2023  
**Software Availability:** Nov-2022

### Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

-----  
Fortran | 548.exchange2\_r(base, peak)  
-----

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389\_2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----

### Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

### Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

### Base Optimization Flags

C benchmarks:  
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather

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## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

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# SPEC CPU®2017 Integer Rate Result

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CPU2017 License: 9017

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Tested by: Lenovo Global Technology

Test Date: Apr-2023

Hardware Availability: Apr-2023

Software Availability: Nov-2022

## Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: basepeak = yes
```

```
502.gcc_r: -m32 -flto -z muldefs -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc
```

```
505.mcf_r: basepeak = yes
```

```
525.x264_r: basepeak = yes
```

```
557.xz_r: basepeak = yes
```

C++ benchmarks:

(Continued on next page)



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## Peak Optimization Flags (Continued)

```
520.omnetpp_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-inline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

```
523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -inline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdalloc-ext
```

```
531.deepsjeng_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -inline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v118/aocc4/b1/rate/amd\_rate\_aocc400\_genoa\_B\_lib/lib32

C++ benchmarks (except as noted below):

-Wno-unused-command-line-argument

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Software Availability: Nov-2022

## Peak Other Flags (Continued)

```
523.xalancbmk_r: -L/usr/lib32 -Wno-unused-command-line-argument  
-L/home/work/cpu2017/v118/aocc4/bl/rate/amd_rate_aocc400_genoa_B_lib/lib32
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-R.html>

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-R.xml>

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

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