



# SPEC CPU®2017 Floating Point Rate Result

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## Nettrix

SPECrate®2017\_fp\_base = 591

R620 G50 (Intel Xeon Gold 6418H, 2.10 GHz)

SPECrate®2017\_fp\_peak = 591

CPU2017 License: 6138

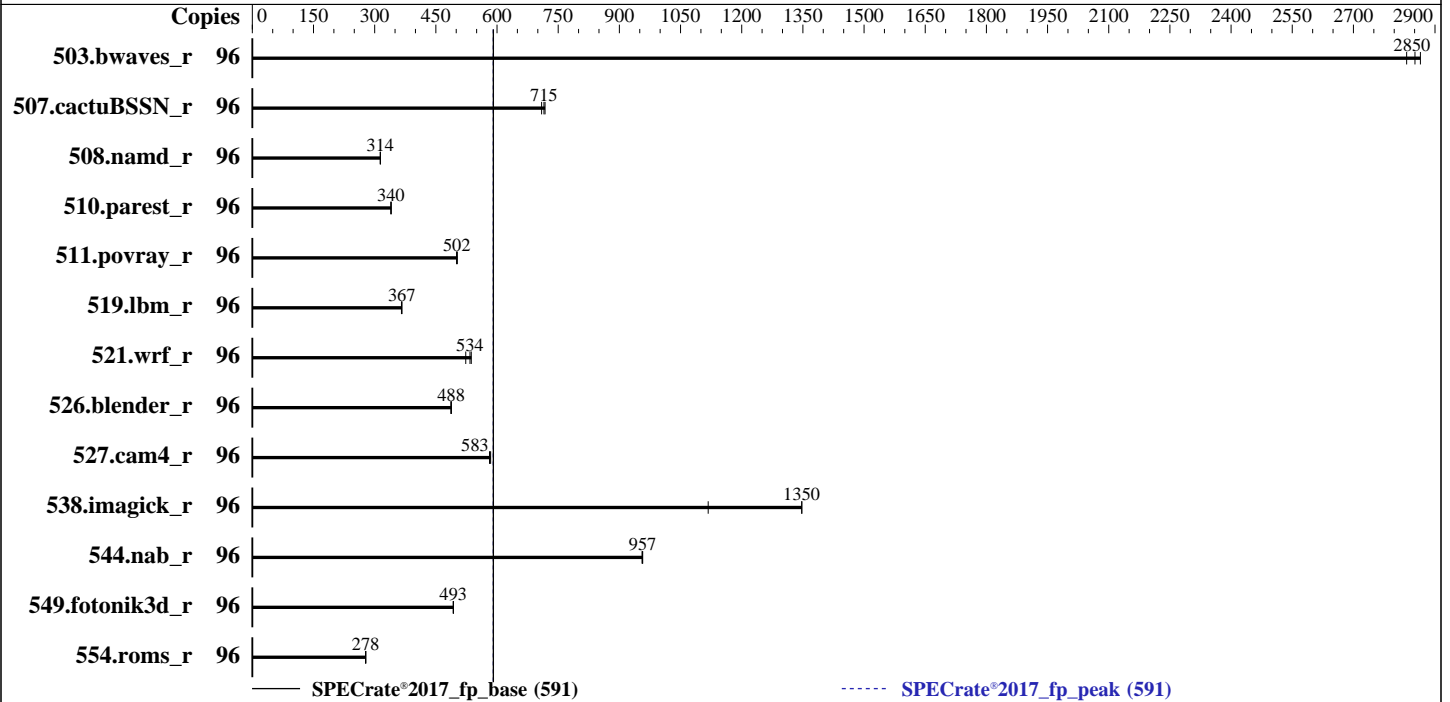
Test Sponsor: Nettrix

Tested by: Nettrix

Test Date: Mar-2023

Hardware Availability: Jan-2023

Software Availability: Dec-2022



### Hardware

CPU Name: Intel Xeon Gold 6418H  
 Max MHz: 4000  
 Nominal: 2100  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 60 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++  
 Compiler for Linux;  
 Fortran: Version 2023.0 of Intel Fortran Compiler  
 for Linux;  
 Parallel: No  
 Firmware: Nettrix BIOS Version NNH1041018-U00-1 released  
 Nov-2022  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost  
 of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	<b>338</b>	<b>2850</b>	336	2860	340	2830	96	<b>338</b>	<b>2850</b>	336	2860	340	2830
507.cactuBSSN_r	96	169	718	171	709	<b>170</b>	<b>715</b>	96	169	718	171	709	<b>170</b>	<b>715</b>
508.namd_r	96	290	314	291	314	<b>290</b>	<b>314</b>	96	290	314	291	314	<b>290</b>	<b>314</b>
510.parest_r	96	739	340	<b>739</b>	<b>340</b>	738	340	96	739	340	<b>739</b>	<b>340</b>	738	340
511.povray_r	96	446	503	<b>447</b>	<b>502</b>	448	501	96	446	503	<b>447</b>	<b>502</b>	448	501
519.lbm_r	96	277	366	<b>276</b>	<b>367</b>	276	367	96	277	366	<b>276</b>	<b>367</b>	276	367
521.wrf_r	96	<b>403</b>	<b>534</b>	411	523	401	537	96	<b>403</b>	<b>534</b>	411	523	401	537
526.blender_r	96	<b>300</b>	<b>488</b>	300	488	300	487	96	<b>300</b>	<b>488</b>	300	488	300	487
527.cam4_r	96	289	582	287	584	<b>288</b>	<b>583</b>	96	289	582	287	584	<b>288</b>	<b>583</b>
538.imagick_r	96	177	1350	<b>177</b>	<b>1350</b>	214	1120	96	177	1350	<b>177</b>	<b>1350</b>	214	1120
544.nab_r	96	<b>169</b>	<b>957</b>	169	957	169	956	96	<b>169</b>	<b>957</b>	169	957	169	956
549.fotonik3d_r	96	<b>759</b>	<b>493</b>	759	493	759	493	96	<b>759</b>	<b>493</b>	759	493	759	493
554.roms_r	96	549	278	548	278	<b>549</b>	<b>278</b>	96	549	278	548	278	<b>549</b>	<b>278</b>

SPECrate®2017\_fp\_base = **591**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/lijq/lib/intel64:/home/lijq/je5.0.1-64"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4  
NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)



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### General Notes (Continued)

is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS Configuration:  
SNC (Sub NUMA) set to Enable SNC4 (4-clusters)  
Patrol Scrub set to Disabled  
LLC dead line alloc set to Disabled  
XPT Prefetch set to Enabled  
KTI Prefetch set to Disabled  
DCU Streamer Prefetcher set to Disabled  
Hardware P-States set to Native Mode  
  
Sysinfo program /home/lijq/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sat Apr 30 05:14:11 2022  
  
SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline

(Continued on next page)



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### Platform Notes (Continued)

- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent\_hugepage
- 18. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
-----
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
-----
```

```
-----
2. w
05:14:11 up 9:14, 1 user, load average: 37.66, 79.24, 89.65
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root      tty1     -              20:12       6:02m      1.09s   0.01s  -bash
-----
```

```
-----
3. Username
From environment variable $USER: root
-----
```

```
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4125214
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4125214
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
-----
```

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### Platform Notes (Continued)

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
-bash
runcpu --nobuild --reportable --iterations 3 --define default-platform-flags --define numcopies=96 -c
ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=48 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --reportable --iterations 3 --define default-platform-flags --define numcopies=96
--configfile ic2023.0-lin-sapphirerapids-rate-20221201.cfg --define smt-on --define cores=48 --define
physicalfirst --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all
--nopower --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.051/templogs/preenv.fprate.051.0.log --lognum 051.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/lijq

```

```

6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Gold 6418H
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000111
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 24
siblings       : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-23
physical id 1: core ids 0-23
physical id 0: apicids 0-47
physical id 1: apicids 128-175

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Vendor ID: GenuineIntel

```

(Continued on next page)



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### Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Gold 6418H  
CPU family: 6  
Model: 143  
Thread(s) per core: 2  
Core(s) per socket: 24  
Socket(s): 2  
Stepping: 8  
CPU max MHz: 4000.0000  
CPU min MHz: 800.0000  
BogoMIPS: 4200.00

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni pclmulqdq dtes64 ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb cat\_l3 cat\_l2 cdp\_l3 invpcid\_single intel\_ppin cdp\_l2 ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi flexpriority ept vpid ept\_ad fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local split\_lock\_detect avx\_vnni avx512\_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp\_act\_window hwp\_epp hwp\_pkg\_req avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear serialize tsxldtrk pconfig arch\_lbr avx512\_fp16 amx\_tile flush\_lld arch\_capabilities

Virtualization: VT-x  
L1d cache: 2.3 MiB (48 instances)  
L1i cache: 1.5 MiB (48 instances)  
L2 cache: 96 MiB (48 instances)  
L3 cache: 120 MiB (2 instances)  
NUMA node(s): 4  
NUMA node0 CPU(s): 0-11,48-59  
NUMA node1 CPU(s): 12-23,60-71  
NUMA node2 CPU(s): 24-35,72-83  
NUMA node3 CPU(s): 36-47,84-95  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

(Continued on next page)



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### Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	2M	96M	16	Unified	2	2048	1	64
L3	60M	120M	15	Unified	3	65536	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-11,48-59
node 0 size: 257563 MB
node 0 free: 241224 MB
node 1 cpus: 12-23,60-71
node 1 size: 258007 MB
node 1 free: 245025 MB
node 2 cpus: 24-35,72-83
node 2 size: 258041 MB
node 2 free: 244503 MB
node 3 cpus: 36-47,84-95
node 3 size: 257712 MB
node 3 free: 244573 MB
node distances:
node  0  1  2  3
  0:  10  12  21  21
  1:  12  10  21  21
  2:  21  21  10  12
  3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 1056077316 kB

10. who -r

run-level 3 Apr 29 20:00

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

```

Default Target Status
multi-user      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES

```

(Continued on next page)



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### Platform Notes (Continued)

```

enabled          apparmor auditd cron getty@ haveged irqbalance issue-generator kbdsettings kdump
                  kdump-early postfix purge-kernels rollback smartd sshd wicked wickedd-auto4 wickedd-dhcp4
                  wickedd-dhcp6 wickedd-nanny
enabled-runtime  systemd-remount-fs
disabled         boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
                  exchange-bmc-os-info grub2-once haveged-switch-root ipmievd issue-add-ssh-keys kexec-load
                  lunmask nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@ smartd_generate_opts
                  systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                  systemd-time-wait-sync systemd-timesyncd tuned
indirect         wickedd

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=85a6fd00-fd0c-4883-b29c-e0cf4342a6ea
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=300M,high
crashkernel=72M,low

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 4.00 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. tuned-adm active
  Current active profile: throughput-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500

```

(Continued on next page)





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### Platform Notes (Continued)

```

vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio           1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                    10
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             0

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

```

```

-----
20. Disk information
SPEC is set to: /home/lijq
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       btrfs    892G      156G   736G   18% /home

```

```

-----
21. /sys/devices/virtual/dmi/id
Vendor:          Nettrix
Product:         R620 G50
Product Family:  Rack
Serial:         6101810603447823

```

(Continued on next page)



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## Platform Notes (Continued)

### 22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

16x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: NNH1041018-U00-1  
BIOS Date: 11/01/2022  
BIOS Revision: 5.29

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak)  
| 544.nab\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
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Software Availability: Dec-2022

## Compiler Version Notes (Continued)

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2023.0.0 Build 20221201  
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Version 2023.0.0 Build 20221201  
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201  
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=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201  
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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2023.0.0 Build 20221201  
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## Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

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# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECrate®2017\_fp\_base = 591

R620 G50 (Intel Xeon Gold 6418H, 2.10 GHz)

SPECrate®2017\_fp\_peak = 591

CPU2017 License: 6138

Test Sponsor: Nettrix

Tested by: Nettrix

Test Date: Mar-2023

Hardware Availability: Jan-2023

Software Availability: Dec-2022

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
 526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
 527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math  
 -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
 -Wno-implicit-int -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast  
 -ffast-math -flto -mfpmath=sse -funroll-loops  
 -qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
 -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math -flto  
 -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

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Tested by: Nettrix

Test Date: Mar-2023  
Hardware Availability: Jan-2023  
Software Availability: Dec-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

## Peak Compiler Invocation

C benchmarks:

```
icx
```

C++ benchmarks:

```
icpx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using both C and C++:

```
icpx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```



# SPEC CPU®2017 Floating Point Rate Result

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Nettrix

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R620 G50 (Intel Xeon Gold 6418H, 2.10 GHz)

SPECrate®2017\_fp\_peak = 591

CPU2017 License: 6138  
Test Sponsor: Nettrix  
Tested by: Nettrix

Test Date: Mar-2023  
Hardware Availability: Jan-2023  
Software Availability: Dec-2022

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: basepeak = yes

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes



# SPEC CPU®2017 Floating Point Rate Result

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SPECrate®2017\_fp\_peak = 591

**CPU2017 License:** 6138  
**Test Sponsor:** Nettrix  
**Tested by:** Nettrix

**Test Date:** Mar-2023  
**Hardware Availability:** Jan-2023  
**Software Availability:** Dec-2022

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>  
<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>  
<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.xml>

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