



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

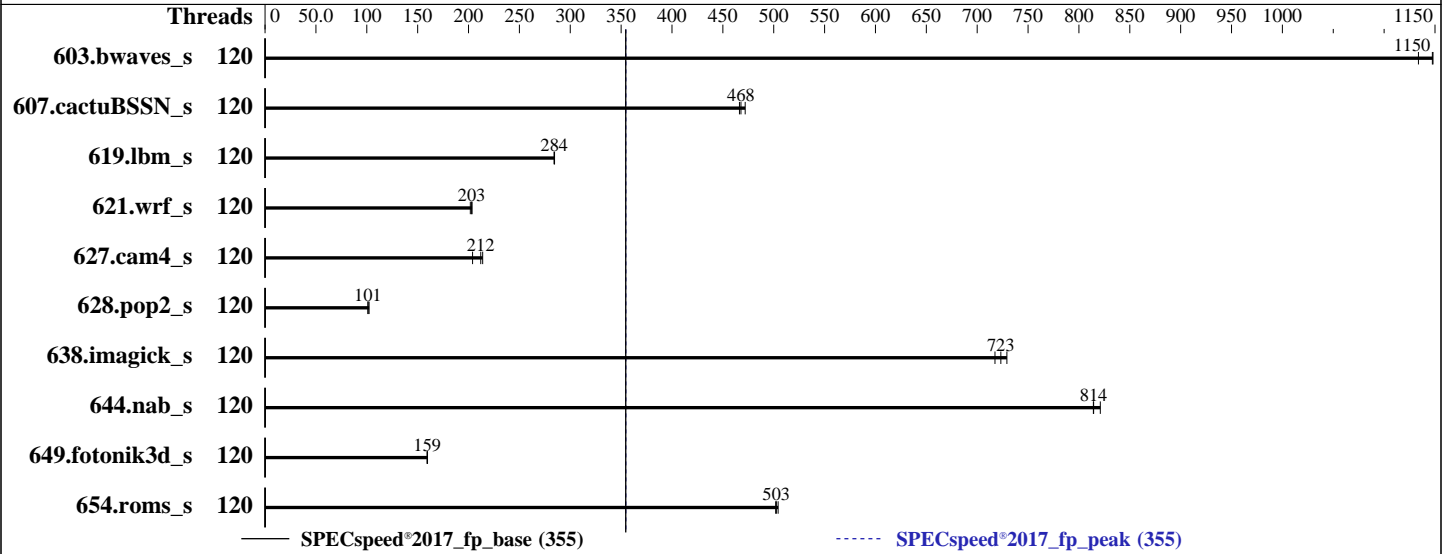
SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022



Hardware

CPU Name: Intel Xeon Platinum 8490H
 Max MHz: 3500
 Nominal: 1900
 Enabled: 120 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 112.5 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 960 GB NVME SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP3
 5.3.18-150300.59.101-default
 Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++
 Compiler Build 20220316 for Linux;
 Fortran: Version 2022.1 of Intel Fortran Compiler
 Build 20220316 for Linux;
 Parallel: Yes
 Firmware: Nettrix BIOS Version NNH1041018-U00-1 released
 Nov-2022
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost
 of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	120	51.4	1150	<u>51.4</u>	<u>1150</u>	52.0	1130	120	51.4	1150	<u>51.4</u>	<u>1150</u>	52.0	1130
607.cactuBSSN_s	120	35.3	472	35.7	466	<u>35.6</u>	<u>468</u>	120	35.3	472	35.7	466	<u>35.6</u>	<u>468</u>
619.lbm_s	120	18.4	285	18.4	284	<u>18.4</u>	<u>284</u>	120	18.4	285	18.4	284	<u>18.4</u>	<u>284</u>
621.wrf_s	120	65.6	202	<u>65.1</u>	<u>203</u>	65.0	203	120	65.6	202	<u>65.1</u>	<u>203</u>	65.0	203
627.cam4_s	120	<u>41.8</u>	<u>212</u>	43.4	204	41.4	214	120	<u>41.8</u>	<u>212</u>	43.4	204	41.4	214
628.pop2_s	120	<u>117</u>	<u>101</u>	117	101	116	102	120	<u>117</u>	<u>101</u>	117	101	116	102
638.imagick_s	120	20.1	717	<u>19.9</u>	<u>723</u>	19.8	729	120	20.1	717	<u>19.9</u>	<u>723</u>	19.8	729
644.nab_s	120	<u>21.5</u>	<u>814</u>	21.3	821	21.5	814	120	<u>21.5</u>	<u>814</u>	21.3	821	21.5	814
649.fotonik3d_s	120	57.2	159	<u>57.2</u>	<u>159</u>	57.1	160	120	57.2	159	<u>57.2</u>	<u>159</u>	57.1	160
654.roms_s	120	31.2	504	31.4	502	<u>31.3</u>	<u>503</u>	120	31.2	504	31.4	502	<u>31.3</u>	<u>503</u>

SPECspeed®2017_fp_base = 355

SPECspeed®2017_fp_peak = 355

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/lijq/lib/intel64:/home/lijq/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:

Enable LP [Global] set to Single LP
LLC Prefetch set to Enabled
SNC (Sub NUMA) set to Disabled
Patrol Scrub set to Disabled
LLC dead line alloc set to Disabled
XPT Prefetch set to Enabled
KTI Prefetch set to Disabled
DCU Streamer Prefetcher set to Disabled
Hardware P-States set to Native Mode

Sysinfo program /home/lijq/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Mon Dec 19 10:43:04 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8490H
 2 "physical id"s (chips)
120 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 60
siblings  : 60
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59
```

From lscpu from util-linux 2.36.2:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 52 bits physical, 57 bits virtual
CPU(s): 120
On-line CPU(s) list: 0-119
Thread(s) per core: 1
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```

Core(s) per socket:          60
Socket(s):                   2
NUMA node(s):               2
Vendor ID:                   GenuineIntel
CPU family:                  6
Model:                       143
Model name:                  Intel(R) Xeon(R) Platinum 8490H
Stepping:                    8
CPU MHz:                     2897.864
CPU max MHz:                 3500.0000
CPU min MHz:                 800.0000
BogoMIPS:                    3800.00
Virtualization:              VT-x
L1d cache:                   5.6 MiB
L1i cache:                   3.8 MiB
L2 cache:                    240 MiB
L3 cache:                    225 MiB
NUMA node0 CPU(s):          0-59
NUMA node1 CPU(s):          60-119
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:    Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:    Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBR SB-eIBRS SW sequence
Vulnerability Srbds:         Not affected
Vulnerability Tsx async abort: Not affected
Flags:                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid cldemote

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```
movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig avx512_fp16
flush_lld arch_capabilities
```

From `lscpu --cache:`

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	5.6M	12	Data	1	64	1	64
L1i	32K	3.8M	8	Instruction	1	64	1	64
L2	2M	240M	16	Unified	2	2048	1	64
L3	112.5M	225M	15	Unified	3	122880	1	64

```
/proc/cpuinfo cache data
cache size : 115200 KB
```

From `numactl --hardware`

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

```
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56
57 58 59
```

node 0 size: 515602 MB

node 0 free: 513932 MB

```
node 1 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84
85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109
110 111 112 113 114 115 116 117 118 119
```

node 1 size: 515783 MB

node 1 free: 514064 MB

node distances:

```
node 0 1
0: 10 21
1: 21 10
```

From `/proc/meminfo`

MemTotal: 1056138672 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

From `/etc/*release* /etc/*version*`

os-release:

NAME="SLES"

VERSION="15-SP3"

VERSION_ID="15.3"

PRETTY_NAME="SUSE Linux Enterprise Server 15 SP3"

ID="sles"

ID_LIKE="suse"

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Platform Notes (Continued)

```
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:15:sp3"
```

```
uname -a:  
Linux localhost 5.3.18-150300.59.101-default #1 SMP Tue Nov 1 11:32:03 UTC 2022  
(b2a976e) x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
mmio_stale_data:	Not affected
retbleed:	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling, PBRSE-eIBRS: SW sequence
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 3 Dec 19 10:07

```
SPEC is set to: /home/lijq  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/nvme0n1p3 xfs   854G  137G  718G  16% /home
```

```
From /sys/devices/virtual/dmi/id  
Vendor:          Nettrix  
Product:         R620 G50 LP  
Product Family: Rack  
Serial:          6101823603509646
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M321R8GA0BB0-CQKEG 64 GB 2 rank 4800

BIOS:

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Platform Notes (Continued)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: NNH1041018-U00-1
BIOS Date: 11/01/2022
BIOS Revision: 5.29

(End of data from sysinfo program)

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
644.nab_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
654.roms_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
628.pop2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Compiler Version Notes (Continued)

2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

```
icx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```

Peak Portability Flags

Same as Base Portability Flags



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Nettrix

SPECspeed®2017_fp_base = 355

R620 G50 LP (Intel Xeon Platinum 8490H, 1.90 GHz)

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6138
Test Sponsor: Nettrix
Tested by: Nettrix

Test Date: Dec-2022
Hardware Availability: Jan-2023
Software Availability: Nov-2022

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.html>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.xml>

http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-12-18 21:43:04-0500.
Report generated on 2023-01-10 19:01:47 by CPU2017 PDF formatter v6442.
Originally published on 2023-01-10.